

(12) United States Design Patent (10) Patent No.: US D548,740 S **** Aug. 14, 2007** Takiar et al. (45) **Date of Patent:**

SIP BASED FLASH MEMORY CARD (54)

- (75)Inventors: **Hem Takiar**, Fremont, CA (US); Robert C. Miller, San Jose, CA (US); Warren Middlekauff, San Jose, CA (US); Michael W. Patterson, Portland, OR (US); Shrikar Bhagath, San Jose, CA (US)
- Assignee: SanDisk Corporation, Milpitas, CA (73)

D534,169 S	*	12/2006	Nakamura et al	D14/435
D537,081 S	*	2/2007	Takiar et al	D14/436
D538,286 S	*	3/2007	Takiar et al	D14/436

* cited by examiner

```
Primary Examiner—M. H. Tung
(74) Attorney, Agent, or Firm-Vierra Magen Marcus &
DeNiro LLP
```

(US)

14 Years (**)Term:

Appl. No.: 29/230,382 (21)

May 19, 2005 (22)Filed:

- LOC (8) Cl. 14-02 (51)
- (52)
- Field of Classification Search D14/435–8, (58)D14/478-80; 235/441-3, 487-95; 257/678-9;174/250; 40/124.01; 361/736-7, 728-30 See application file for complete search history.
- (56)**References Cited**

U.S. PATENT DOCUMENTS

D267,009 S	*	11/1982	Doi D14/436
D452,690 S	*	1/2002	Wallace et al D14/436
D452,864 S	*	1/2002	Wallace et al D14/436
D453,338 S	*	2/2002	Pentz et al D14/436
D453,934 S	*	2/2002	Wallace et al D14/436
D492,314 S	*	6/2004	Huang et al D14/436
D492,688 S	*	7/2004	Wallace et al D14/437
D504,433 S	*	4/2005	Nishizawa et al D14/436
D517,559 S	*	3/2006	Yu et al D14/436
D518,483 S	*	4/2006	Yu et al D14/436
D525,978 S	*	8/2006	Takiar et al D14/436
D529,030 S	*	9/2006	Nakamura et al D14/435
D529,031 S	*	9/2006	Huang et al D14/436

The ornamental design for an SIP based flash memory card, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of an SIP based flash memory card according to embodiments of the present invention. FIG. 2 is a bottom view of an SIP based flash memory card according to embodiments of the present invention. FIG. 3 is a view of a first end of an SIP based flash memory card according to embodiments of the present invention. FIG. 4 is a view of a first side of an SIP based flash memory card according to embodiments of the present invention. FIG. 5 is a view of a second side of an SIP based flash

memory card according to embodiments of the present invention.

FIG. 6 is a view of a second end of an SIP based flash memory card according to embodiments of the present invention; and,

FIG. 7 is a top view of an SIP based flash memory card according to embodiments of the present invention.

The broken lines on the drawings are for illustrative purposes only and form no part of the claimed ornamental design.

1 Claim, 1 Drawing Sheet



U.S. Patent

•

Aug. 14, 2007

US D548,740 S



FIG. 3



FIG. 4 FIG. 2 FIG. 5

FIG. 6





