

US00D548203S

(12) **United States Design Patent** (10) **Patent No.:** **US D548,203 S**  
**Takahashi** (45) **Date of Patent:** **\*\* Aug. 7, 2007**

(54) **SEMICONDUCTOR**

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(\*\*) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (8) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/182**

(58) **Field of Classification Search** ..... D13/133,  
D13/146–147, 149, 154, 182, 184; 174/250,  
174/253; 257/666, 694, 696, 820; 381/760  
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a semiconductor, as shown and described.

**DESCRIPTION**

FIG. 1 is a bottom, front and left side perspective view of a semiconductor showing my new design, FIG. 2 is a front elevational view thereof, FIG. 3 is a rear elevational view thereof, FIG. 4 is a right side elevational view thereof, FIG. 5 is a right side elevational view thereof, FIG. 6 is a top plan view thereof; and, FIG. 7 is a bottom plan view thereof.

**1 Claim, 4 Drawing Sheets**

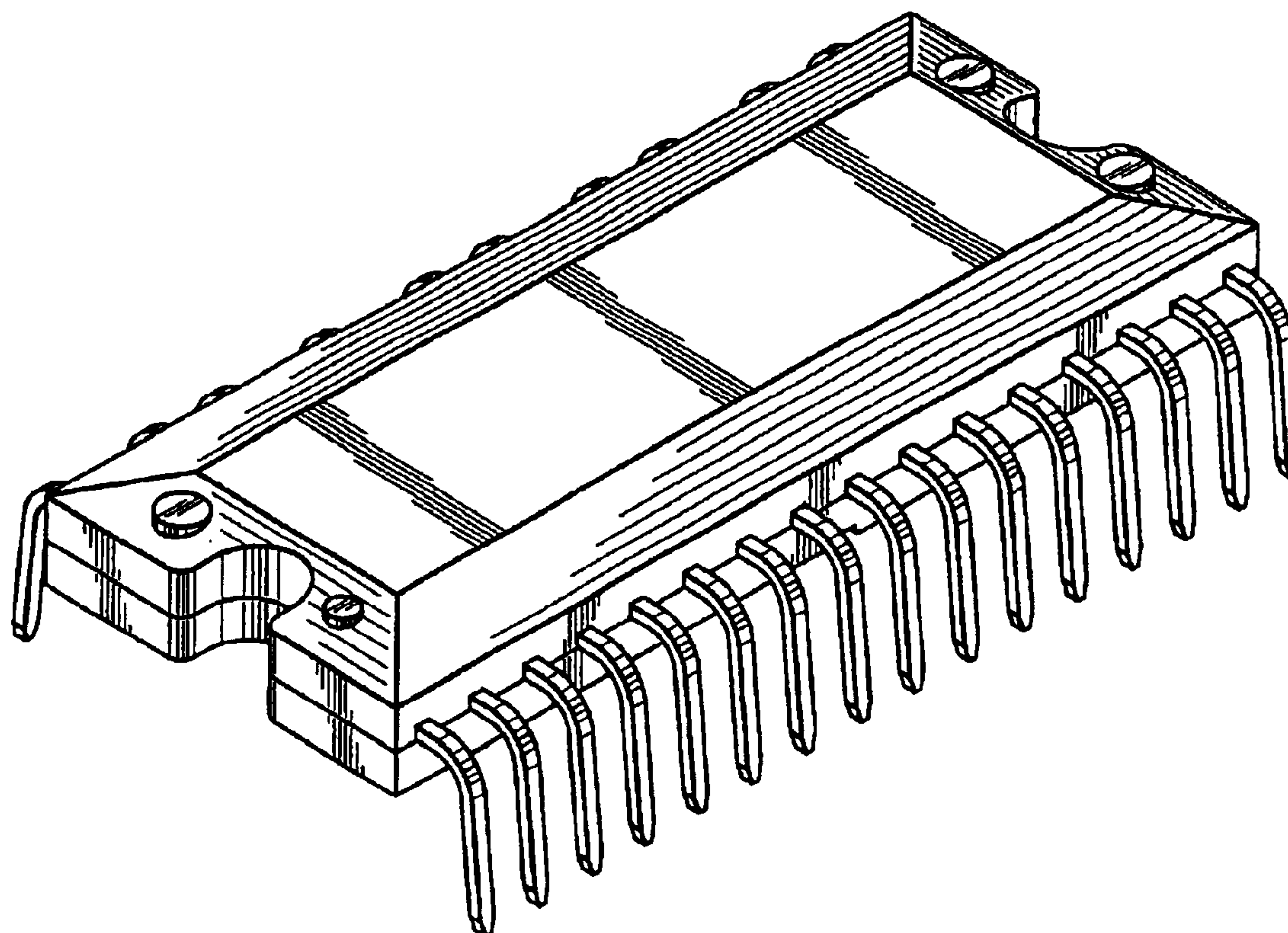


FIG. 1

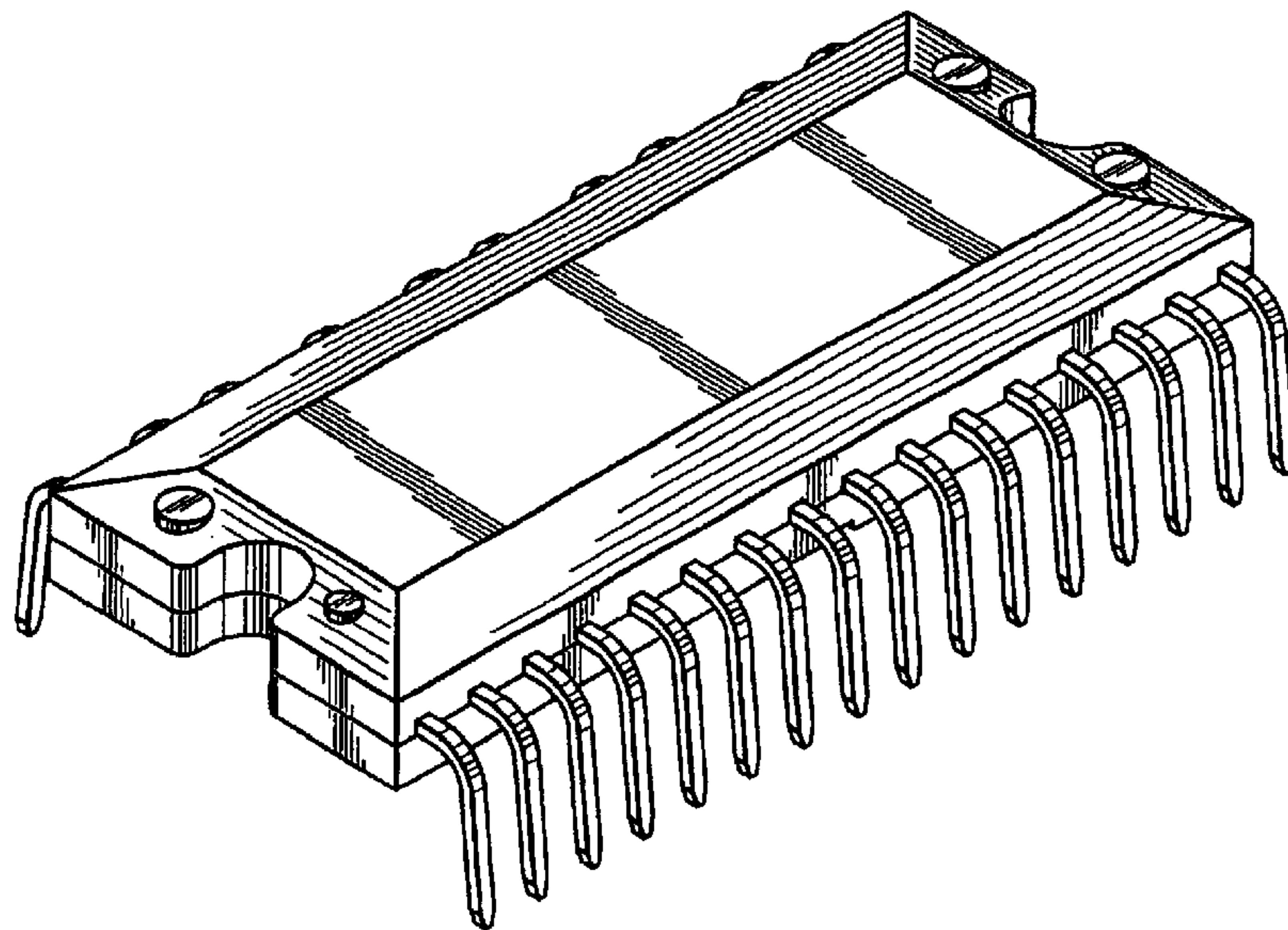


FIG. 2

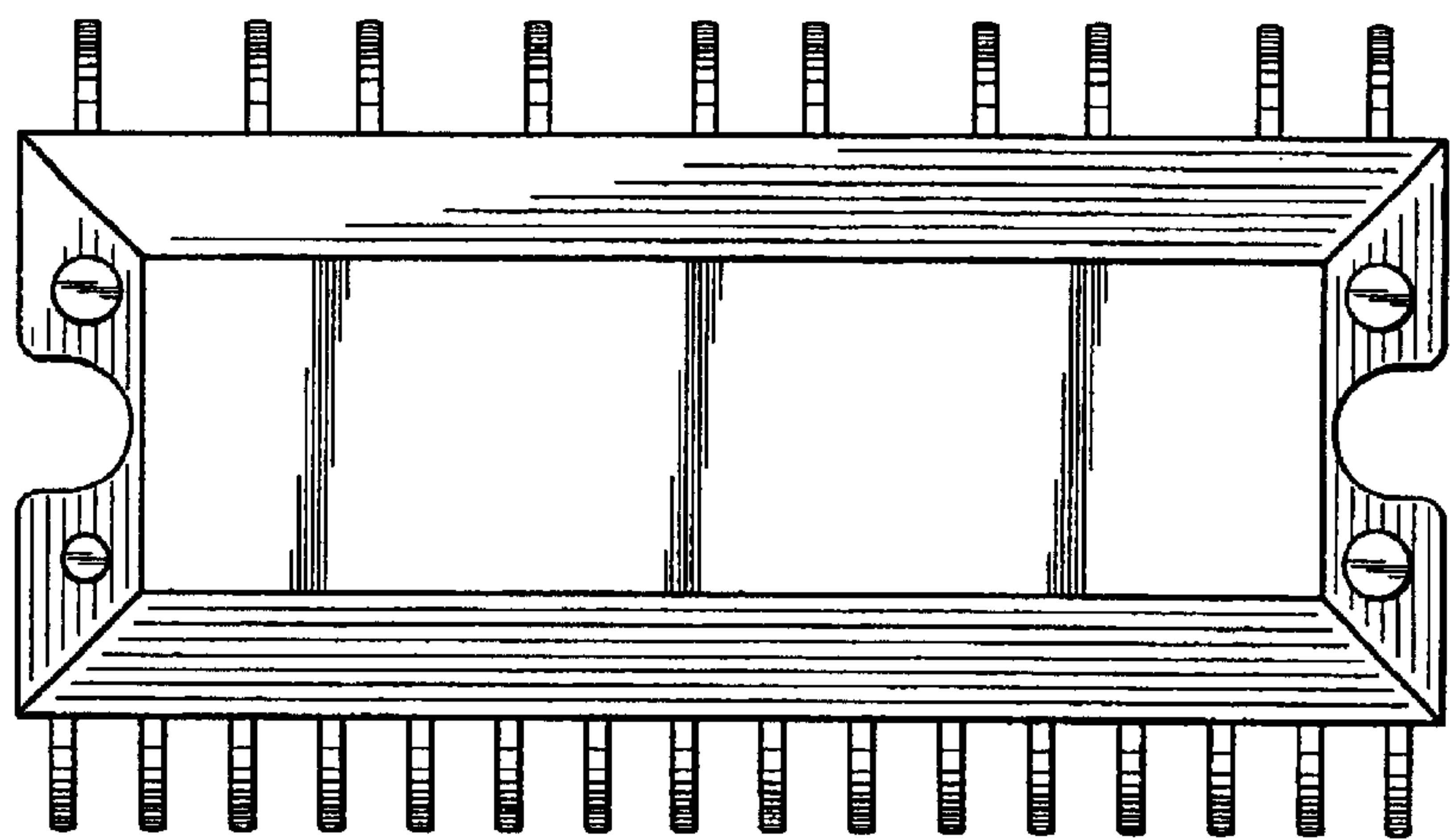


FIG. 3

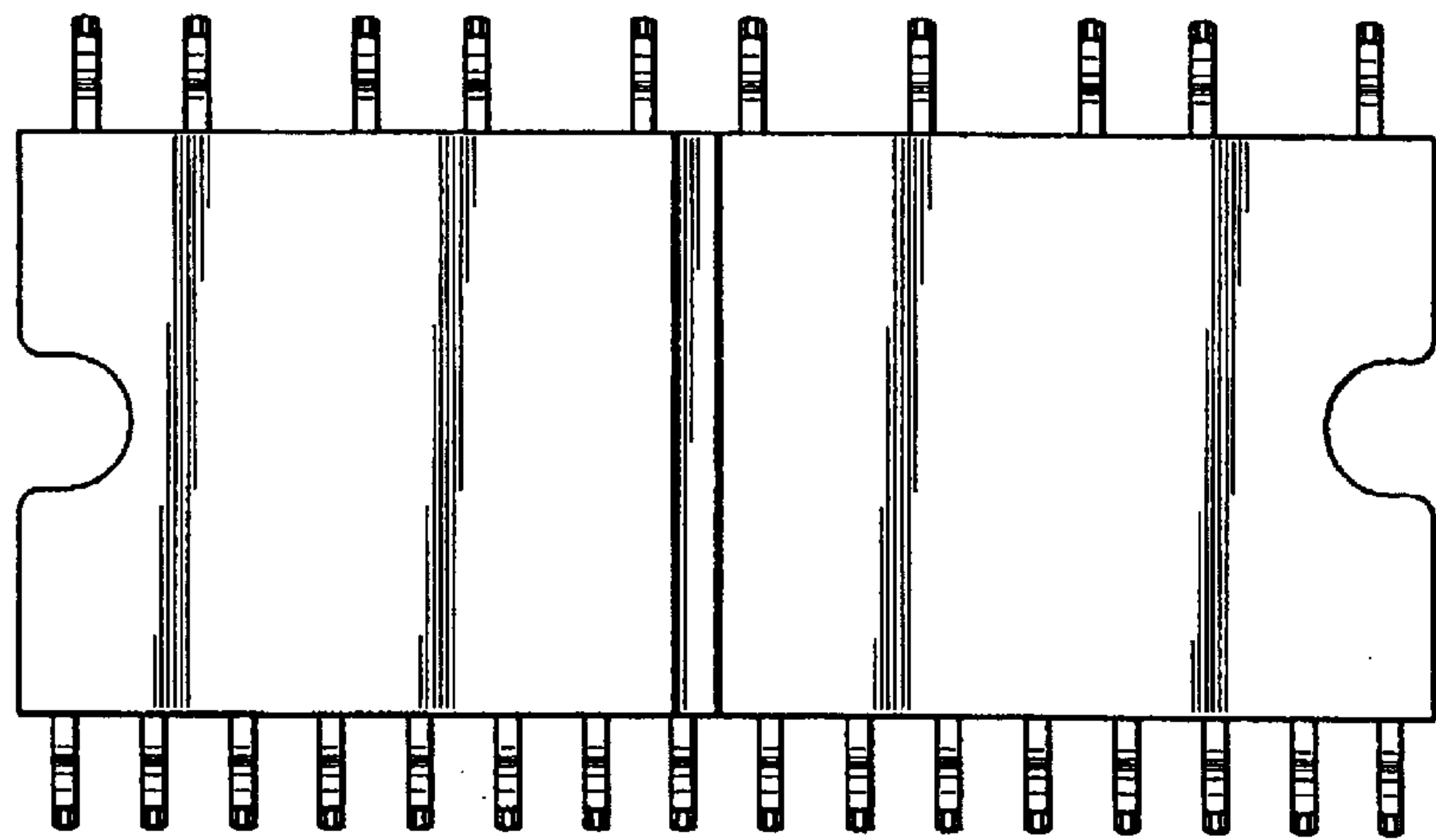


FIG. 4

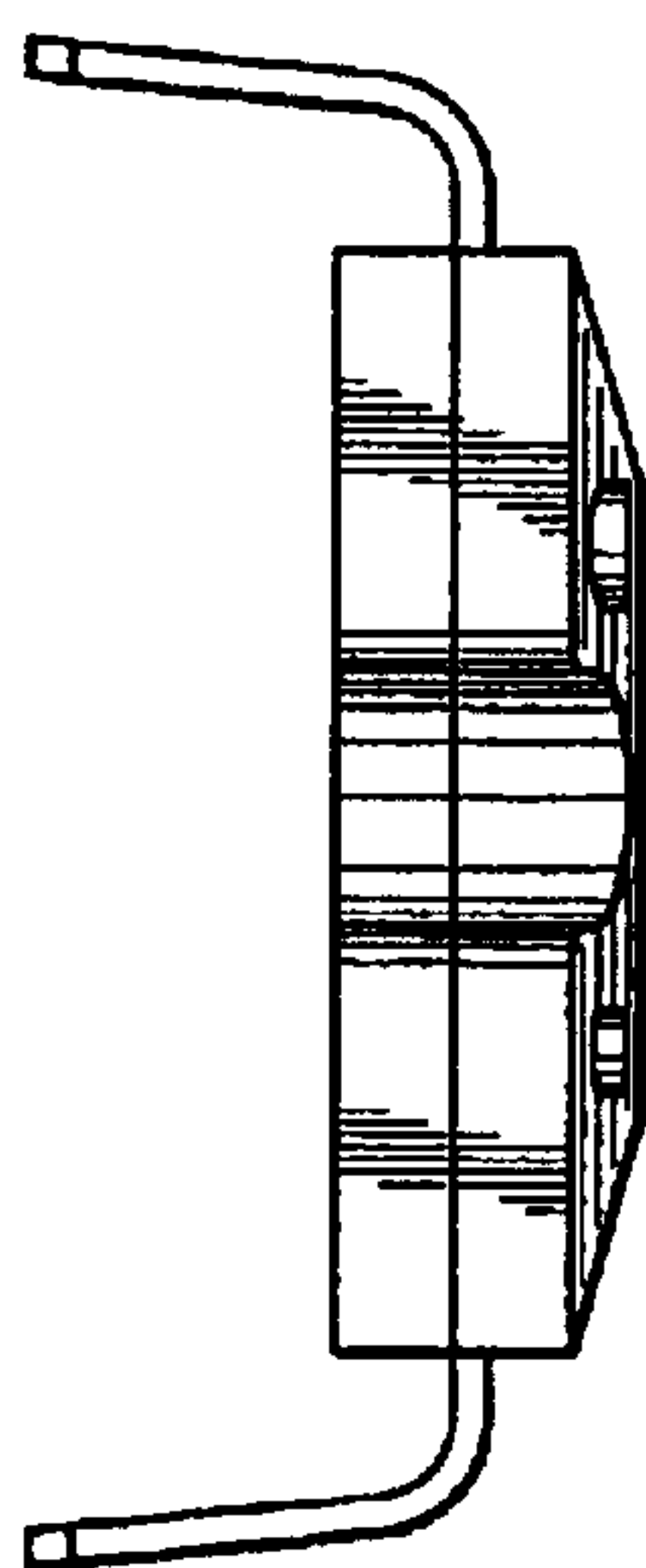


FIG. 5

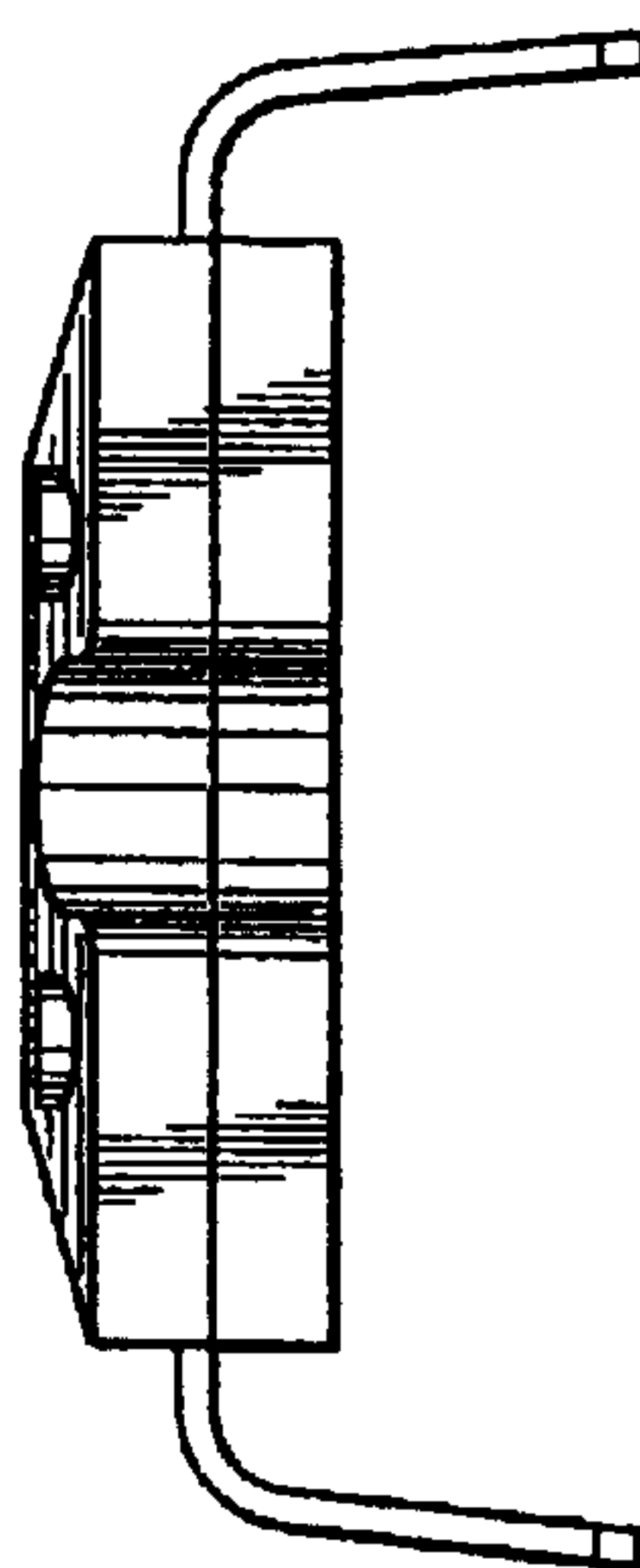


FIG. 6

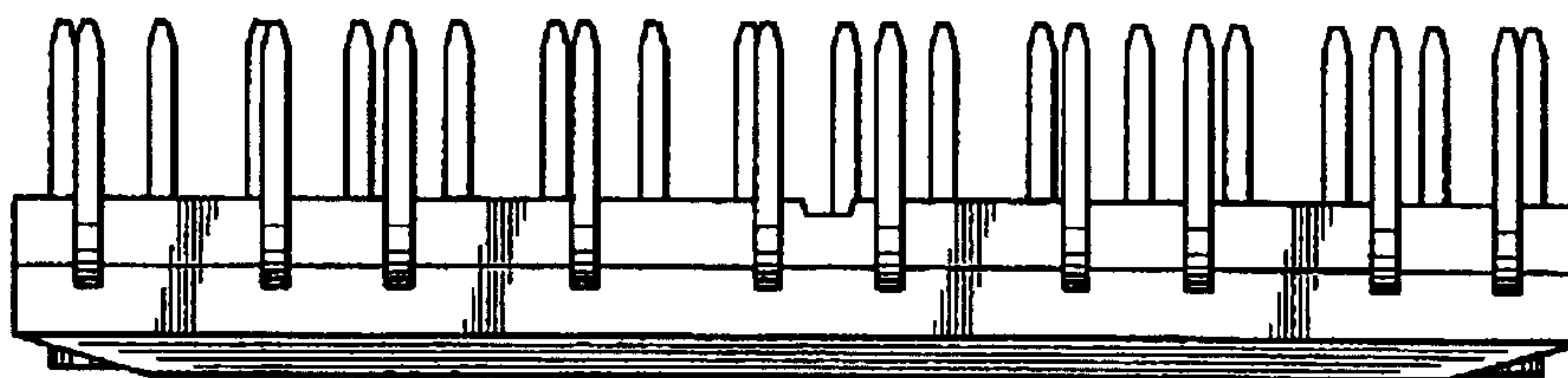


FIG. 7

