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(12) **United States Design Patent**
Kihara et al.

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(45) **Date of Patent:** **** Jul. 10, 2007**

(54) **SEMICONDUCTOR MANUFACTURING APPARATUS**

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(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

Jul. 15, 2005 (JP) 2005-020680

(51) **LOC (8) Cl.** **15-99**

(52) **U.S. Cl.** **D15/199**

(58) **Field of Classification Search** D10/46,
D10/75; D15/199; 29/25.01, 33 R, 722,
29/855; 55/385.2; 156/643, 646, 653, 656;
228/4.5, 175; 257/288, 690, 730, 786; 414/217,
414/225, 935-941; 437/189, 196, 203, 204,
437/205, 209, 902; 454/182, 851
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a semiconductor manufacturing apparatus, as shown.

DESCRIPTION

FIG. 1 is a front, top and left side perspective view of semiconductor manufacturing apparatus showing our new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a right side elevational view thereof;

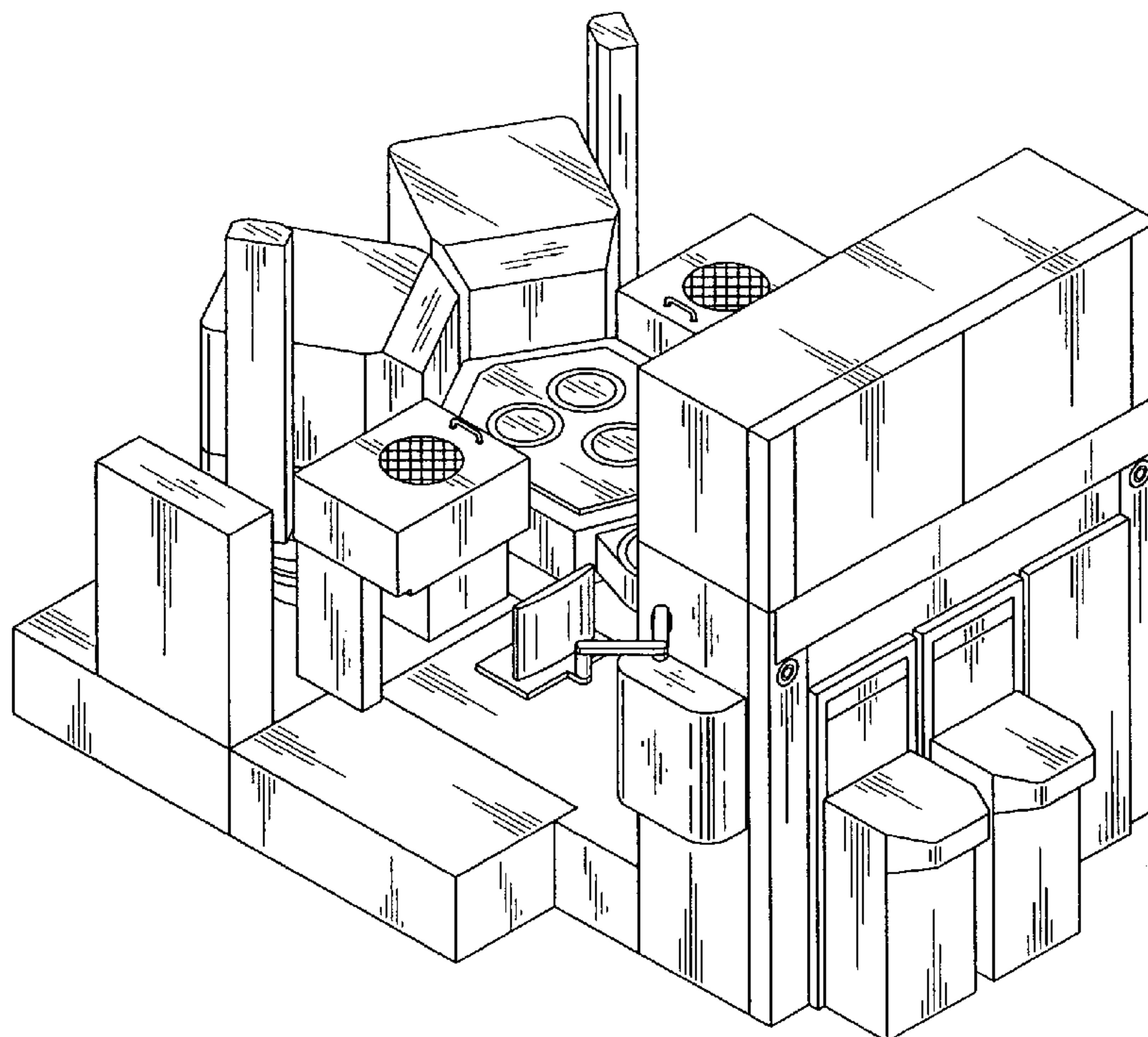
FIG. 4 is a left side elevational view thereof;

FIG. 5 is a top plan elevational view thereof;

FIG. 6 is a bottom plan elevational view thereof; and,

FIG. 7 is a rear elevational view thereof.

1 Claim, 7 Drawing Sheets



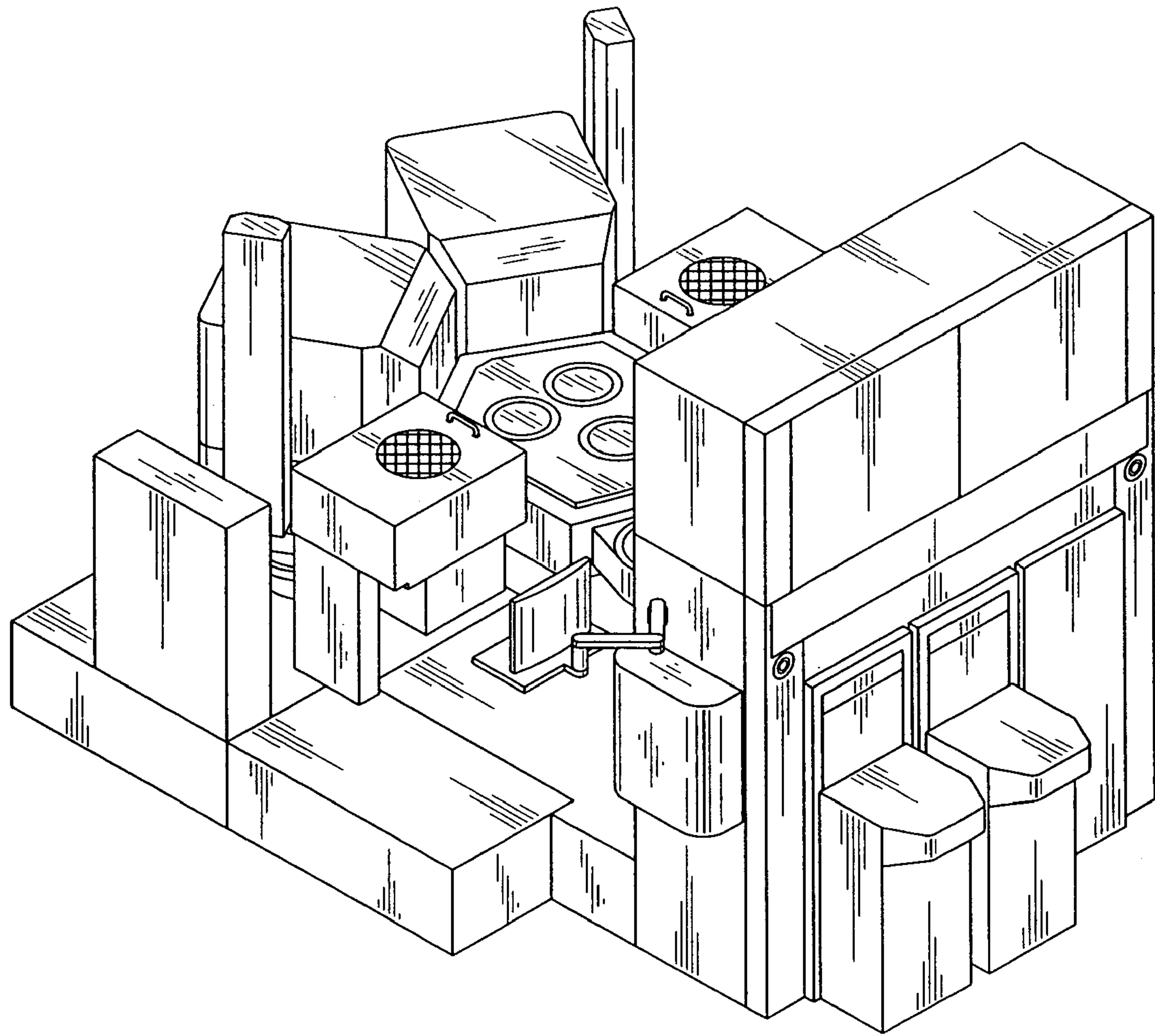


FIG. 1



FIG. 2

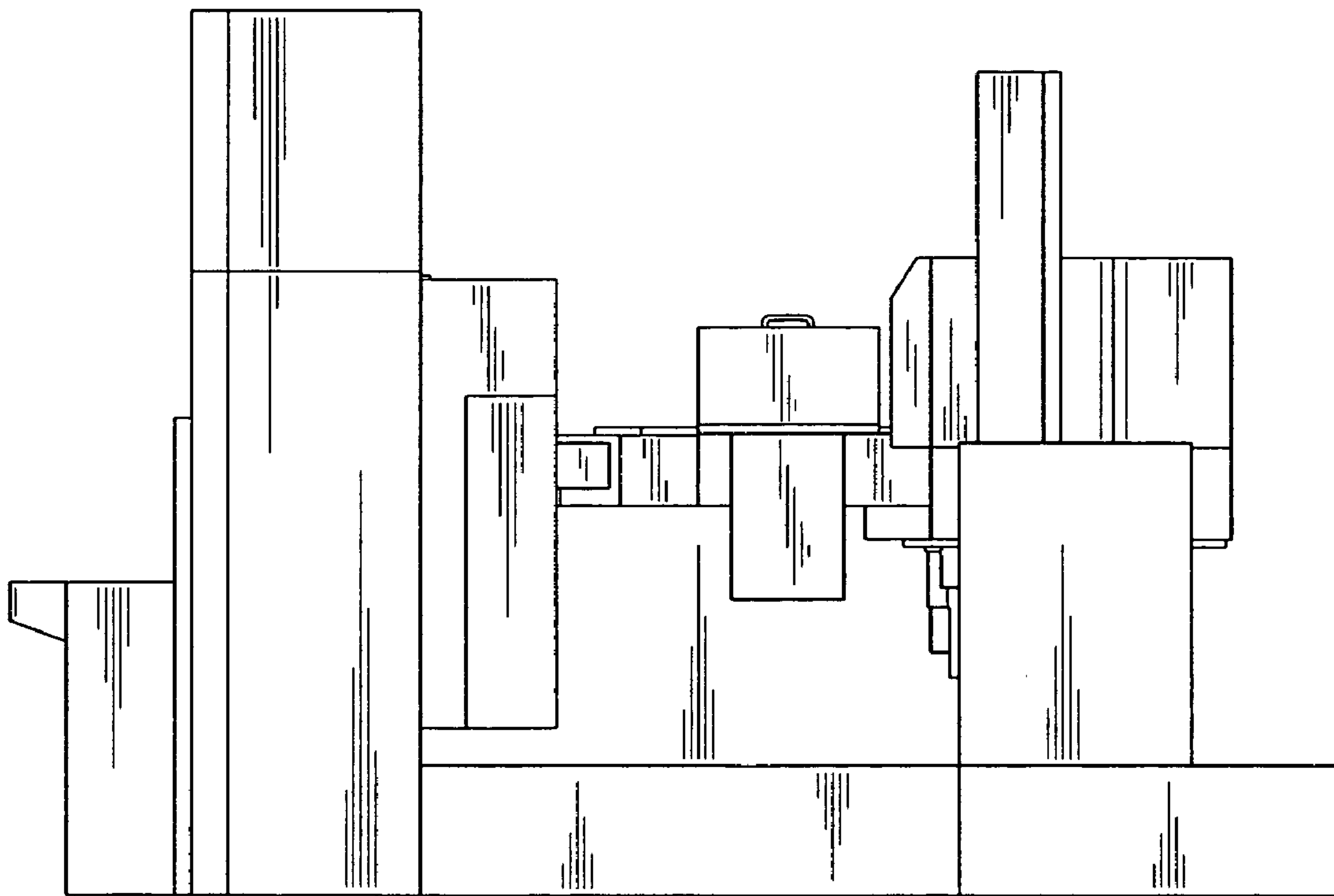


FIG. 3

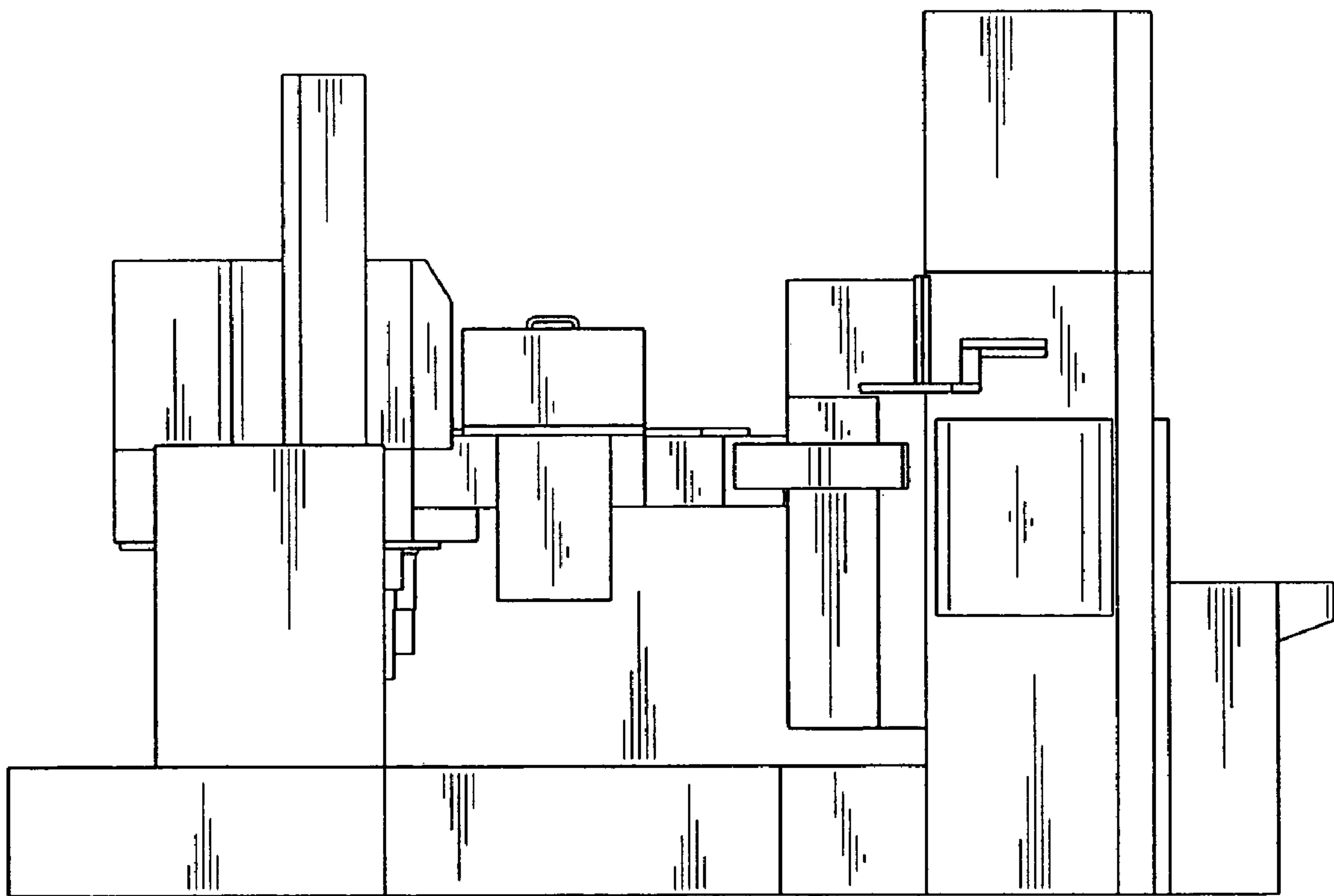


FIG. 4

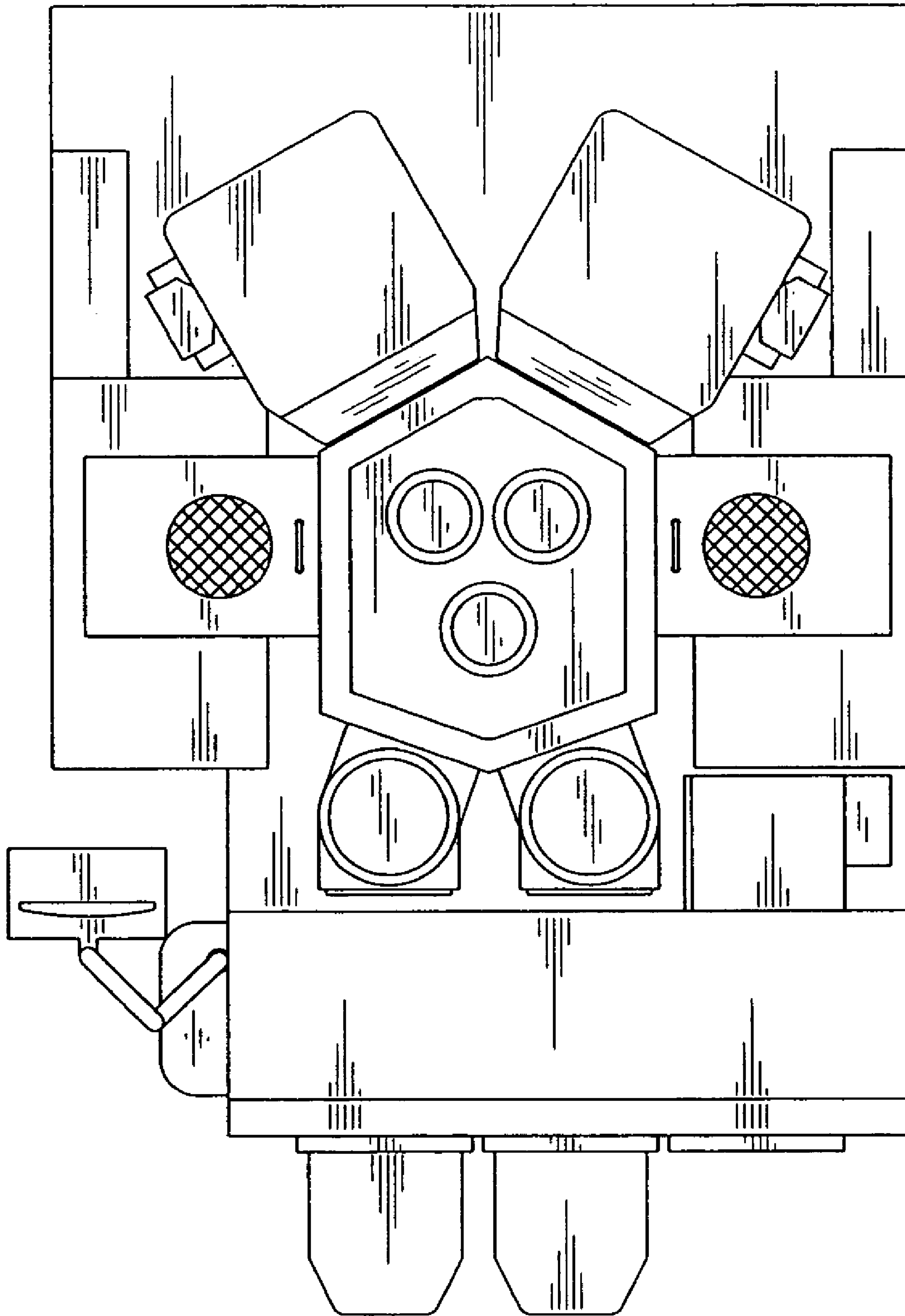


FIG. 5

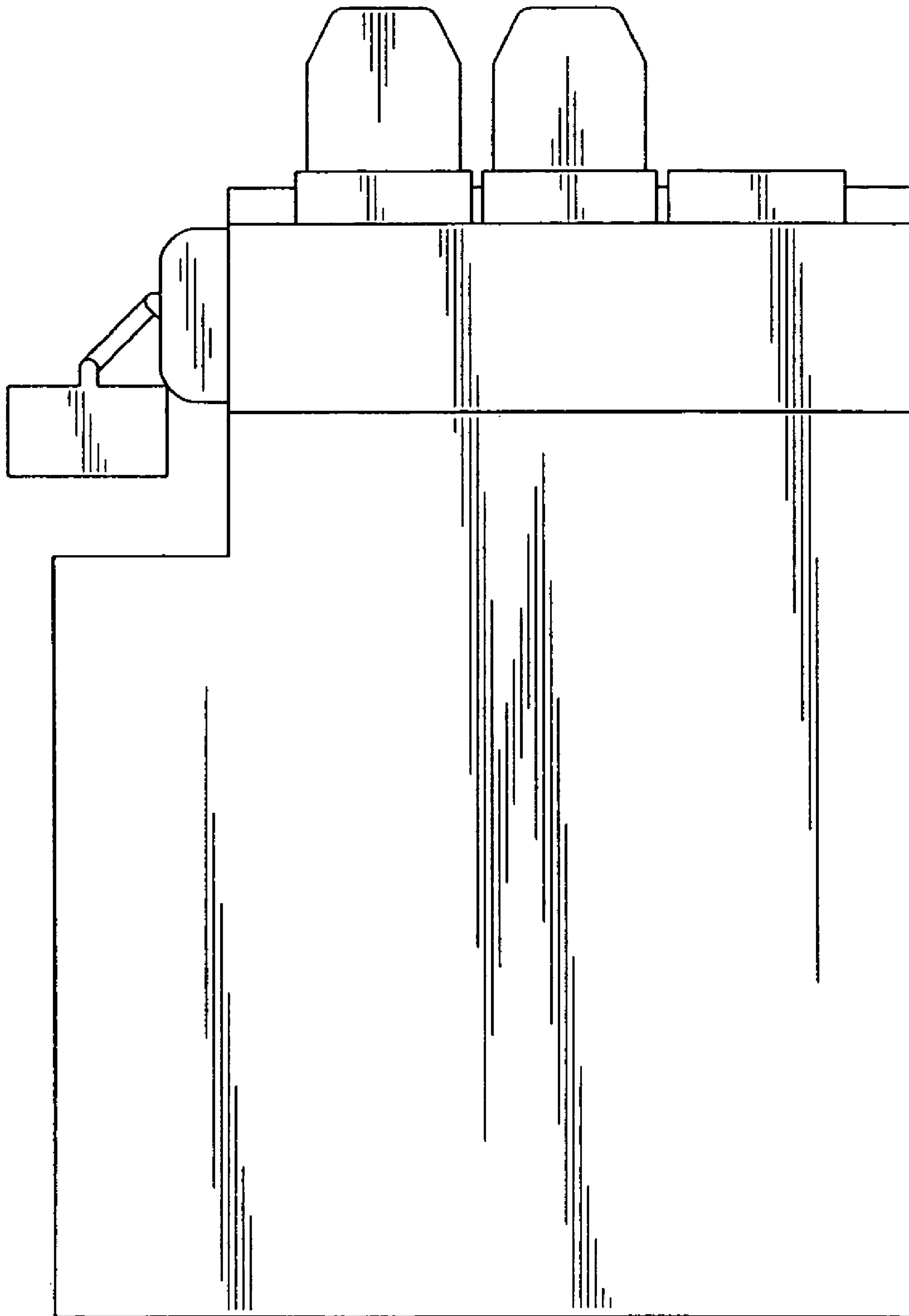


FIG. 6

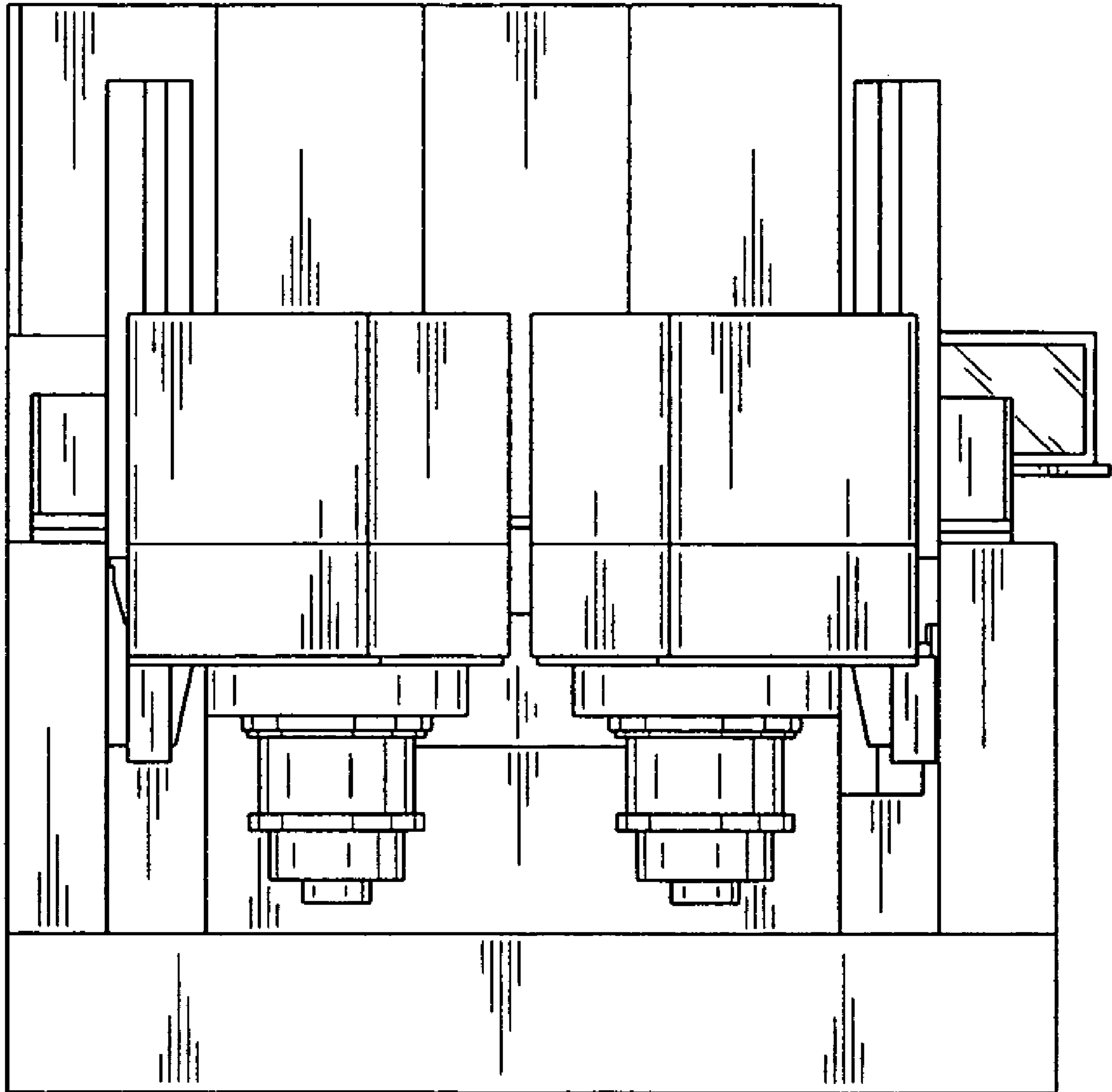


FIG. 7