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Romero et al.

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(54) **COPYING-PREVENTION SOFTWARE CHIP FOR EMBEDDING IN COMPACT DISC**

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(**) Term: **14 Years**

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(51) **LOC (8) Cl.** **14-02**

(52) **U.S. Cl.** **D14/435**

(58) **Field of Classification Search** .. 369/275.1-275.5, 369/44.26, 284, 286, 100, 52.1, 272, 273, 369/280, 281-288; D14/435-37, 478-80, D14/485-95, 474; 283/81; 720/718; 435/6, 435/287.2; 604/891.1; 216/2; D11/80
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

4,446,519 A	5/1984	Thomas	
4,458,315 A	7/1984	Uchenick	
4,740,890 A	4/1988	William	
D305,887 S *	2/1990	Nishimura	D14/436
4,903,255 A	2/1990	Sugaya et al.	
D313,229 S *	12/1990	Audebert et al.	D14/436
D328,599 S *	8/1992	Gloton	D14/437
D335,663 S *	5/1993	Gloton	D14/437
D353,136 S *	12/1994	Gloton	D14/437
5,465,243 A	11/1995	Boardman et al.	
5,862,117 A *	1/1999	Fuentes et al.	369/100
D406,822 S *	3/1999	Huber et al.	D13/182

D412,164 S *	7/1999	Laviron et al.	D14/437
D432,939 S *	10/2000	Hooglander	D11/80
D443,298 S *	6/2001	Webb et al.	D19/10
D456,414 S *	4/2002	Turin	D14/437
6,532,206 B2	3/2003	Maramatsu et al.	
6,808,522 B2 *	10/2004	Richards et al.	604/890.1
2002/0150933 A1 *	10/2002	Ehricht et al.	435/6

* cited by examiner

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(57) **CLAIM**

We claim the ornamental design for a copying-prevention software chip for embedding in compact disc, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a copying-prevention software chip for embedding in compact disc showing our new design;

FIG. 2 is a top plan view thereof;

FIG. 3 is a bottom plan view thereof;

FIG. 4 is a side elevational view thereof, the other side elevational views being identical thereto; and,

FIG. 5 is a reduced top plan view showing the copying-prevention software chip for embedding in compact disc in use; the broken lines depict environmental structure for illustrative purposes only and form no part of the claimed design.

The diagonal lines on the chip, as depicted in FIGS. 1-5, are used to denote a reflective surface.

1 Claim, 1 Drawing Sheet

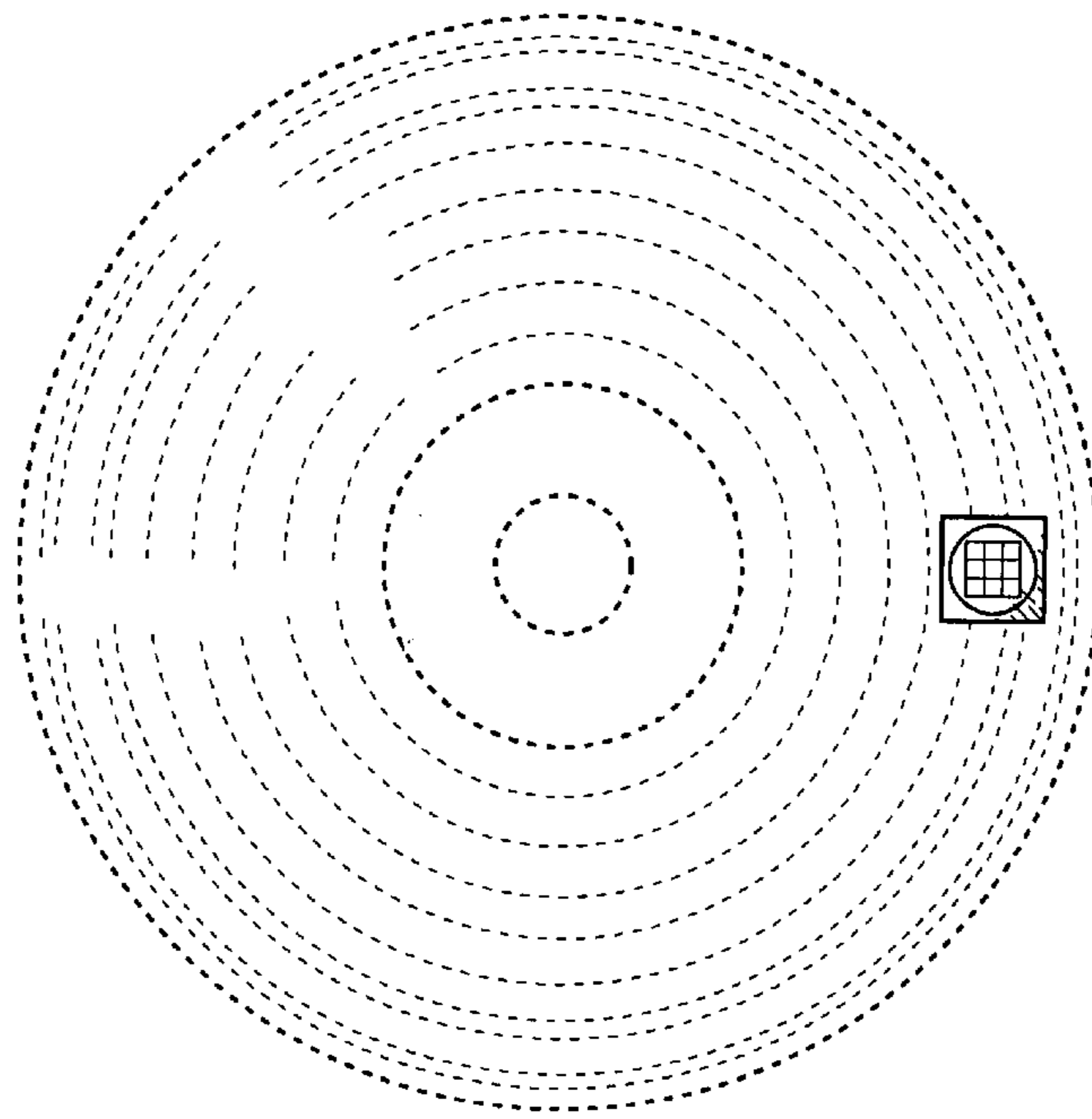


Fig. 1

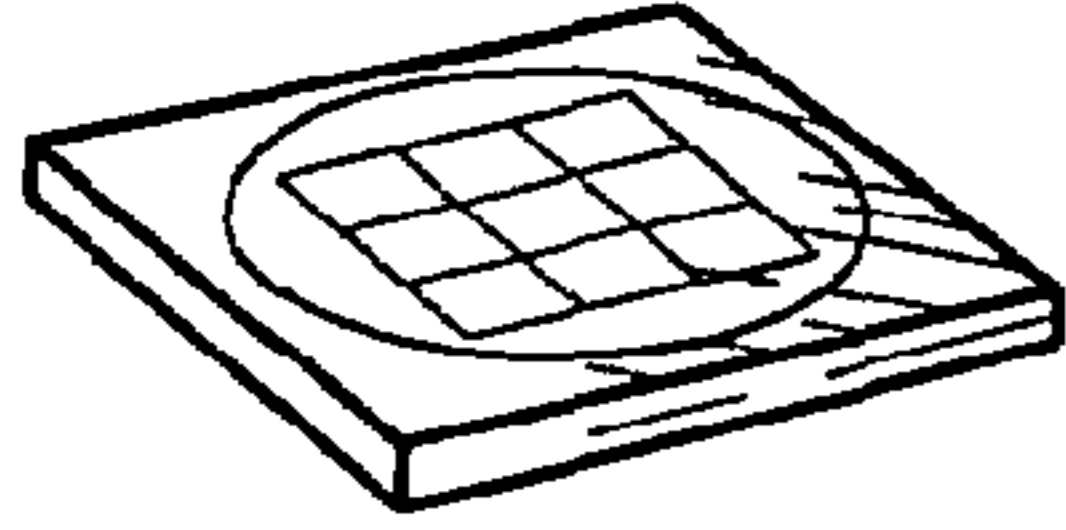


Fig. 2

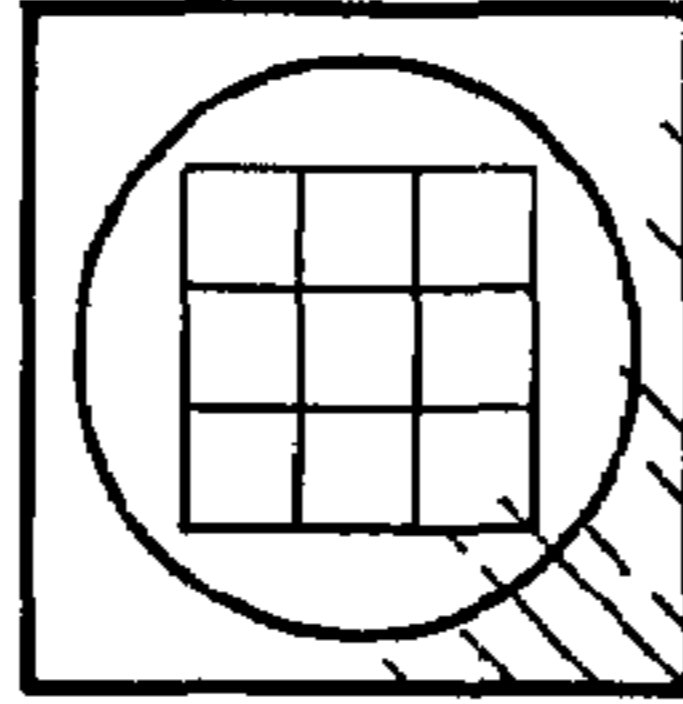


Fig. 3

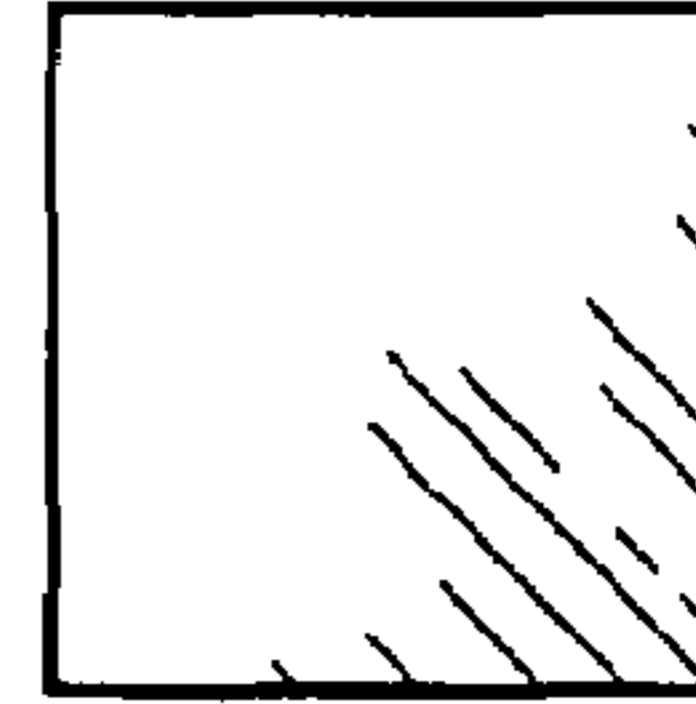


Fig. 4

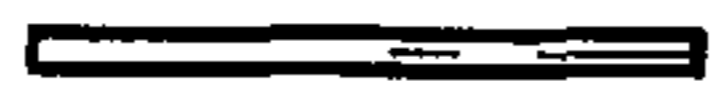


Fig. 5

