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(12) **United States Design Patent** (10) **Patent No.:** **US D541,752 S**
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(54) **SLOT PATTERN FOR EMI/RFI SHIELD**

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(**) Term: **14 Years**

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Related U.S. Application Data

(63) Continuation of application No. 29/235,051, filed on Jul. 27, 2005, now Pat. No. Des. 519,931, and a continuation of application No. 11/175,075, filed on Jul. 5, 2005, now Pat. No. 7,112,740, and a continuation of application No. 11/056,428, filed on Feb. 11, 2005, now Pat. No. 7,078,614.

(51) **LOC (8) Cl.** **13-03**

(52) **U.S. Cl.** **D13/153**

(58) **Field of Classification Search** D13/152-157; D8/377; D25/119, 121-125; 361/800, 818, 361/816; 714/35, 51; 439/92, 95, 607, 939; 174/17, 35, 58, 350, 377
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a slot pattern for EMI/RFI shield, as shown and described.

DESCRIPTION

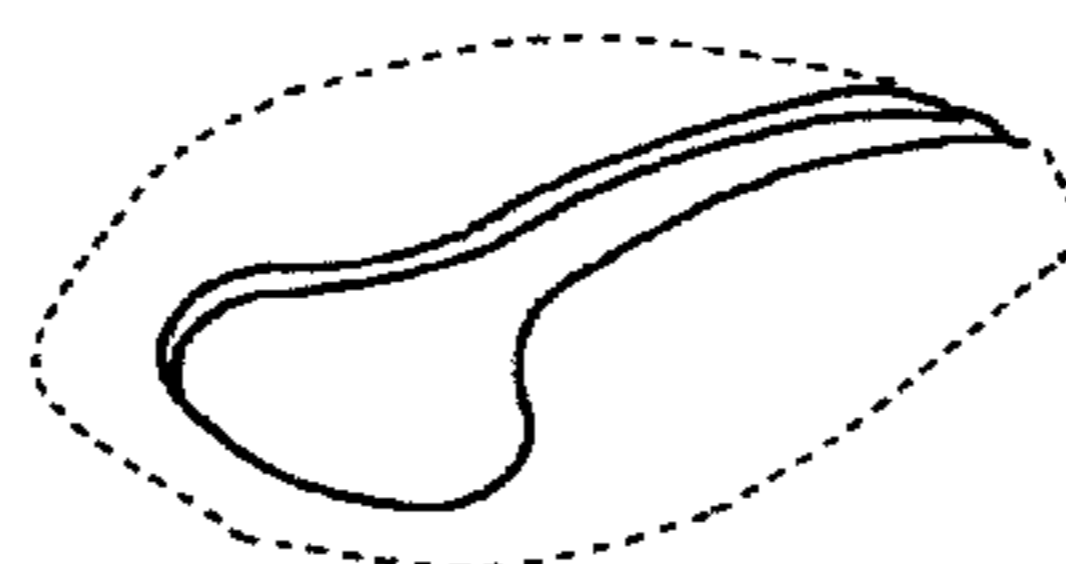
FIG. 1 is a perspective view of the slot pattern for EMI/RFI shield showing my new design disposed, by way of example only, across a generally curved surface portion of an exemplary EMI/RFI shield;

FIG. 2 is a top plan view of the slot pattern for EMI/RFI shield shown in FIG. 1; and,

FIG. 3 is a bottom plan view of the slot pattern for EMI/RFI shield shown in FIG. 1.

The details shown in broken lines are for illustrative purposes only and form no part of the claimed design.

1 Claim, 1 Drawing Sheet



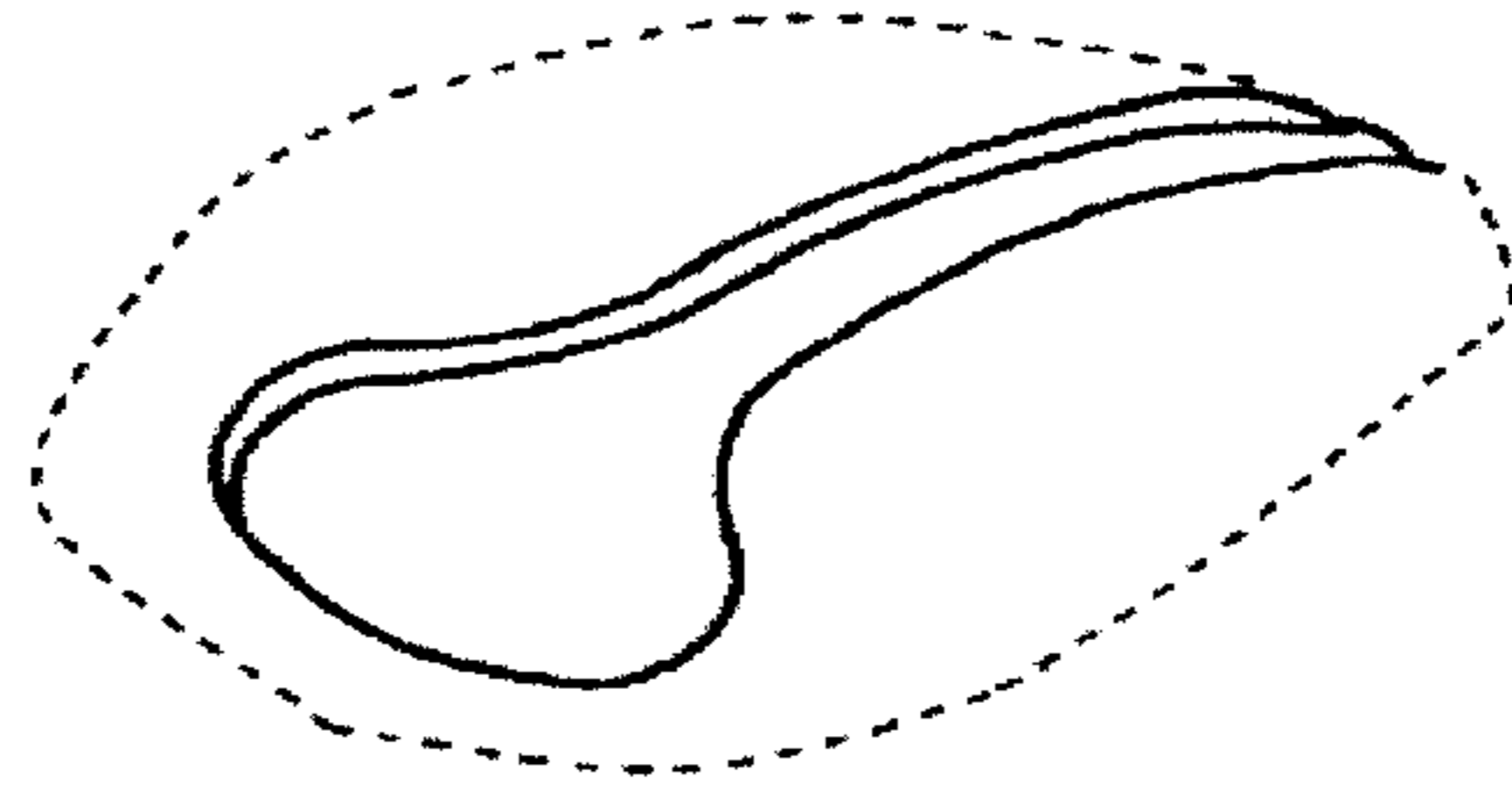


FIG. 1

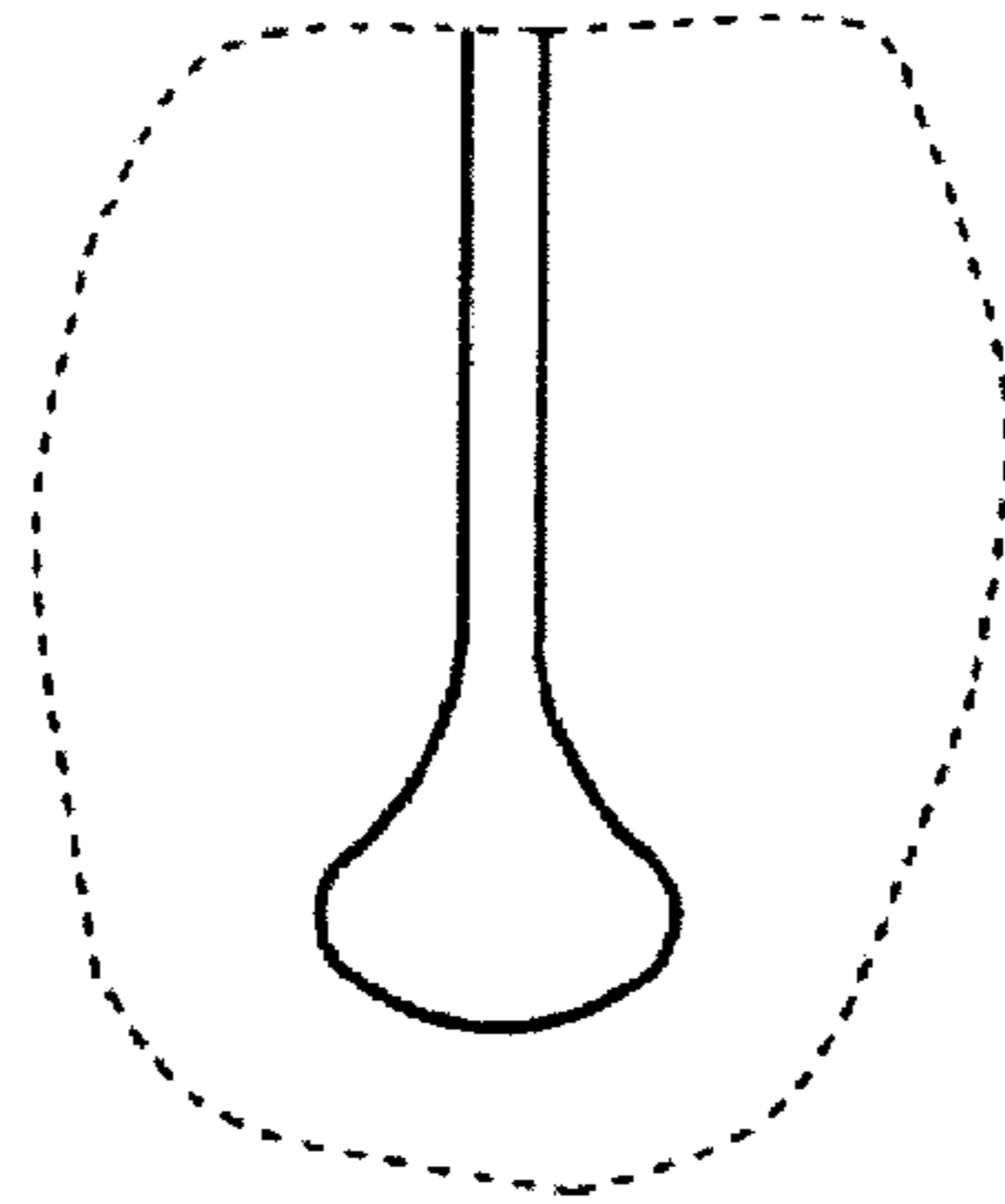


FIG. 2

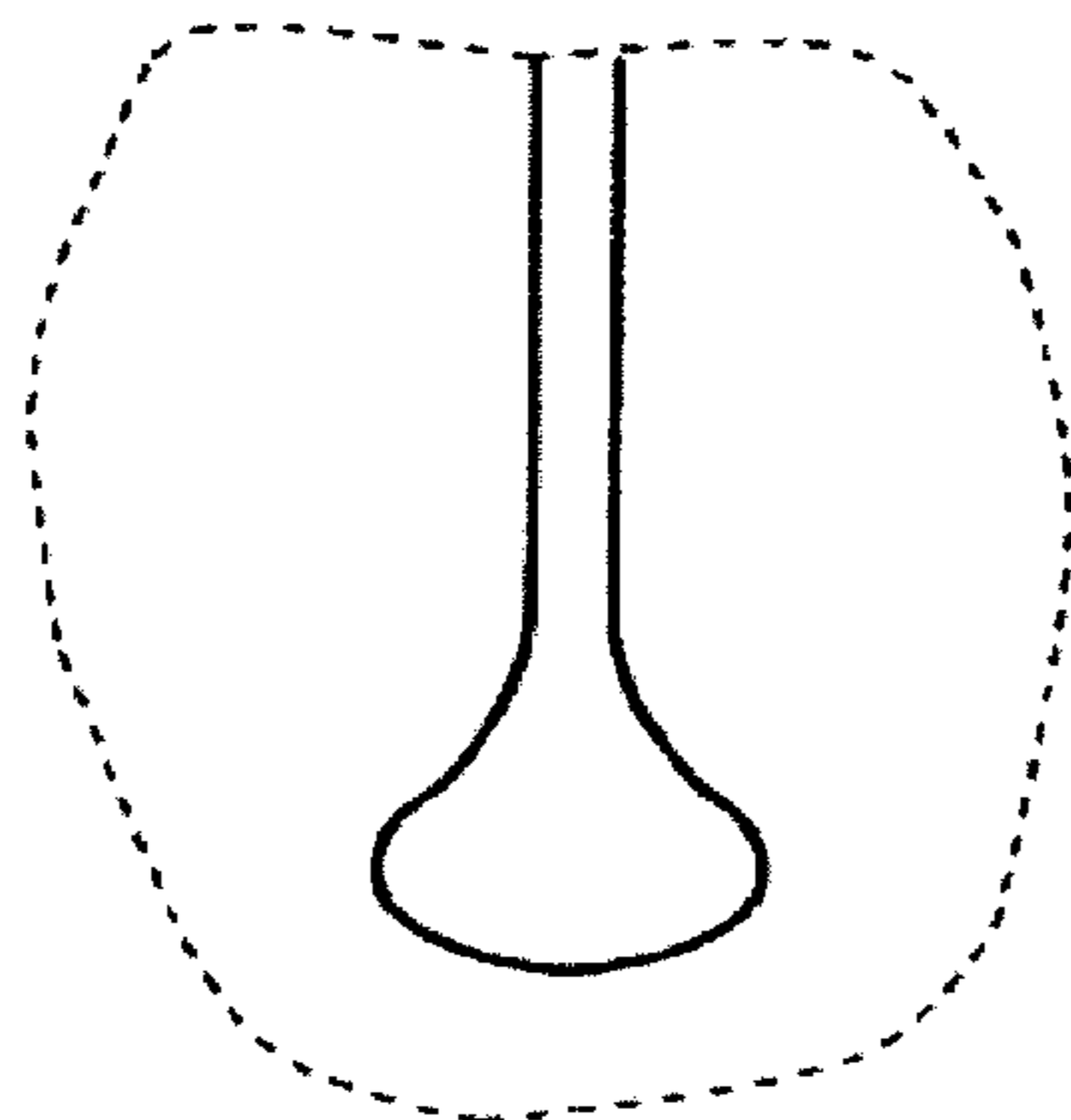


FIG. 3