



US00D541285S

(12) **United States Design Patent**  
Wang et al.(10) **Patent No.:** US D541,285 S  
(45) **Date of Patent:** \*\* Apr. 24, 2007(54) **COMPUTER PROCESSOR MODULE  
HAVING A FRONT RELEASE MECHANISM**(75) Inventors: **Qiang Wang**, Cypress, TX (US); **Minh H. Nguyen**, Katy, TX (US); **David A. Selvidge**, Cypress, TX (US)(73) Assignee: **Hewlett-Packard Development Company, L.P.**, Houston, TX (US)(\*\*) Term: **14 Years**(21) Appl. No.: **29/235,642**(22) Filed: **Aug. 3, 2005**(51) LOC (8) Cl. .... **14-02**(52) U.S. Cl. .... **D14/445**(58) Field of Classification Search ..... D14/441–446,  
D14/363–368, 439; D13/182, 184, 199; 312/223.1–223.3;  
361/680–686, 690–696; 369/34.01, 36.01  
See application file for complete search history.(56) **References Cited**

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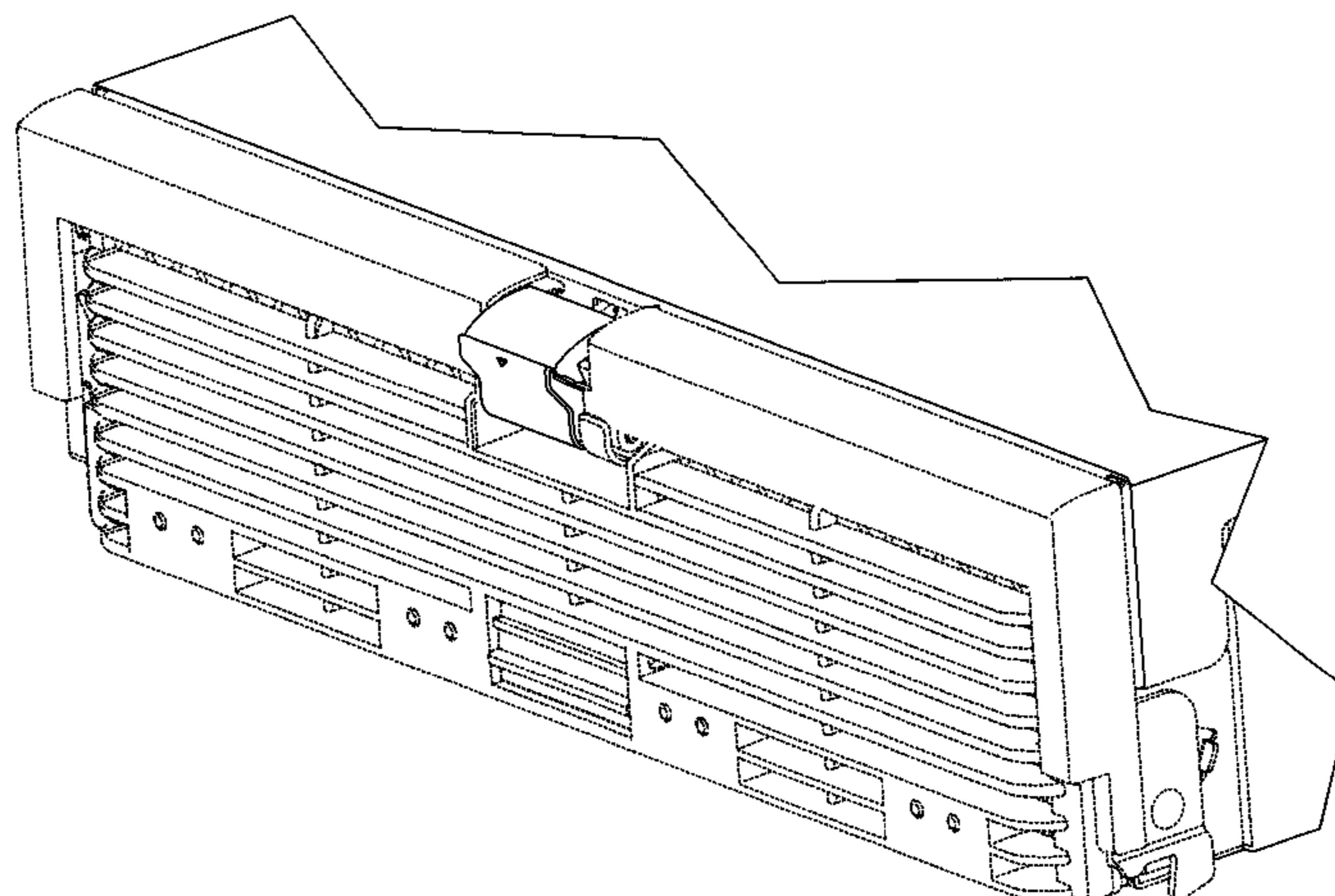
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*Primary Examiner*—Freda S. Nunn

(57)

### **CLAIM**

The ornamental design for a computer processor module having a front release mechanism, as shown and described.

### **DESCRIPTION**

FIG. 1 is an enlarged partial front perspective view of the design for the computer processor module having a front release mechanism in a secured configuration;

FIG. 2 is a front elevational view of the design for the computer processor module having a front release mechanism in a secured configuration as illustrated in FIG. 1;

FIG. 3 is a right side elevational view of the design for the computer processor module having a front release mechanism in a secured configuration as illustrated in FIG. 1, the left side being substantially the same as the illustrated right side;

FIG. 4 is a top elevational view of the design for the computer processor module having a front release mechanism in a secured configuration as illustrated in FIG. 1;

FIG. 5 is a bottom elevational view of the design for the computer processor module having a front release mechanism in a secured configuration as illustrated in FIG. 1;

FIG. 6 is a partial front perspective view of the design for the computer processor module having a front release mechanism as illustrated in FIG. 1, wherein the front release mechanism is illustrated in a partially release configuration;

FIG. 7 is a partial front perspective view of the design for the computer processor module having a front release mechanism as illustrated in FIG. 1, wherein the front release mechanism is illustrated in a fully released configuration;

FIG. 8 is a front elevational view of the design for the computer processor module having a front release mechanism in a fully released configuration as illustrated in FIG. 7;

FIG. 9 is a right side elevational view of the design for the computer processor module having a front release mechanism in a fully released configuration as illustrated in FIG. 7, the left side being substantially the same as the illustrated right side;

FIG. 10 is a top elevational view of the design for the computer processor module having a front release mechanism in a fully released configuration as illustrated in FIG. 7;

FIG. 11 is a bottom elevational view of the design for the computer processor module having a front release mechanism in a fully released configuration as illustrated in FIG. 7;

FIG. 12 is a partial front perspective view of the design for the computer processor module having a front release mechanism as illustrated in FIG. 1, wherein the computer processor module is illustrated in context of a rack mountable computer, e.g., a server, that can be mounted in a rack computer system; and,

FIG. 13 is a front elevational view of the design for the computer processor module having a front release mechanism as illustrated in FIG. 12, wherein the computer processor module is illustrated in context of a rack mountable computer, e.g., a server, that can be mounted in a rack computer system.

In these drawings, the solid lines illustrate the claimed ornamental design, whereas the broken lines illustrate environmental features that form no part of the claimed ornamental design of the subject embodiment.

**1 Claim, 13 Drawing Sheets**

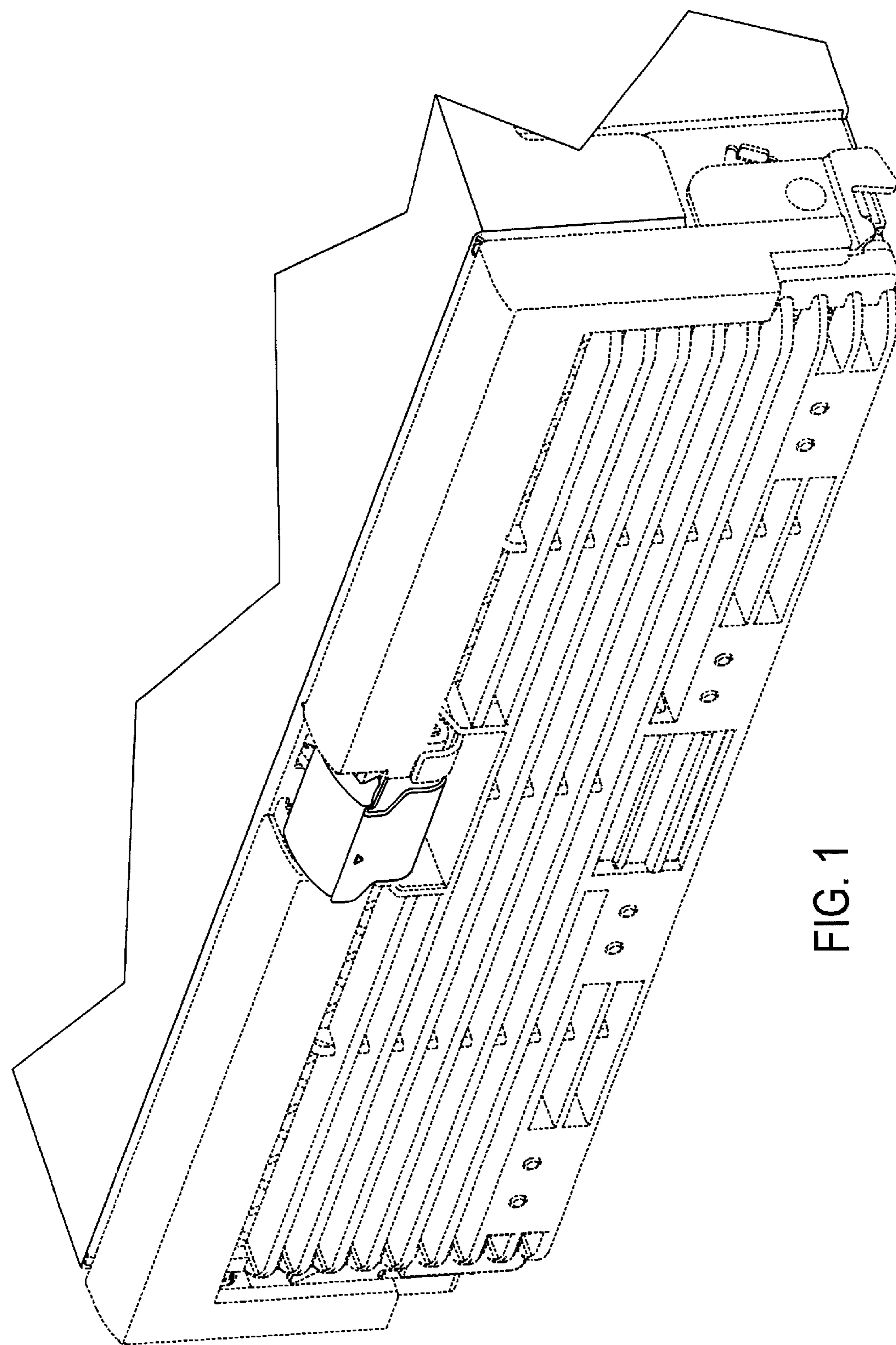


FIG. 1

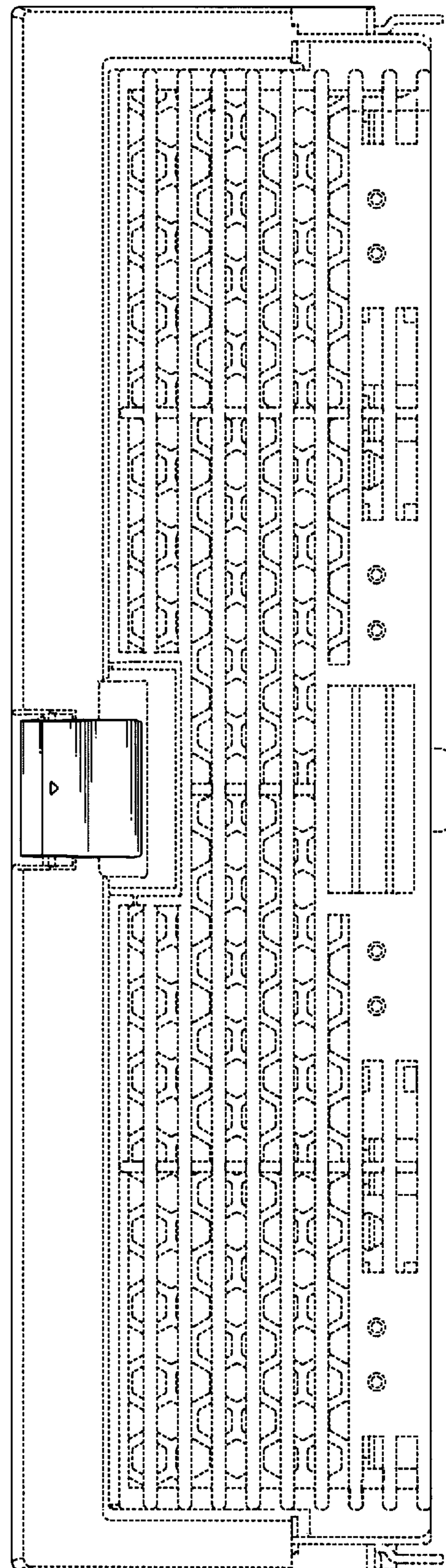


FIG. 2

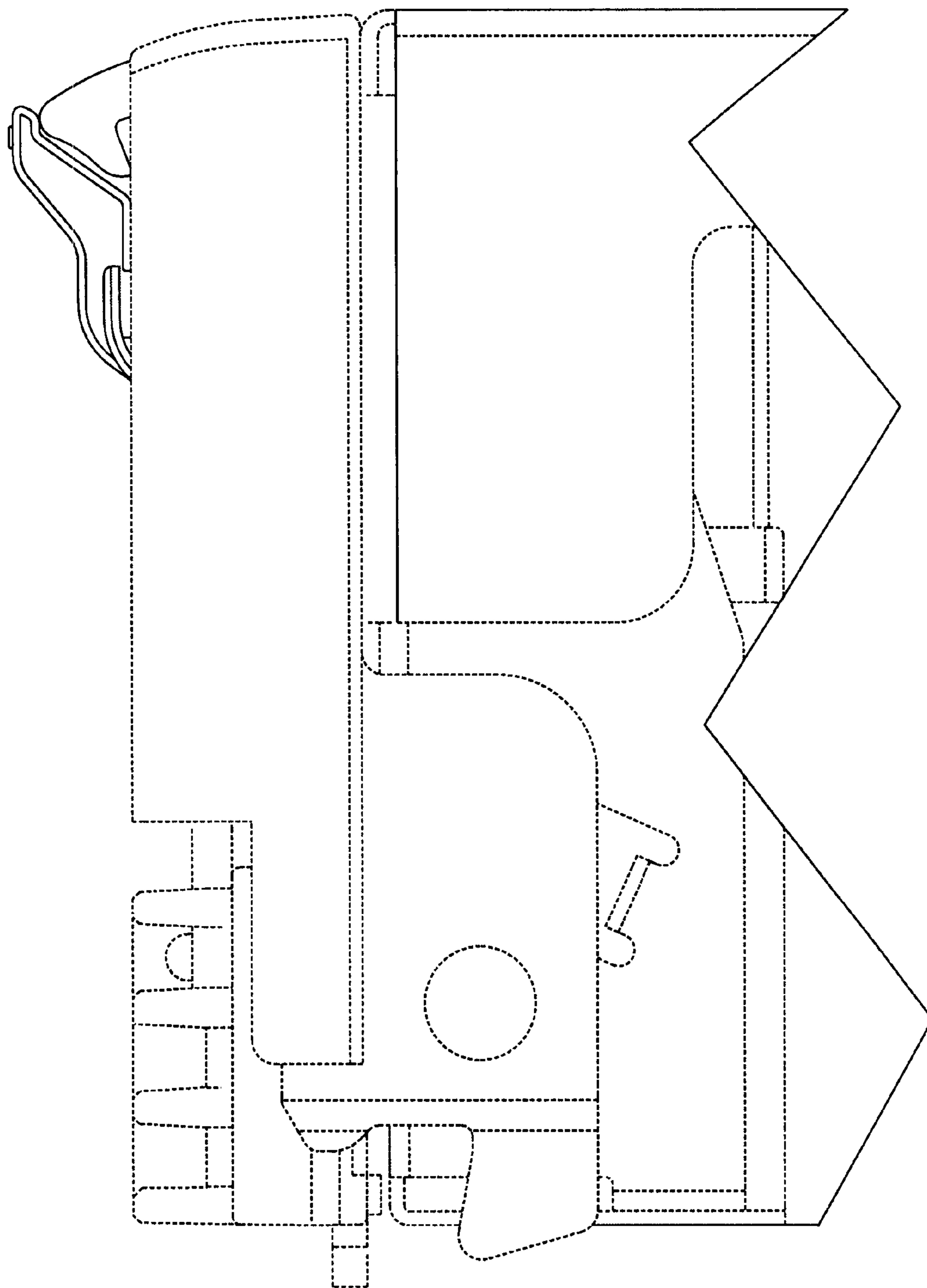
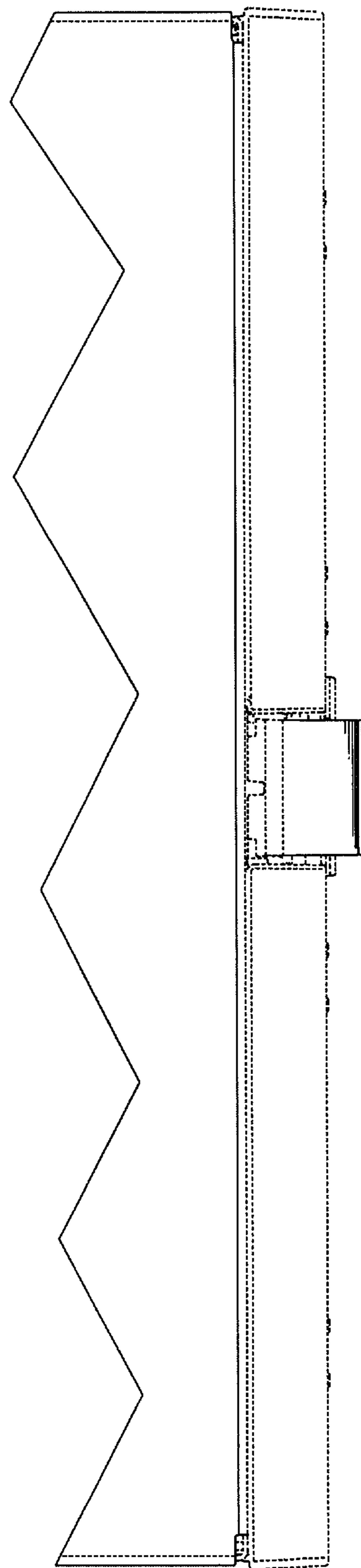
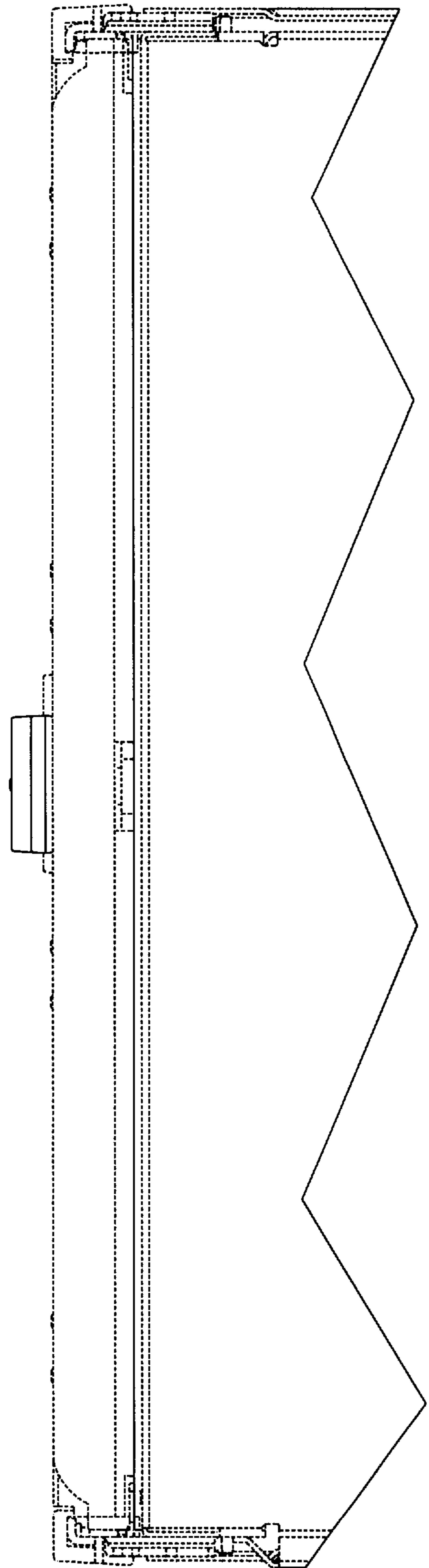


FIG. 3



**FIG. 4**



**FIG. 5**

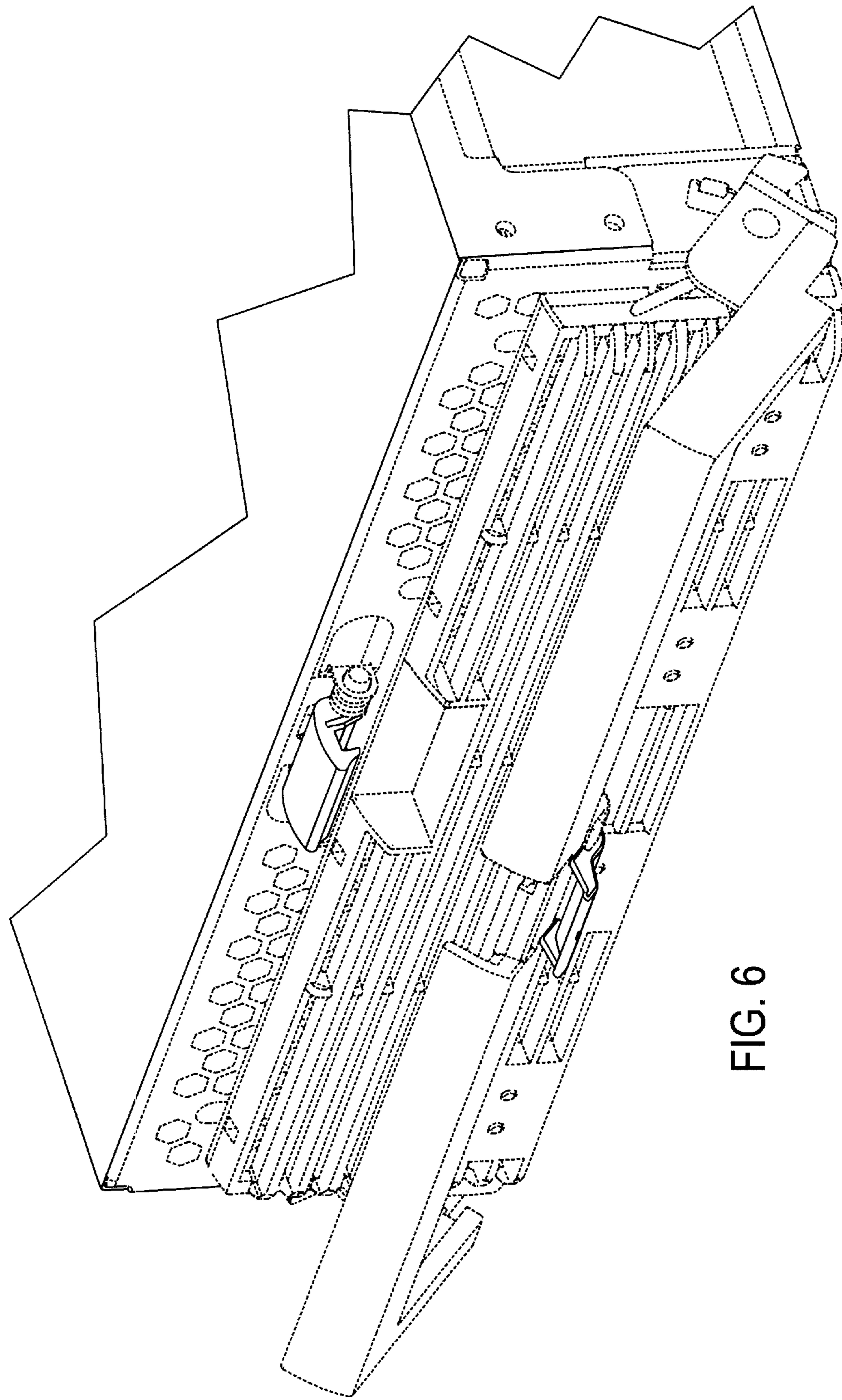


FIG. 6

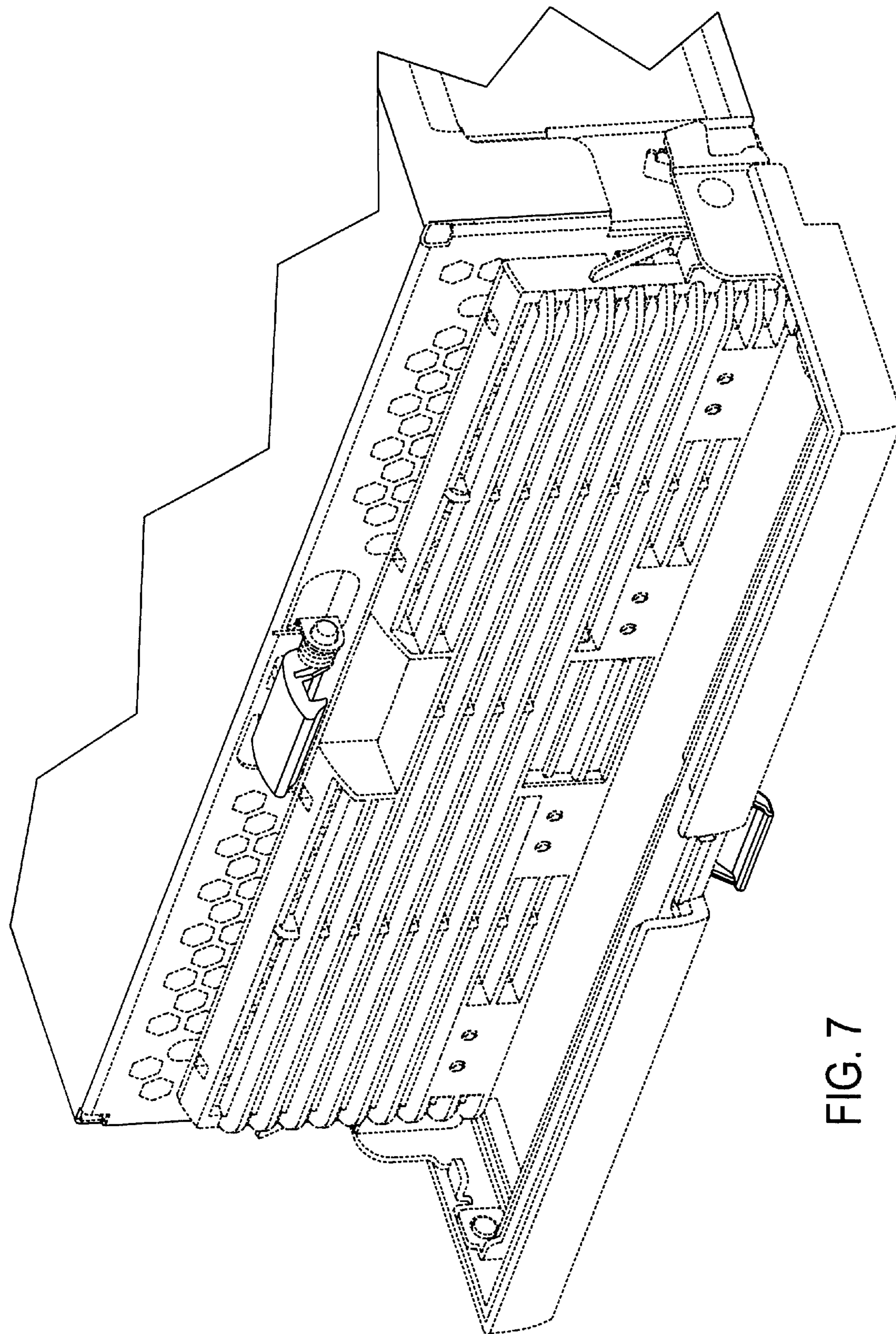
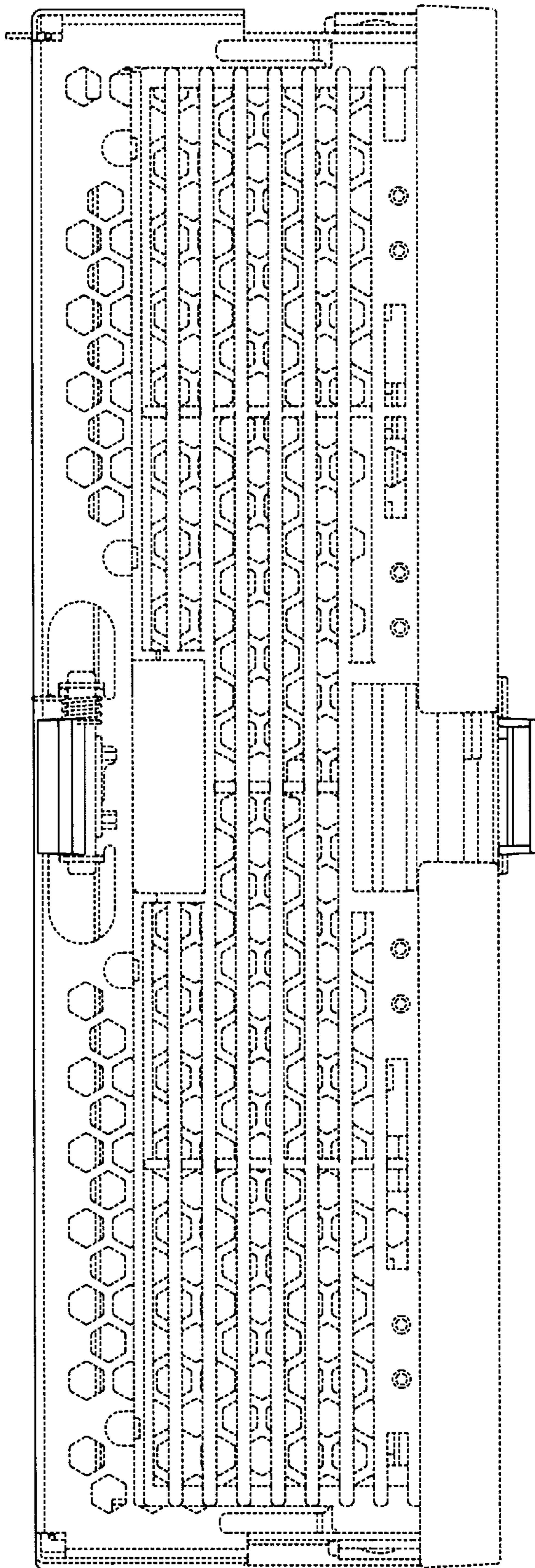
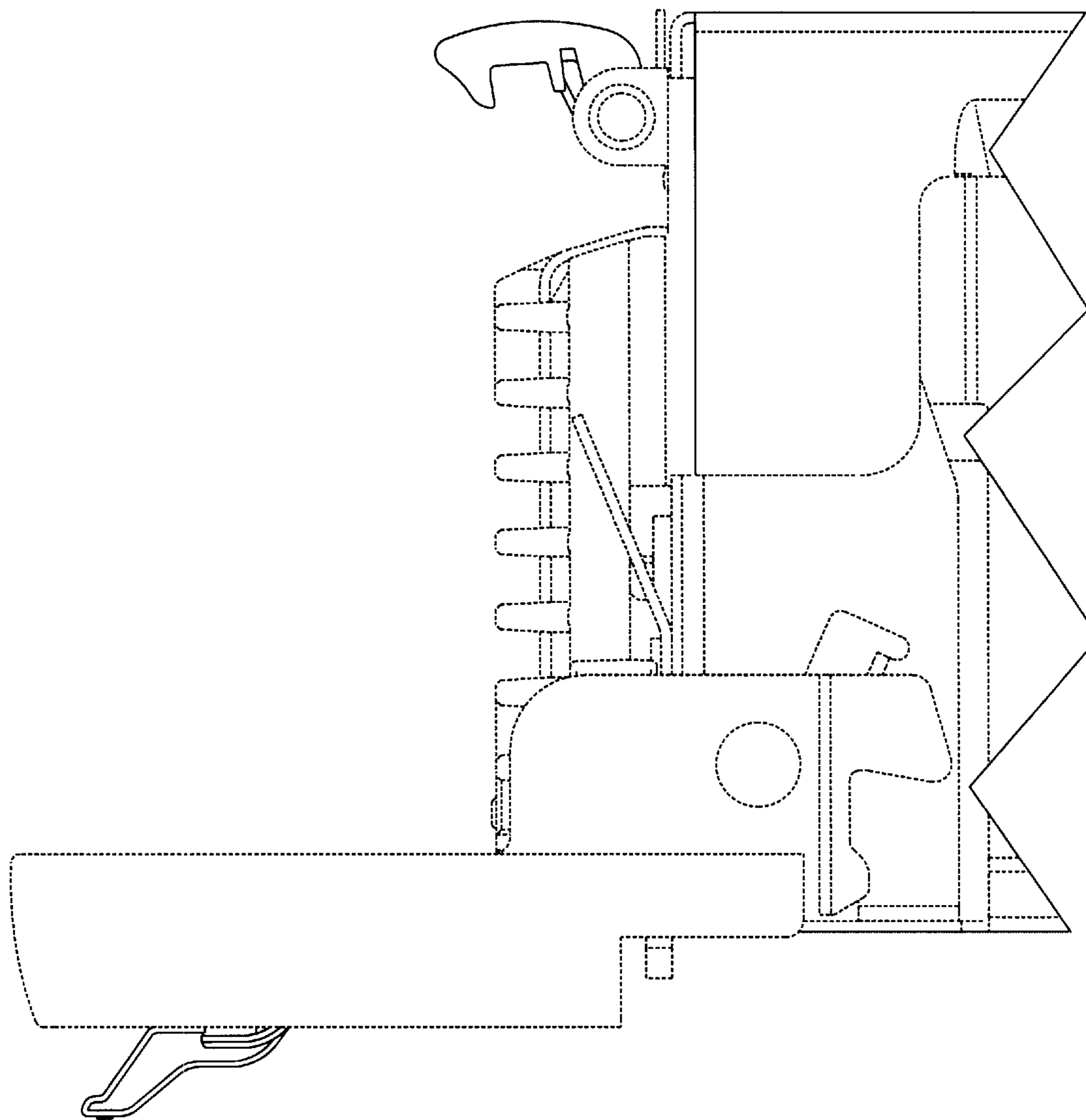


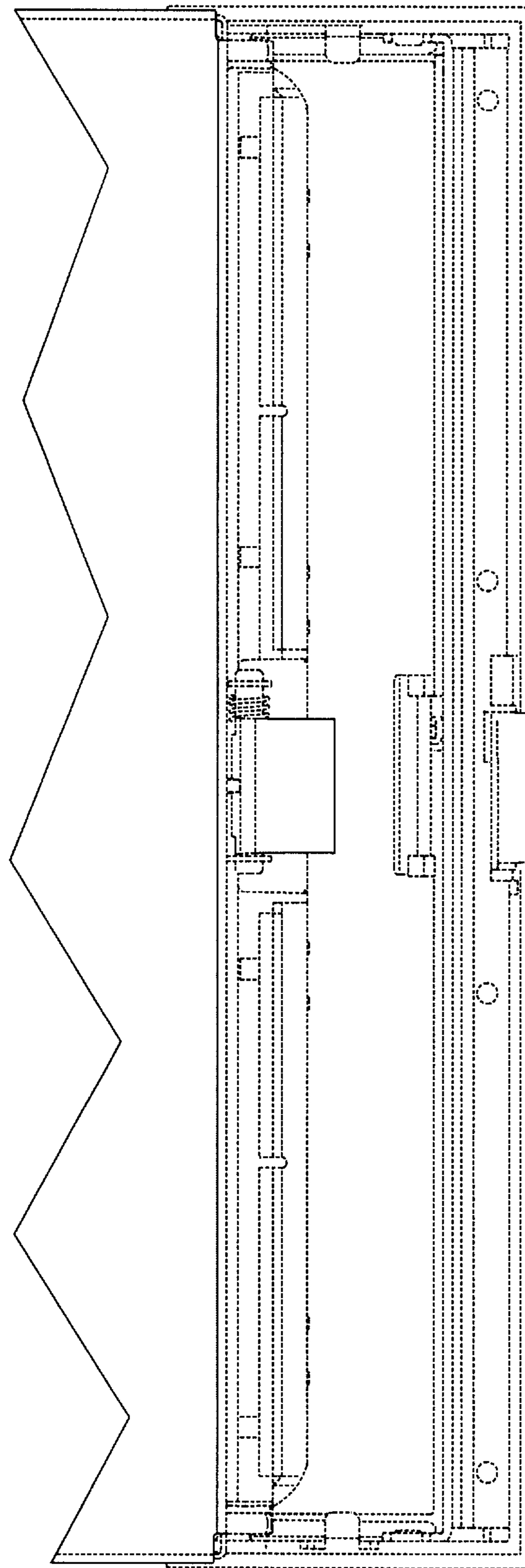
FIG. 7

FIG. 8





**FIG. 9**



**FIG. 10**

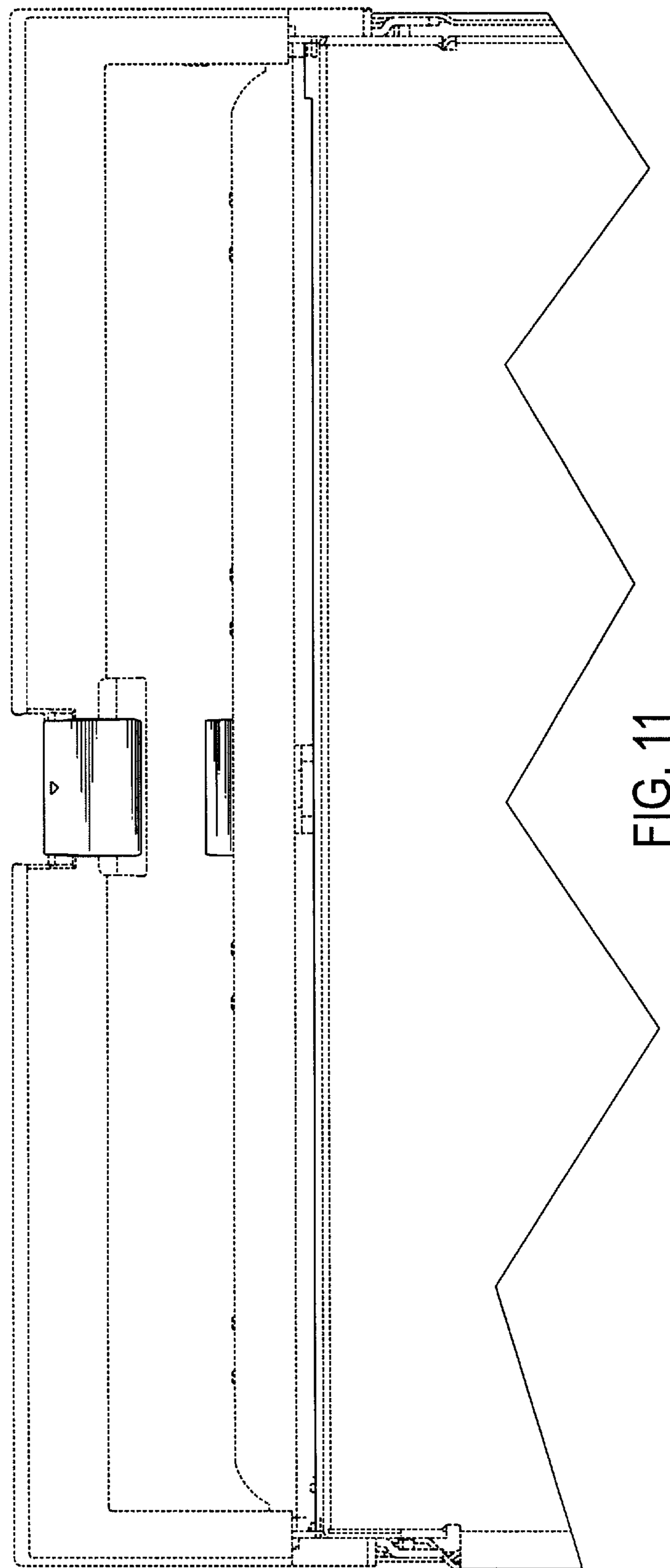


FIG. 11

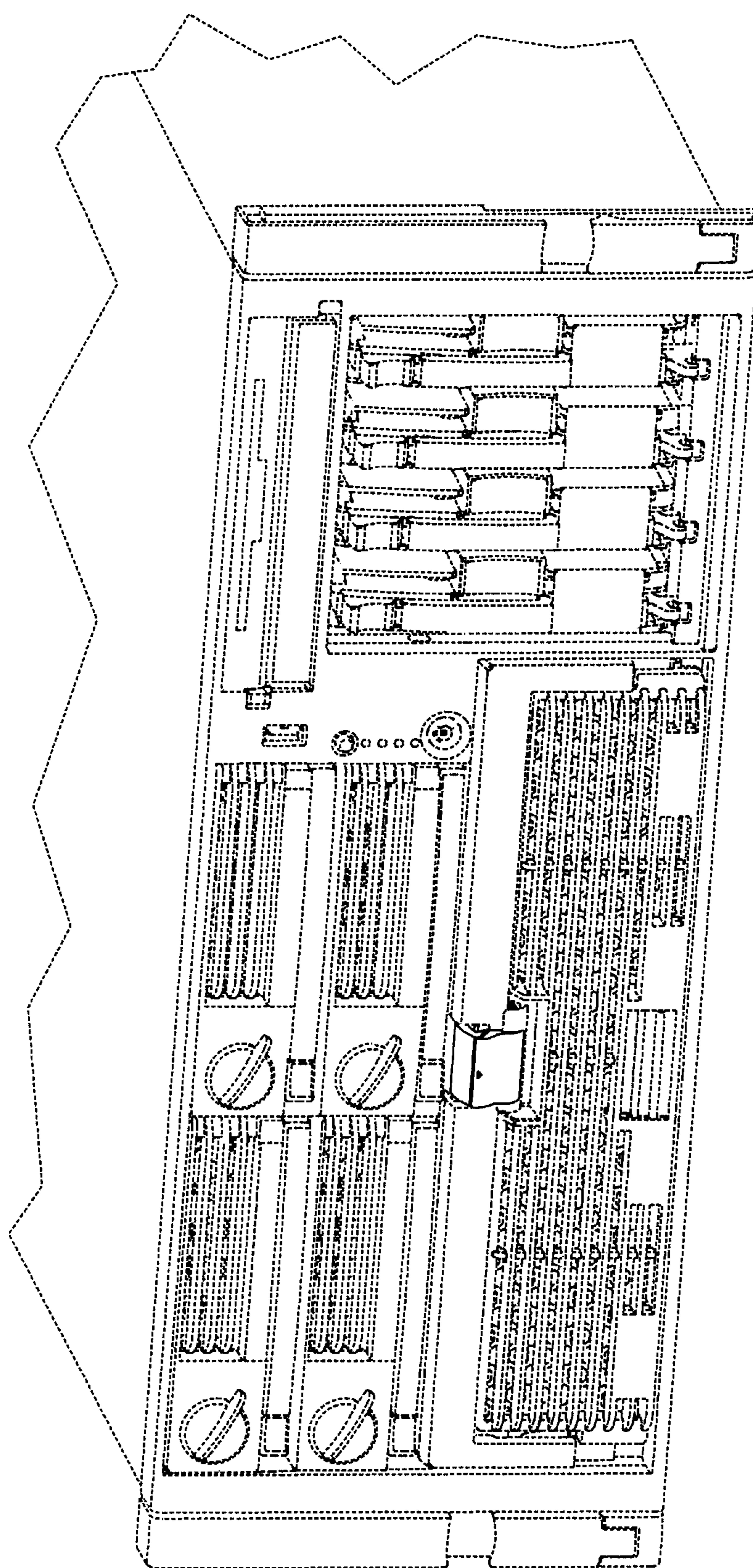


FIG. 12

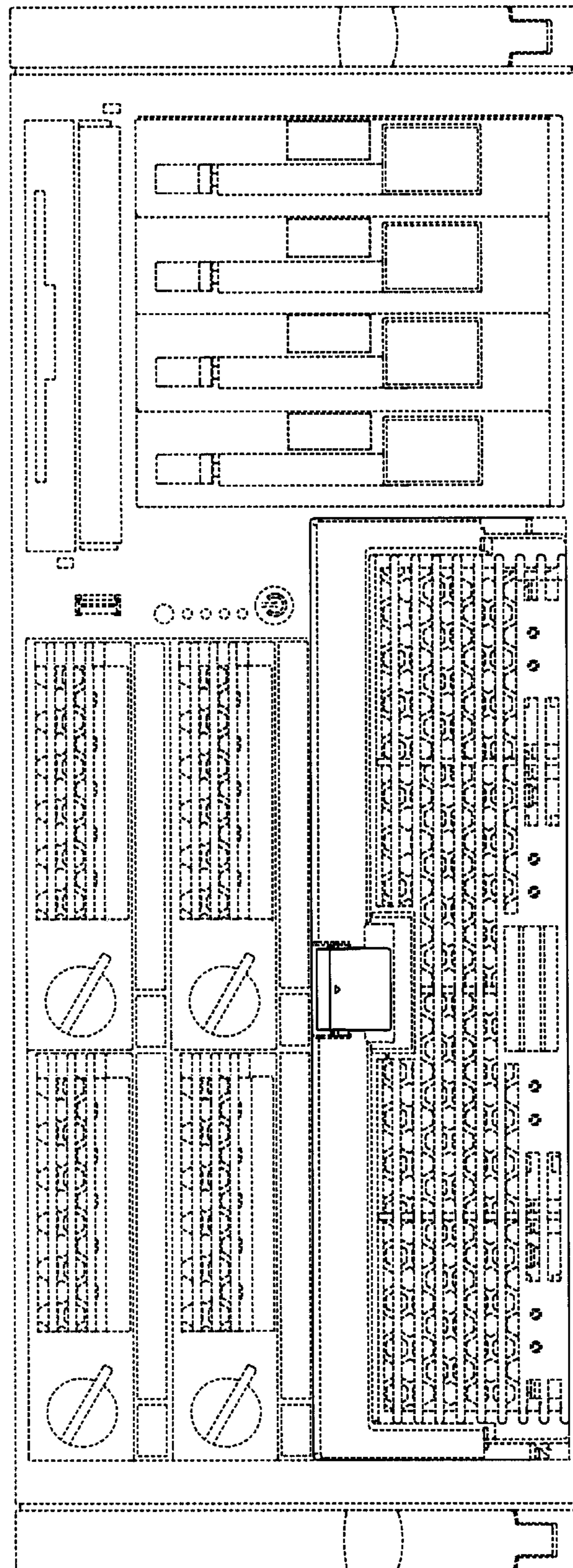


FIG. 13