



US00D539761S

(12) **United States Design Patent** (10) **Patent No.:** **US D539,761 S**
Takahashi et al. (45) **Date of Patent:** **** Apr. 3, 2007**

(54) **SEMICONDUCTOR**

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(**) Term: **14 Years**

(21) Appl. No.: **29/267,014**

(22) Filed: **Oct. 4, 2006**

(30) **Foreign Application Priority Data**

Aug. 22, 2006 (JP) 2006-022163

(51) **LOC (8) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Classification Search** D13/133,
D13/146-147, 149, 154, 182, 184; 174/250,
174/253; 257/666, 694, 696; 361/760, 820
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a semiconductor, as shown and described.

DESCRIPTION

FIG. 1 is a top, front and left side perspective view of a semiconductor showing our new design, FIG. 2 is a front elevational view thereof, FIG. 3 is a rear elevational view thereof, FIG. 4 is a right side elevational view thereof, a left side elevational view being identical thereto FIG. 5 is a top plan view thereof; and, FIG. 6 is a bottom plan view thereof.

1 Claim, 3 Drawing Sheets

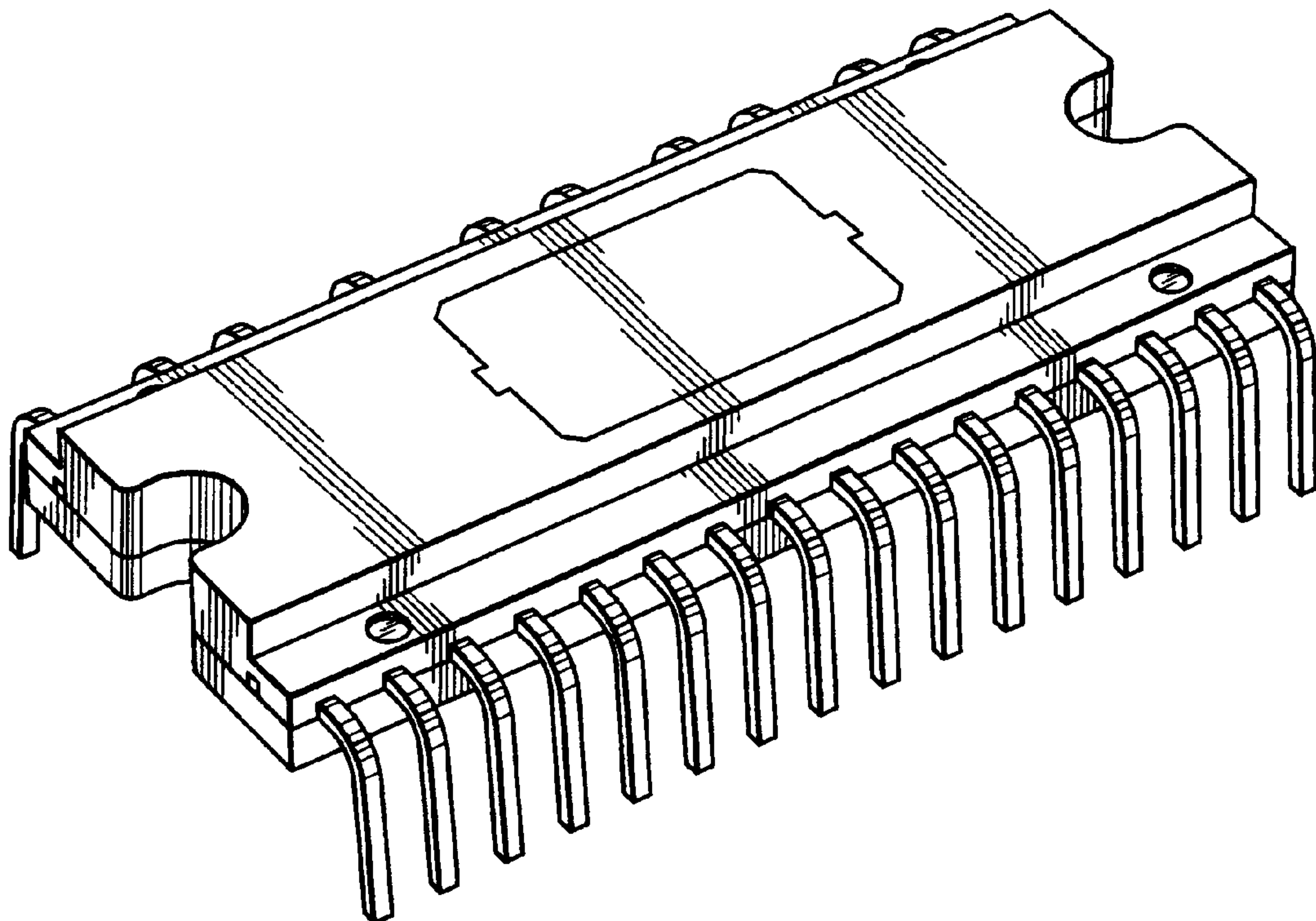


FIG. 1

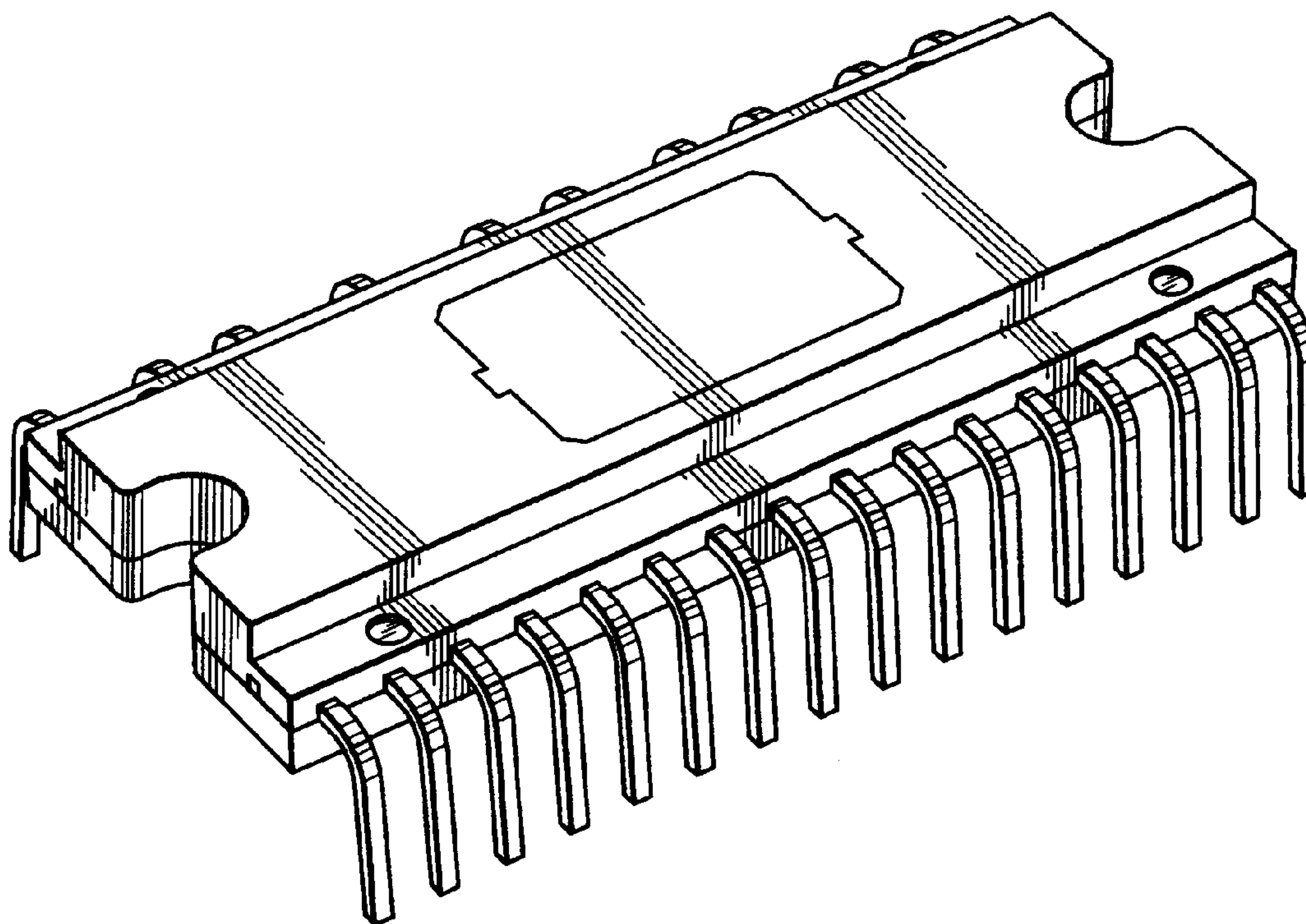


FIG. 2

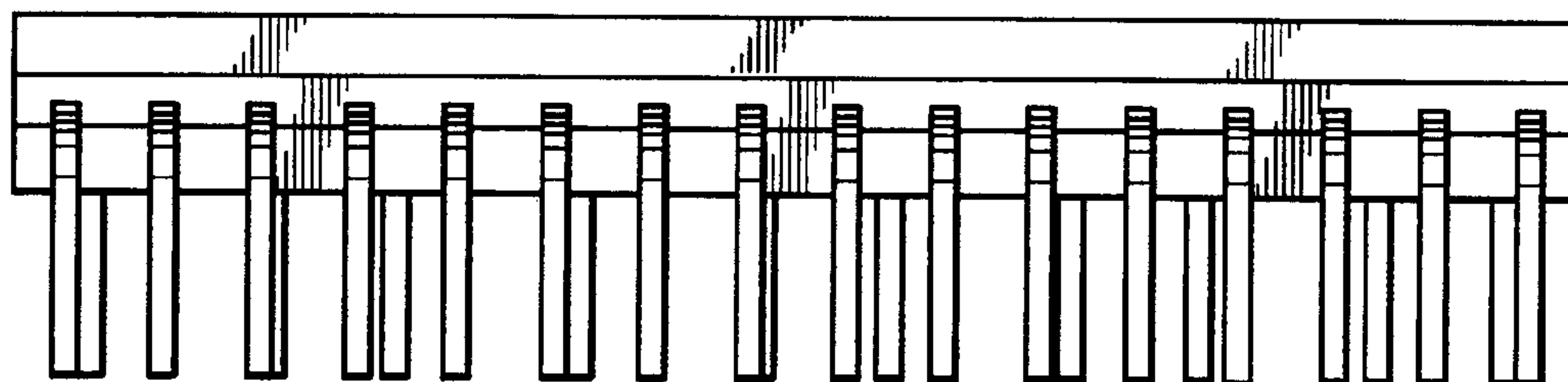


FIG. 3

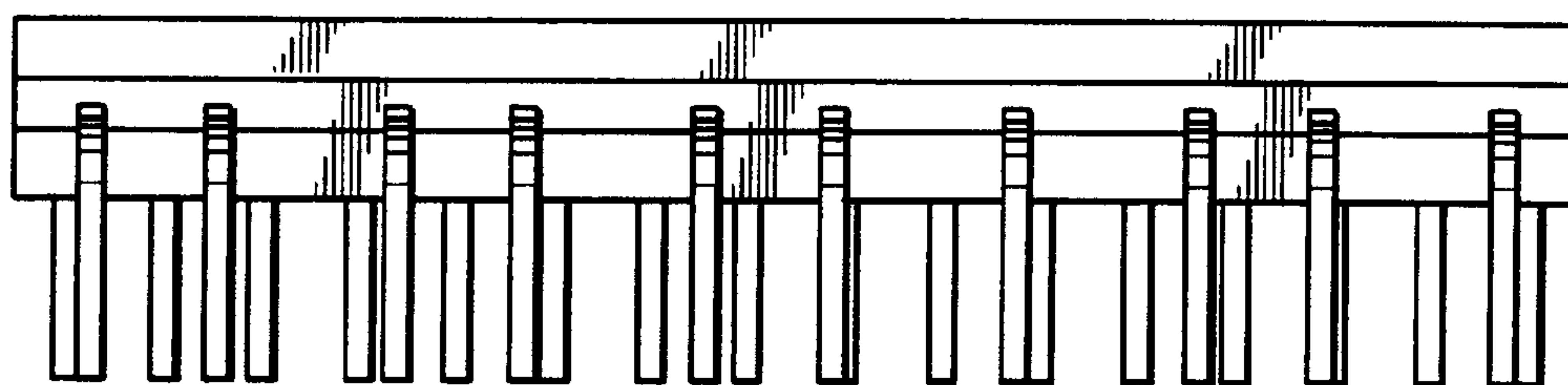


FIG. 4

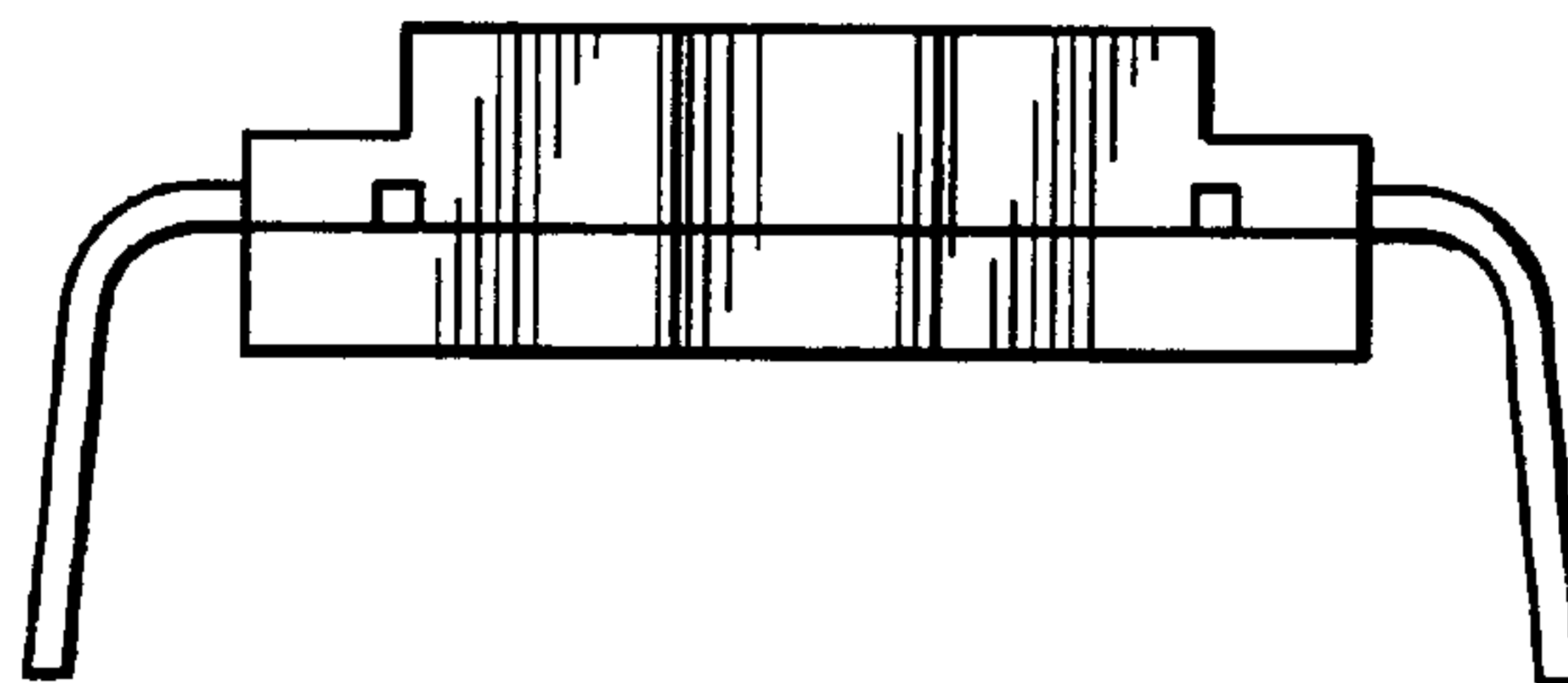


FIG. 5

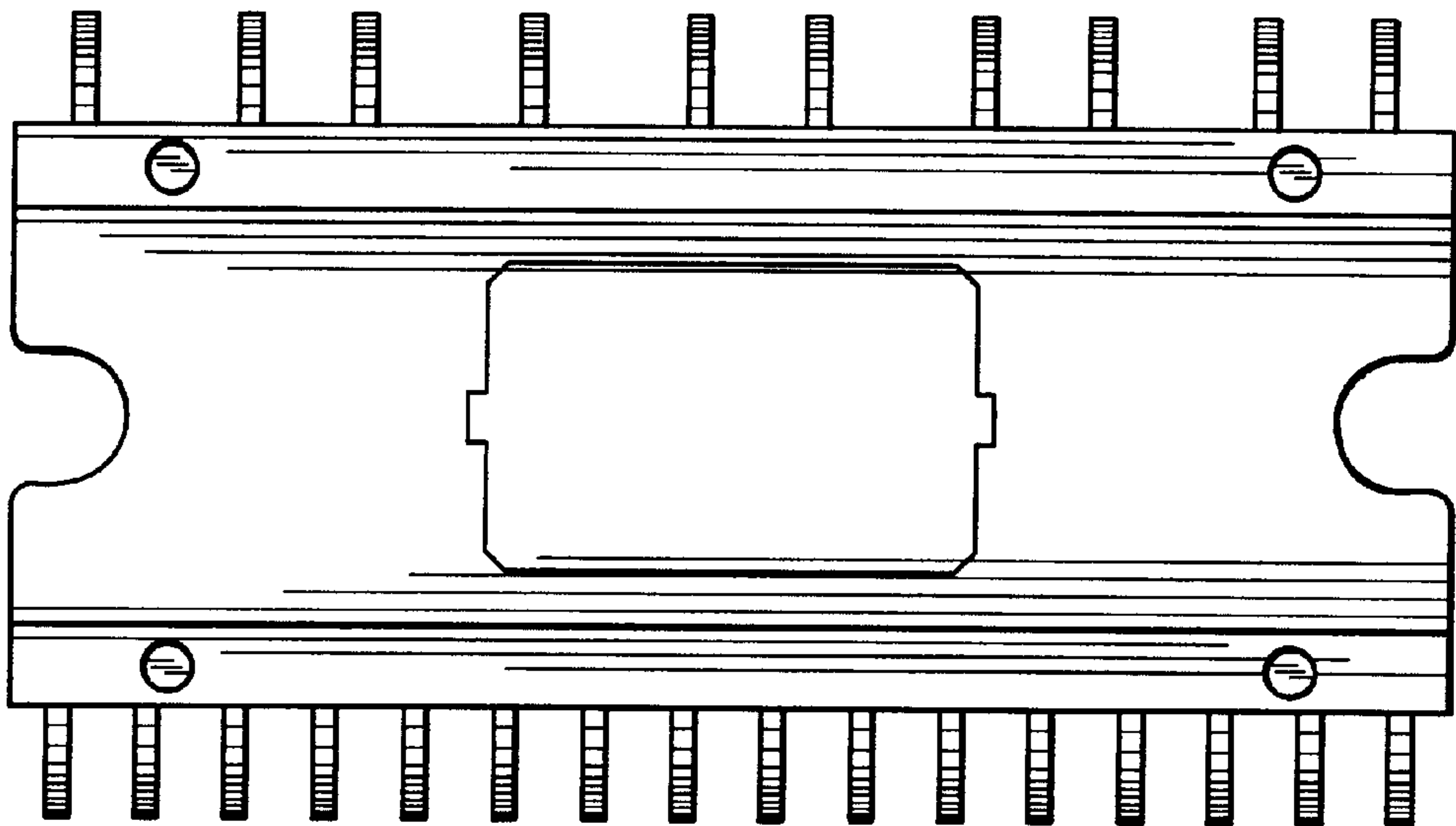


FIG. 6

