



US00D534537S

(12) **United States Design Patent**  
**Smith et al.**

(10) **Patent No.:** **US D534,537 S**

(45) **Date of Patent:** **\*\* Jan. 2, 2007**

(54) **CONNECTING TERMINAL FOR CHIP CARDS**

(75) Inventors: **Cindy M. Smith**, Kirkland, WA (US);  
**Paul R. Nash**, Bellevue, WA (US);  
**Brandon J. Chandler**, Shoreline, WA (US)

(73) Assignee: **Wildseed Ltd.**, Seattle, WA (US)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/197,124**

(22) Filed: **Jan. 6, 2004**

(51) **LOC (8) Cl.** ..... **14-02**

(52) **U.S. Cl.** ..... **D14/437**

(58) **Field of Classification Search** ..... D14/436,  
D14/437, 432; 361/749, 739, 683; 257/787,  
257/738, 750, 778, 782, 784; 235/378-80  
See application file for complete search history.

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

D358,142 S \* 5/1995 Gloton ..... D14/437

D387,746 S \* 12/1997 Ishihara ..... D14/437  
D389,130 S \* 1/1998 Ishihara ..... D14/437  
D405,078 S \* 2/1999 Ross ..... D14/437  
D412,164 S \* 7/1999 Laviron et al. .... D14/437  
D425,519 S \* 5/2000 Merlin et al. .... D14/436  
D427,577 S \* 7/2000 Haas et al. .... D14/437  
D456,414 S \* 4/2002 Turin ..... D14/437

\* cited by examiner

*Primary Examiner*—M. H. Tung

(74) *Attorney, Agent, or Firm*—Schwabe, Williamson & Wyatt, P.C.

(57) **CLAIM**

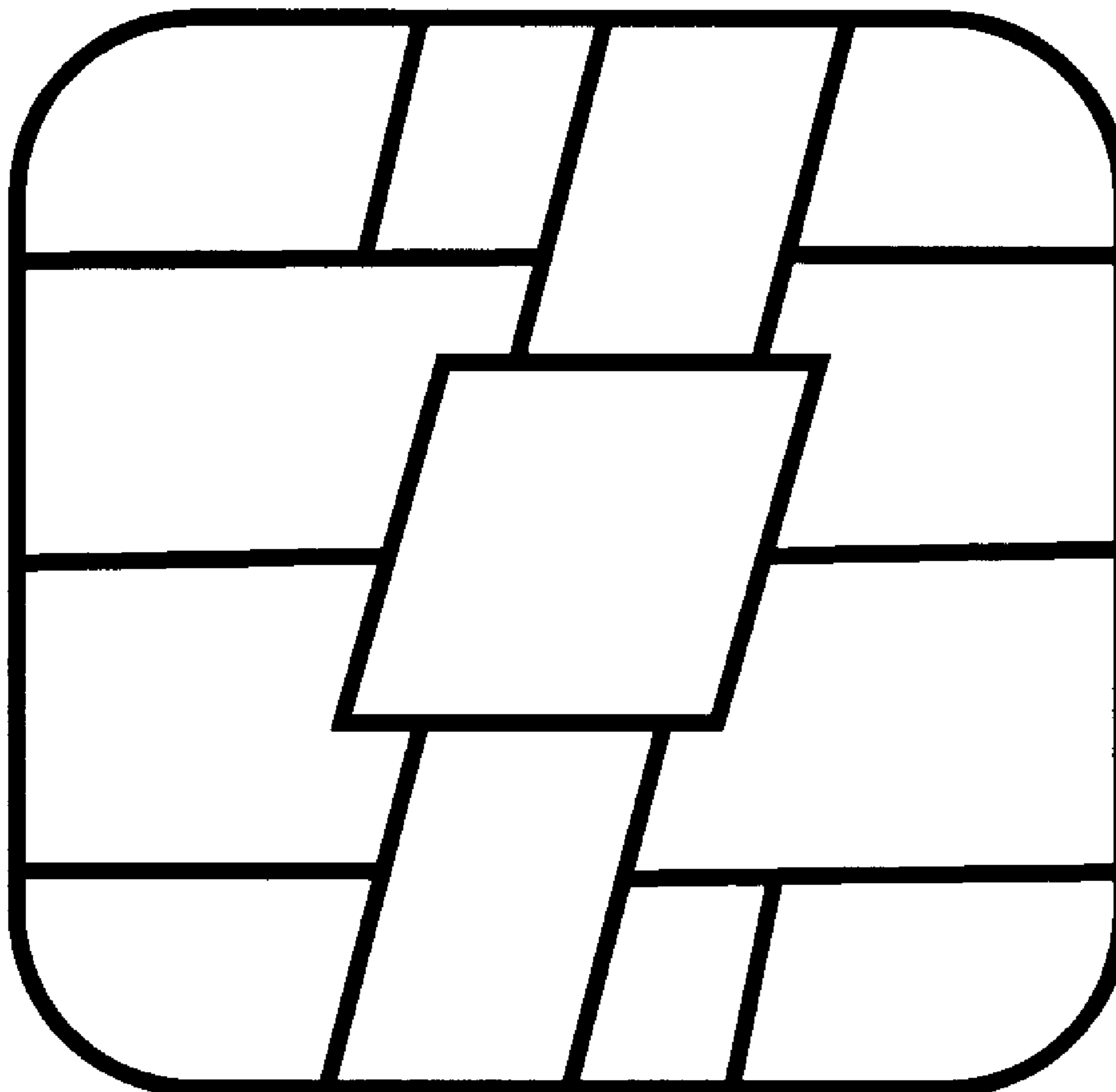
What is claimed is the ornamental design for a connecting terminal for chip cards, as shown and described.

**DESCRIPTION**

FIG. 1 is a top plan view of a connecting terminal for chip cards showing our new design; and, FIG. 2 is a bottom plan view thereof.

The connecting terminal is of no substantial thickness.

**1 Claim, 1 Drawing Sheet**



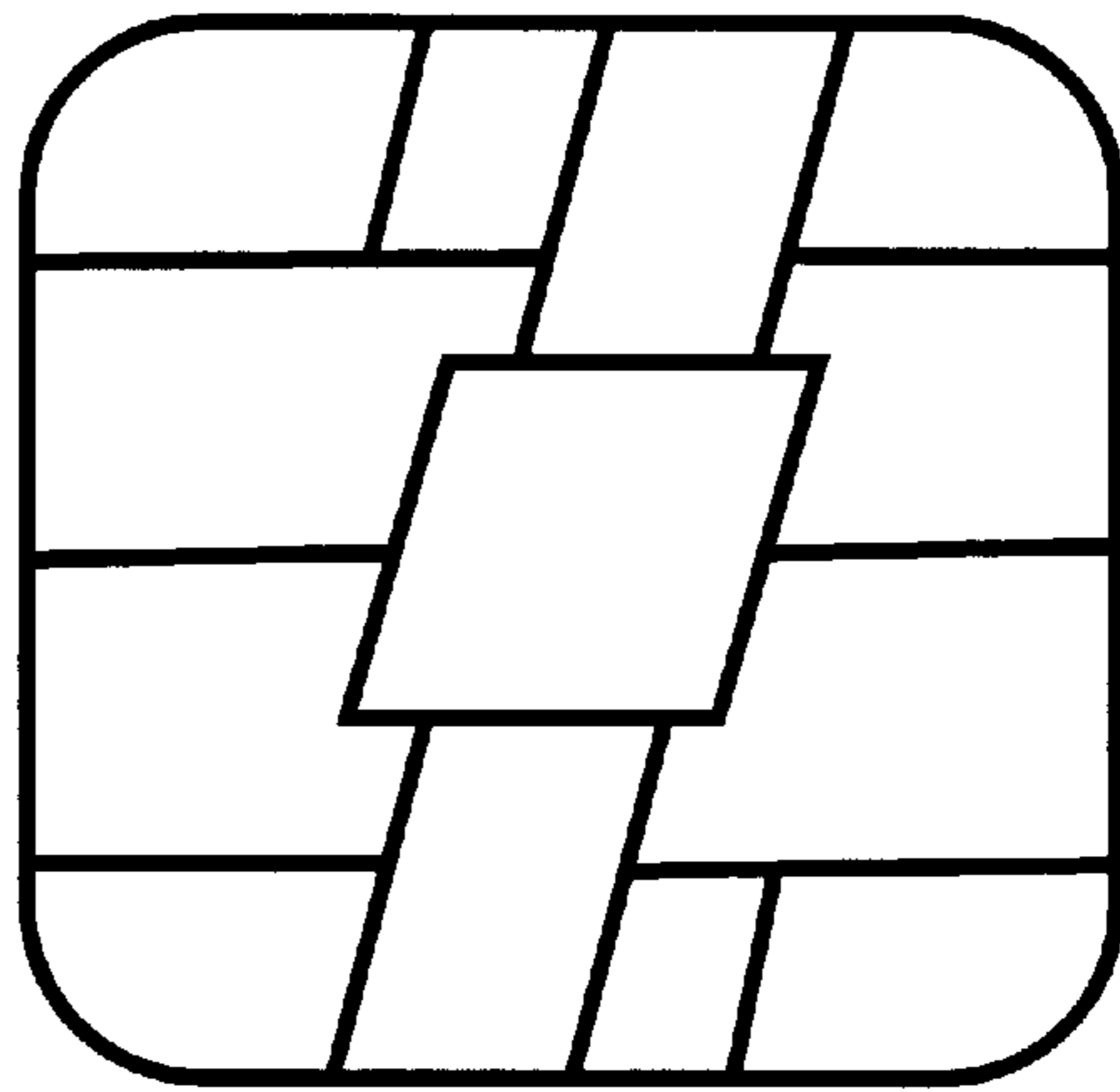


Figure 1

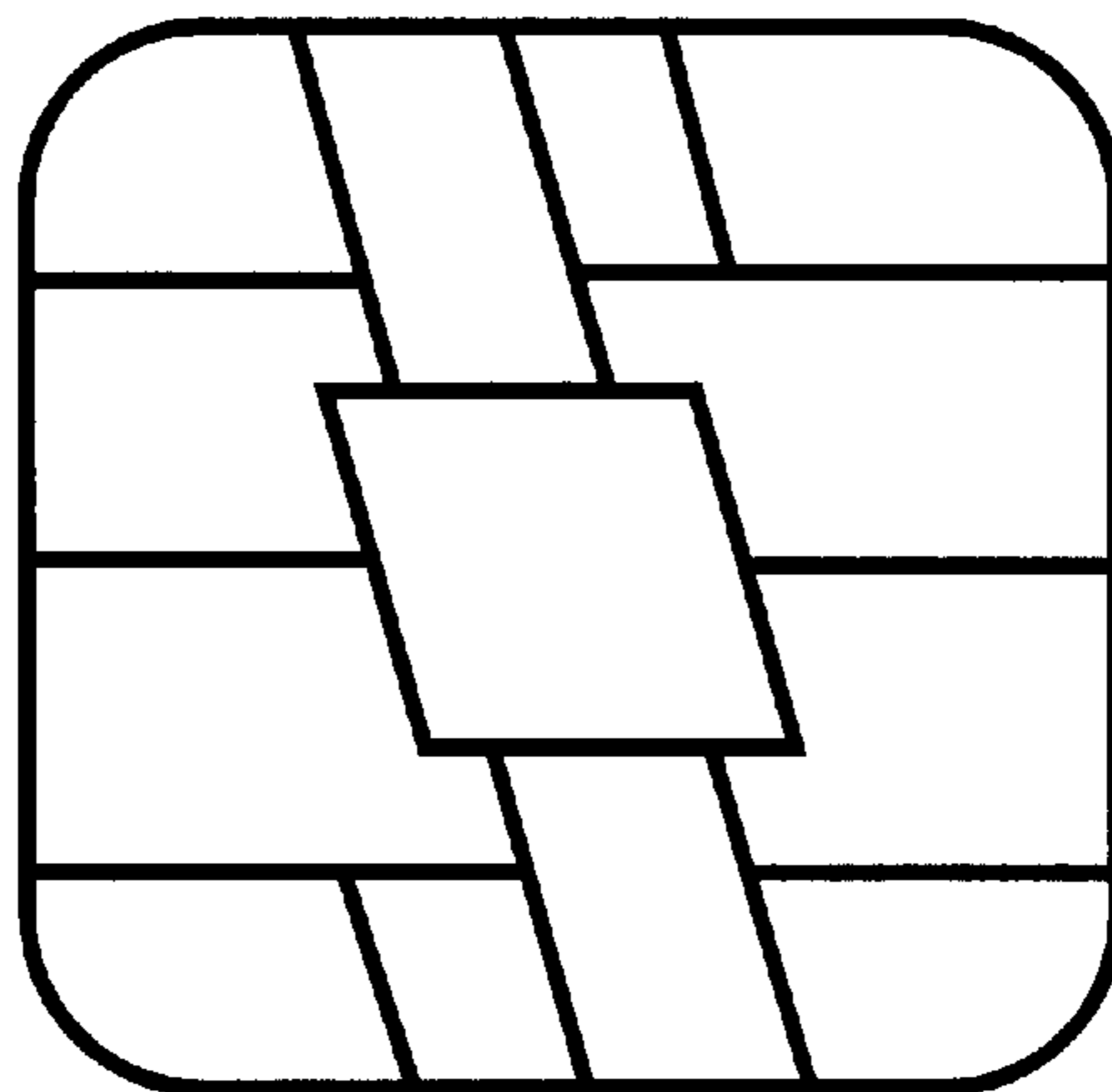


Figure 2