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(12) **United States Design Patent**
Lai

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(54) **ELECTRODE LAYER**

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(**) Term: **14 Years**

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(22) Filed: **Nov. 2, 2005**

(51) **LOC (8) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Classification Search** D13/182;
257/79, 91, 99; 313/503

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D262,962 S * 2/1982 Strumpell D13/182
6,344,665 B1 * 2/2002 Sung et al. 257/91
6,518,598 B1 * 2/2003 Chen 257/91

6,603,152 B1 * 8/2003 Song et al. 257/99
6,650,018 B1 * 11/2003 Zhao et al. 257/775
6,815,886 B1 * 11/2004 Kawase 313/506
2003/0006422 A1 * 1/2003 Miki et al. 257/99
2005/0224823 A1 * 10/2005 Zhao et al. 257/91

* cited by examiner

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(57) **CLAIM**

The ornamental design for an electrode layer, as shown and described.

DESCRIPTION

FIG. 1 is a top plan view of an electrode layer showing my new design;

FIG. 2 is a bottom plan view thereof;

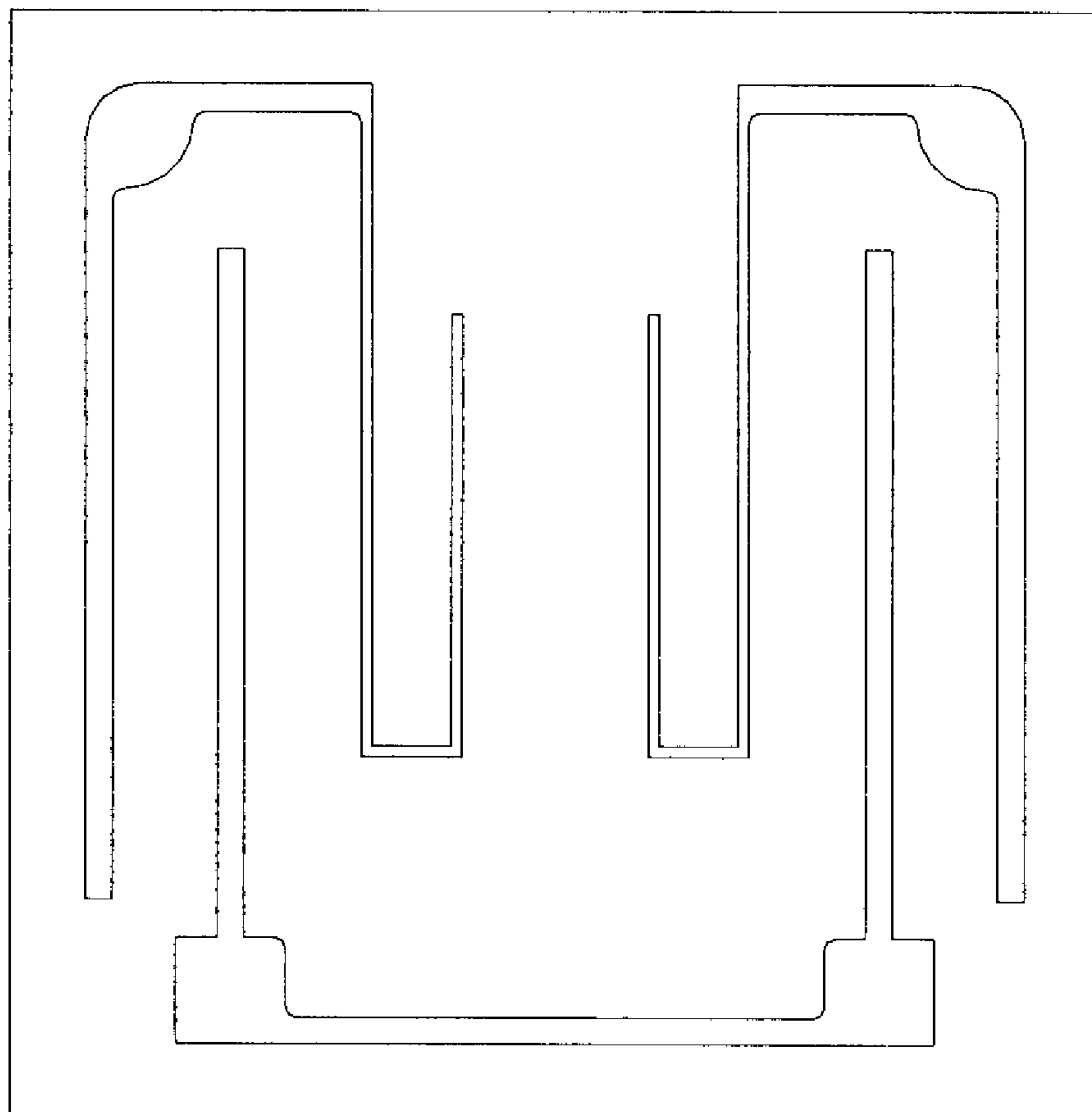
FIG. 3 is a front elevational view thereof;

FIG. 4 is a rear elevational view thereof;

FIG. 5 is a left side elevational view thereof; and,

FIG. 6 is a right side elevational view thereof.

1 Claim, 2 Drawing Sheets



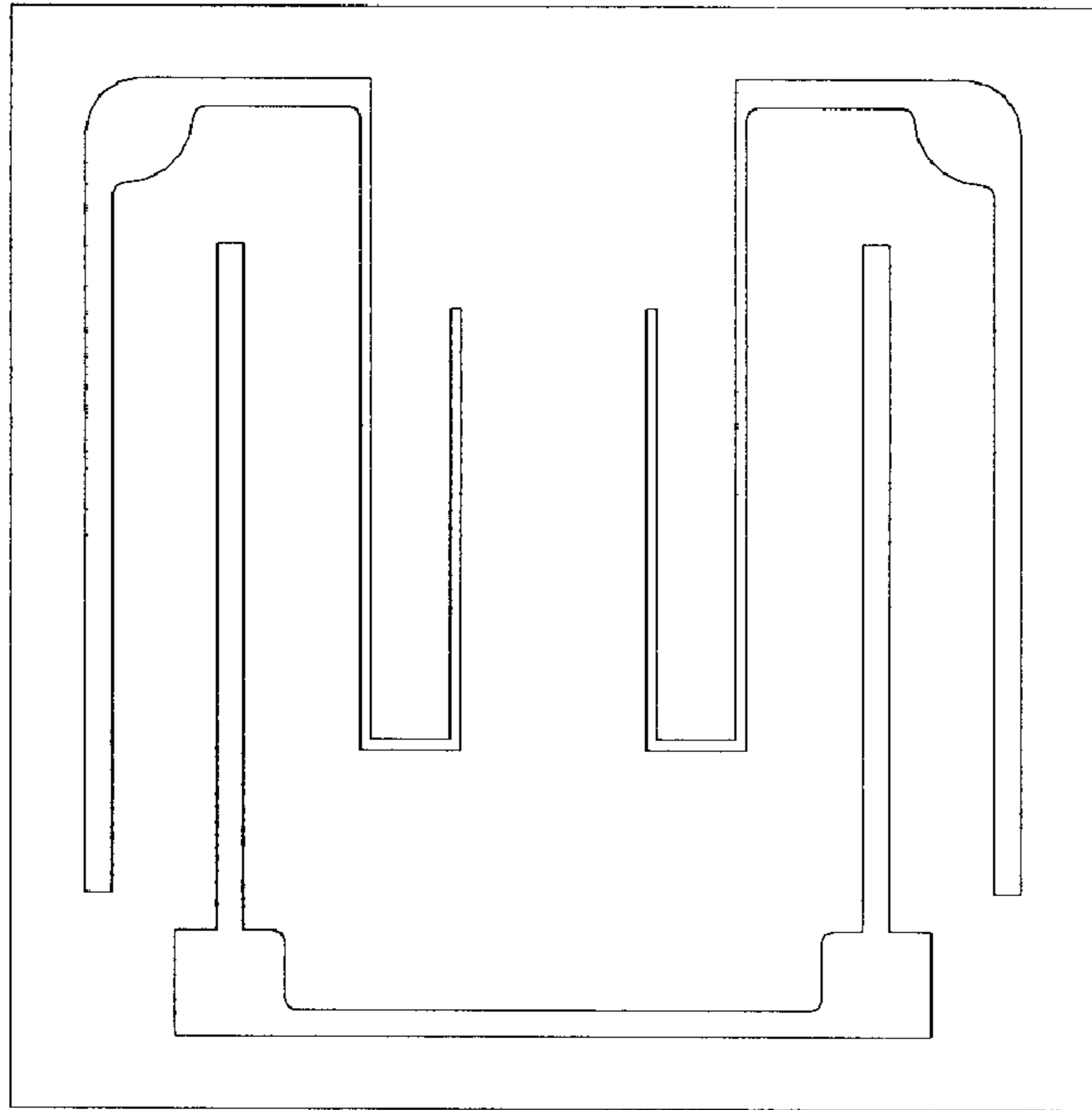


Fig. 1

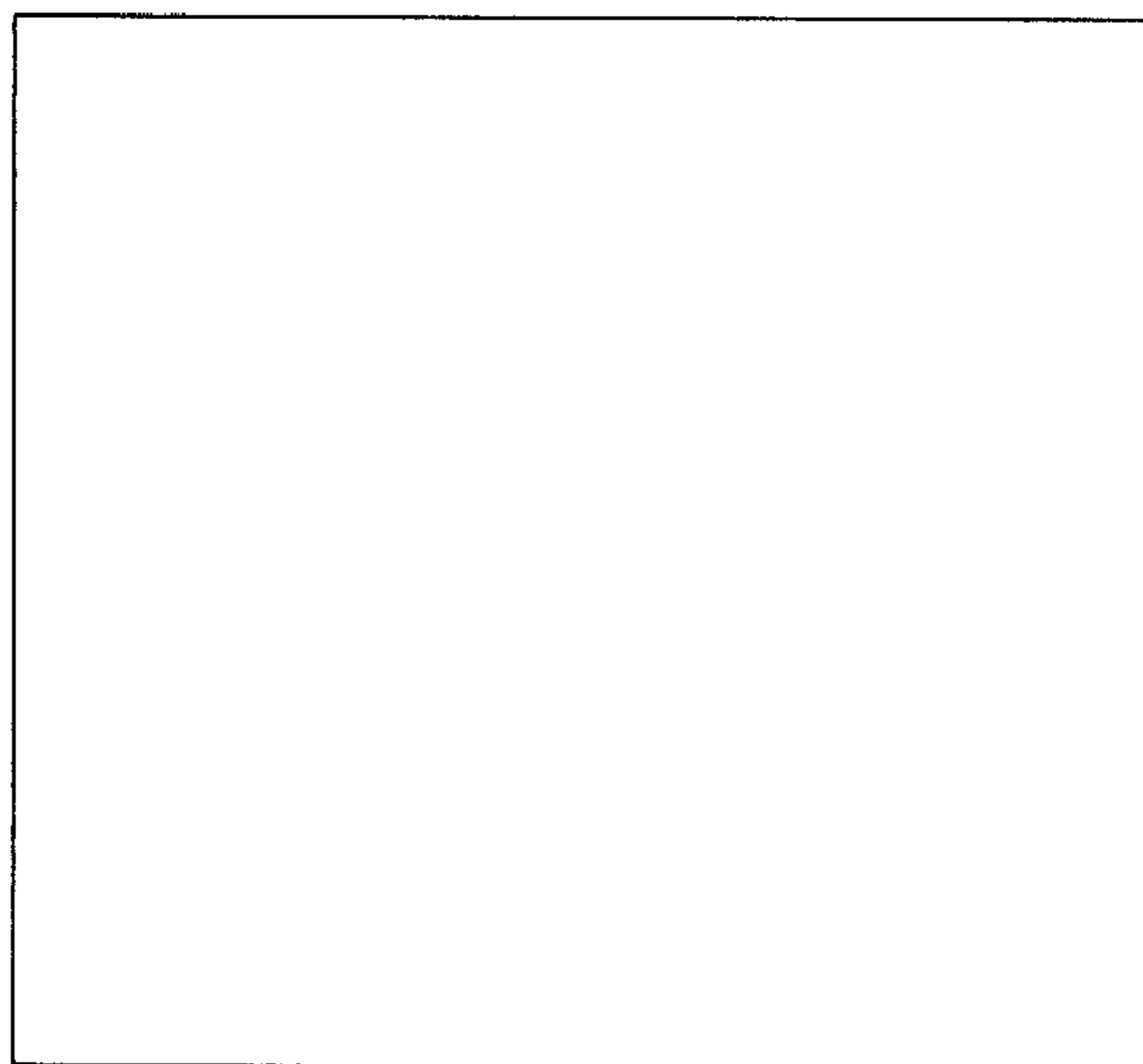


Fig. 2

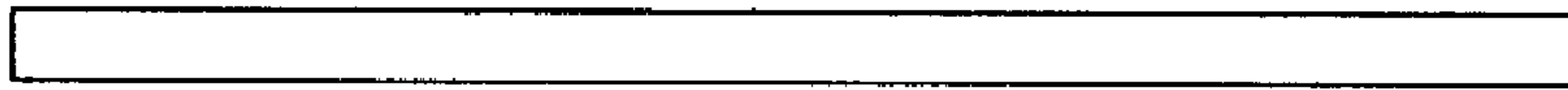


Fig. 3



Fig. 4

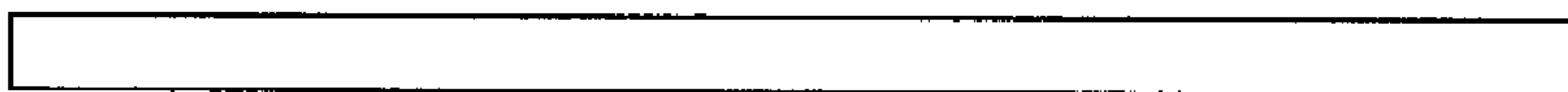


Fig. 5



Fig. 6