



US00D532382S

(12) **United States Design Patent**
Inoue

(10) **Patent No.:** **US D532,382 S**

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(54) **PACKAGE FOR LIGHT EMITTING DIODE**

FOREIGN PATENT DOCUMENTS

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TW 581547 3/2004

(73) Assignee: **Rohm Co., Ltd.**, Kyoto (JP)

* cited by examiner

(*) Notice: This patent is subject to a terminal disclaimer.

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(**) Term: **14 Years**

(57) **CLAIM**

We claim; the ornamental design for a package for light emitting diode, substantially as shown and described.

(21) Appl. No.: **29/227,034**

DESCRIPTION

(22) Filed: **Apr. 6, 2005**

(30) **Foreign Application Priority Data**

Oct. 18, 2004 (JP) 2004-034992

FIG. 1 is a perspective design view of a light emitting diode showing my new design, a lighting element is indicated in dashed lines for environmental purposes only;

(51) **LOC (8) Cl.** **13-03**

FIG. 2 is a front view thereof;

(52) **U.S. Cl.** **D13/180**

FIG. 3 is a rear view thereof;

(58) **Field of Classification Search** D13/180;

FIG. 4 is a right side view thereof;

D26/2; 257/79, 80, 81, 82, 88, 89, 95, 98,

FIG. 5 is a top plan view thereof;

257/99, 100, 433, 434, 666; 313/483, 498,

FIG. 6 is a bottom plan view thereof;

313/500; 361/820; 362/235, 555, 800; 372/45

FIG. 7 is a cross-sectional view taken along line 7—7 in FIG. 2;

See application file for complete search history.

FIG. 8 is a cross-sectional view taken along line 8—8 in FIG. 2; and,

(56) **References Cited**

FIG. 9 is a cross-sectional view taken along line 9—9 in FIG. 2.

U.S. PATENT DOCUMENTS

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The subject matter illustrated in dashed lines in FIGS. 1 through 9 has been shown for illustrative purposes only and forms no part of the claimed design. The dash-dot lines shown in FIGS. 1, 2 and 5-9 define the bounds of the claimed design and form no part thereof.

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1 Claim, 3 Drawing Sheets

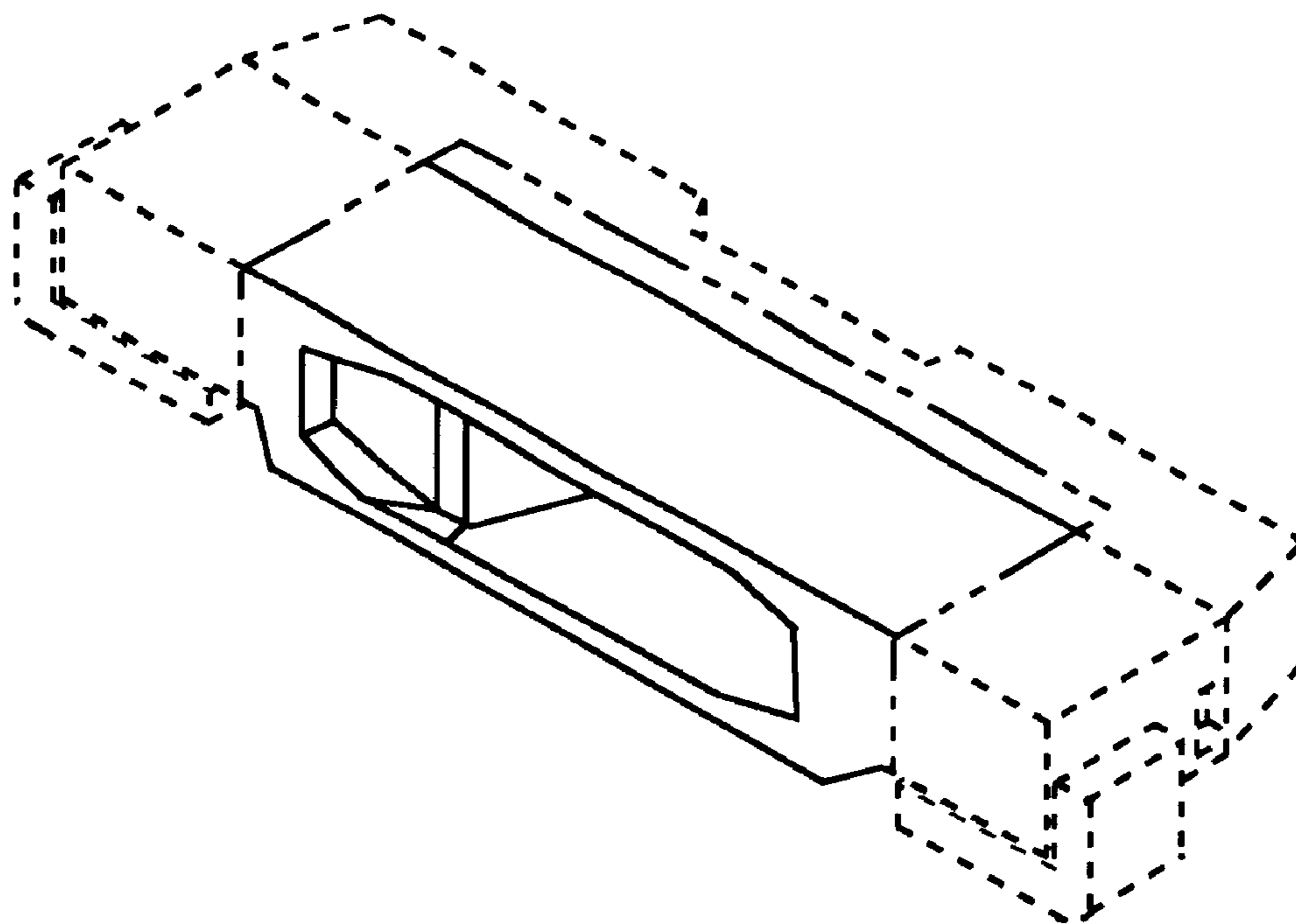


FIG. 1

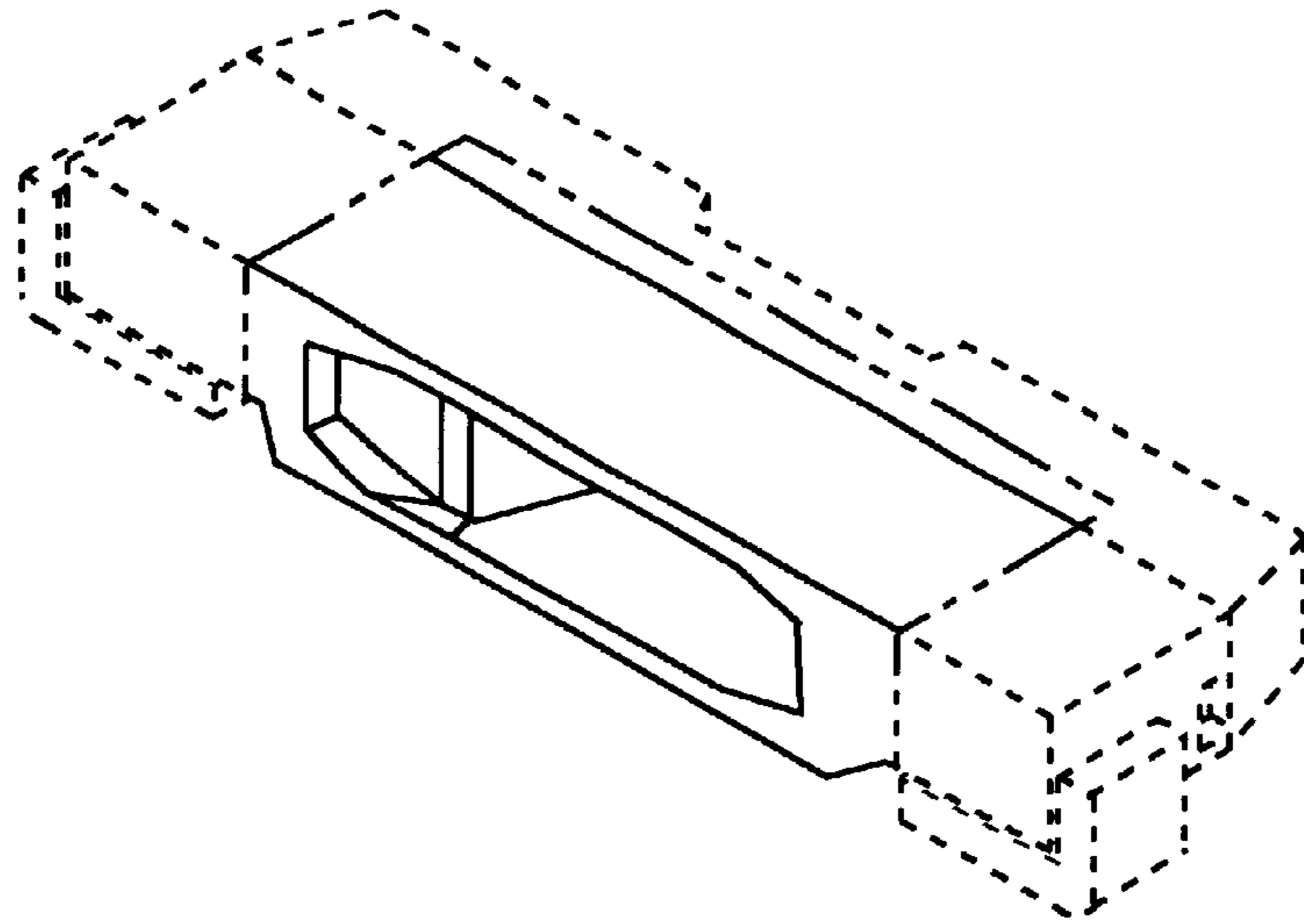


FIG. 2

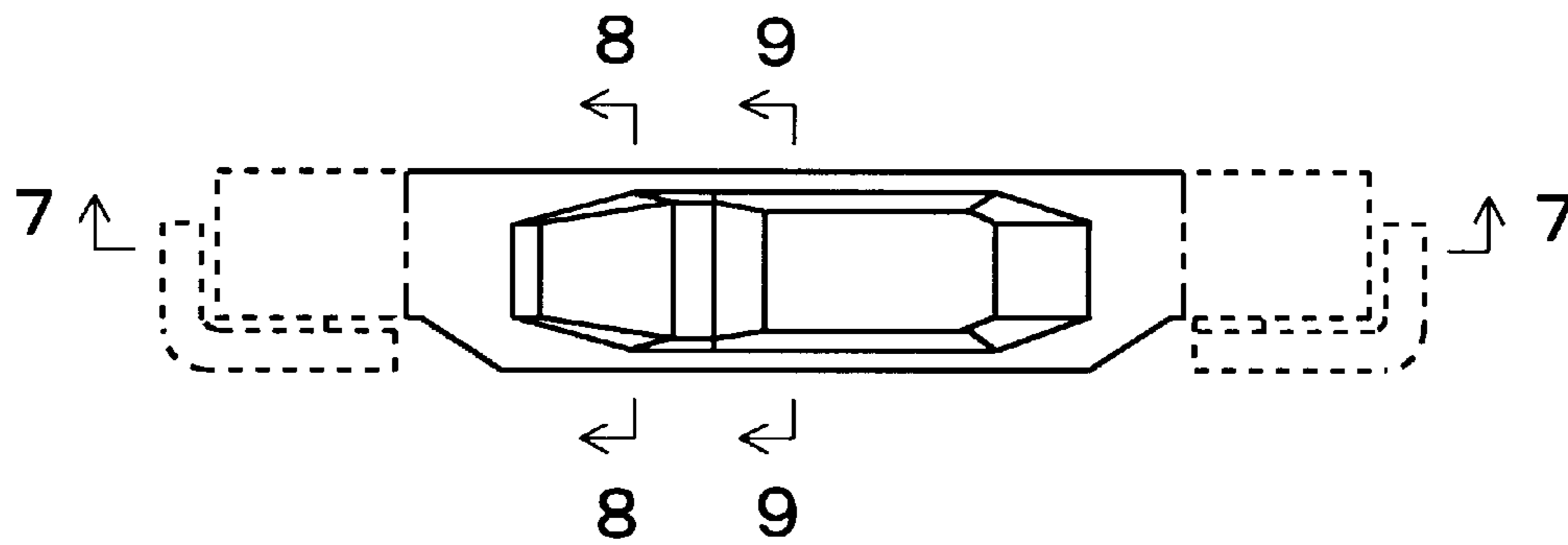


FIG. 3



FIG. 4

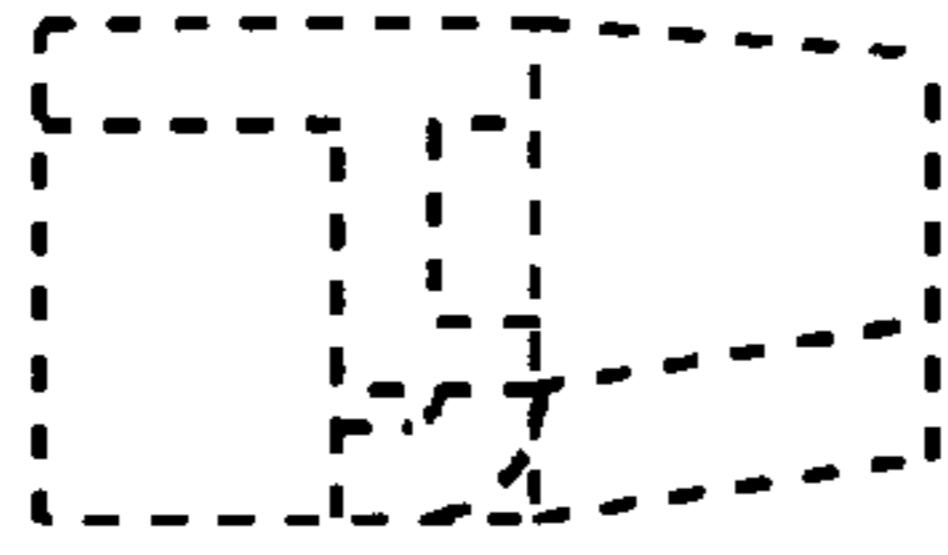


FIG. 5

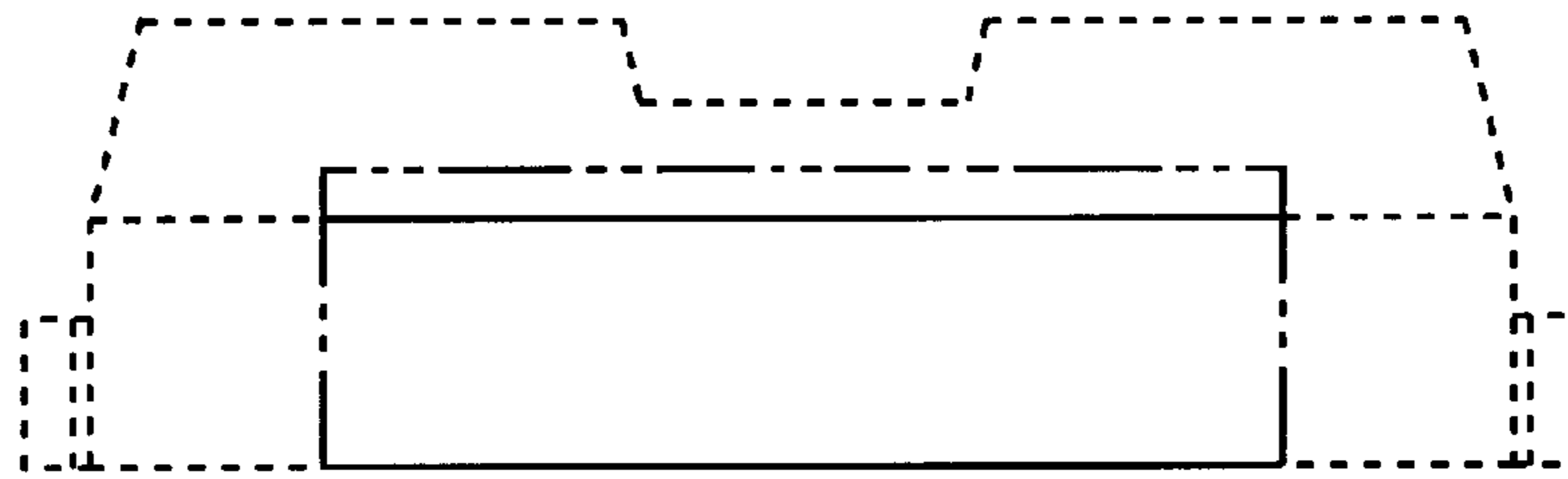


FIG. 6

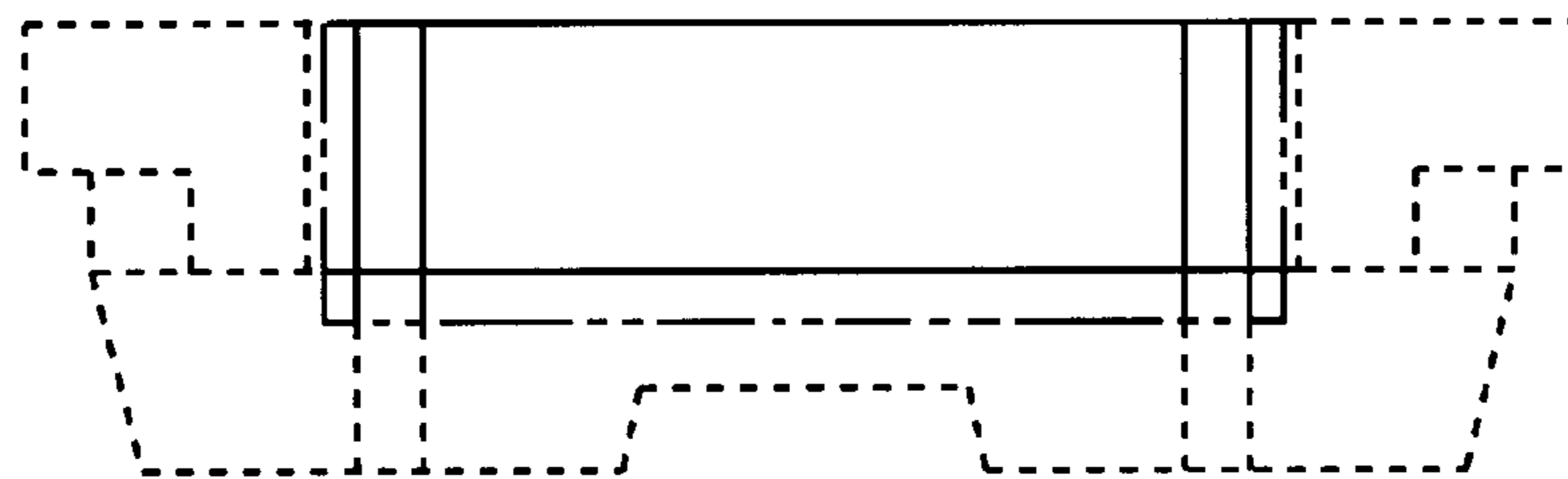


FIG. 7

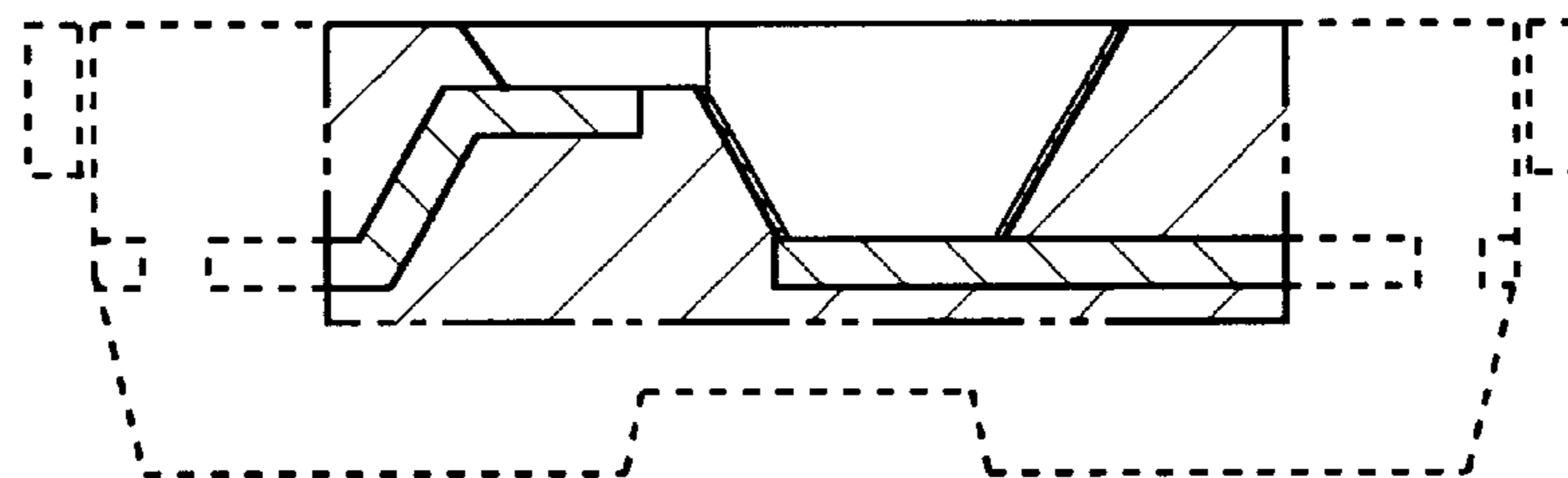


FIG. 8

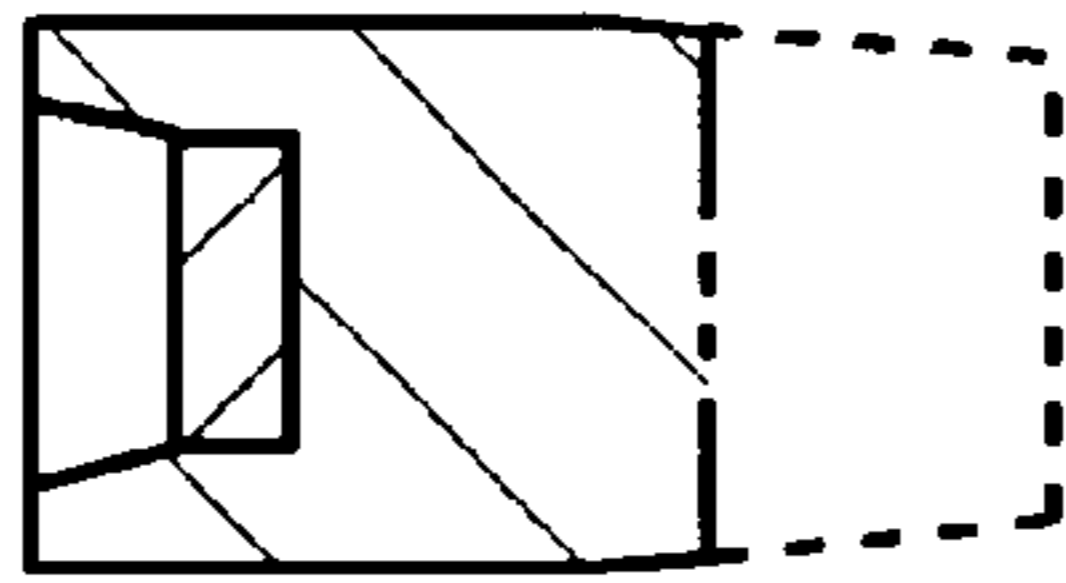


FIG. 9

