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(12) **United States Design Patent**  
**Lee**

(10) **Patent No.:** **US D527,349 S**

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(54) **POWER MODULE FOR PROGRAMMABLE LOGIC CONTROLLER**

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(73) Assignee: **LG Industrial Systems Co., Ltd.**, Seoul (KR)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/203,867**

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(30) **Foreign Application Priority Data**

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(51) **LOC (8) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/162.1**

(58) **Field of Classification Search** ..... D13/162, D13/162.1, 110, 123; 361/679, 686, 687, 361/728, 729, 730, 736; 700/12

See application file for complete search history.

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

D296,432 S \* 6/1988 Harris et al. .... D13/162.1

D302,972 S \* 8/1989 Boucher ..... D13/162.1  
D309,446 S \* 7/1990 Russell ..... D13/162.1  
4,985,803 A \* 1/1991 Pum et al. .... 361/687  
6,594,529 B1 \* 7/2003 Boggs et al. .... 700/12  
D494,132 S \* 8/2004 Song ..... D13/110

\* cited by examiner

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(57) **CLAIM**

I claim the ornamental design for power module for programmable logic controller, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of power module for programmable logic controller showing the new design;

FIG. 2 is a front view thereof;

FIG. 3 is a rear view thereof;

FIG. 4 is a left-side view thereof;

FIG. 5 is a right-side view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

The broken line showing of the environment is for illustrative purpose only and forms no part of the claimed design.

**1 Claim, 5 Drawing Sheets**

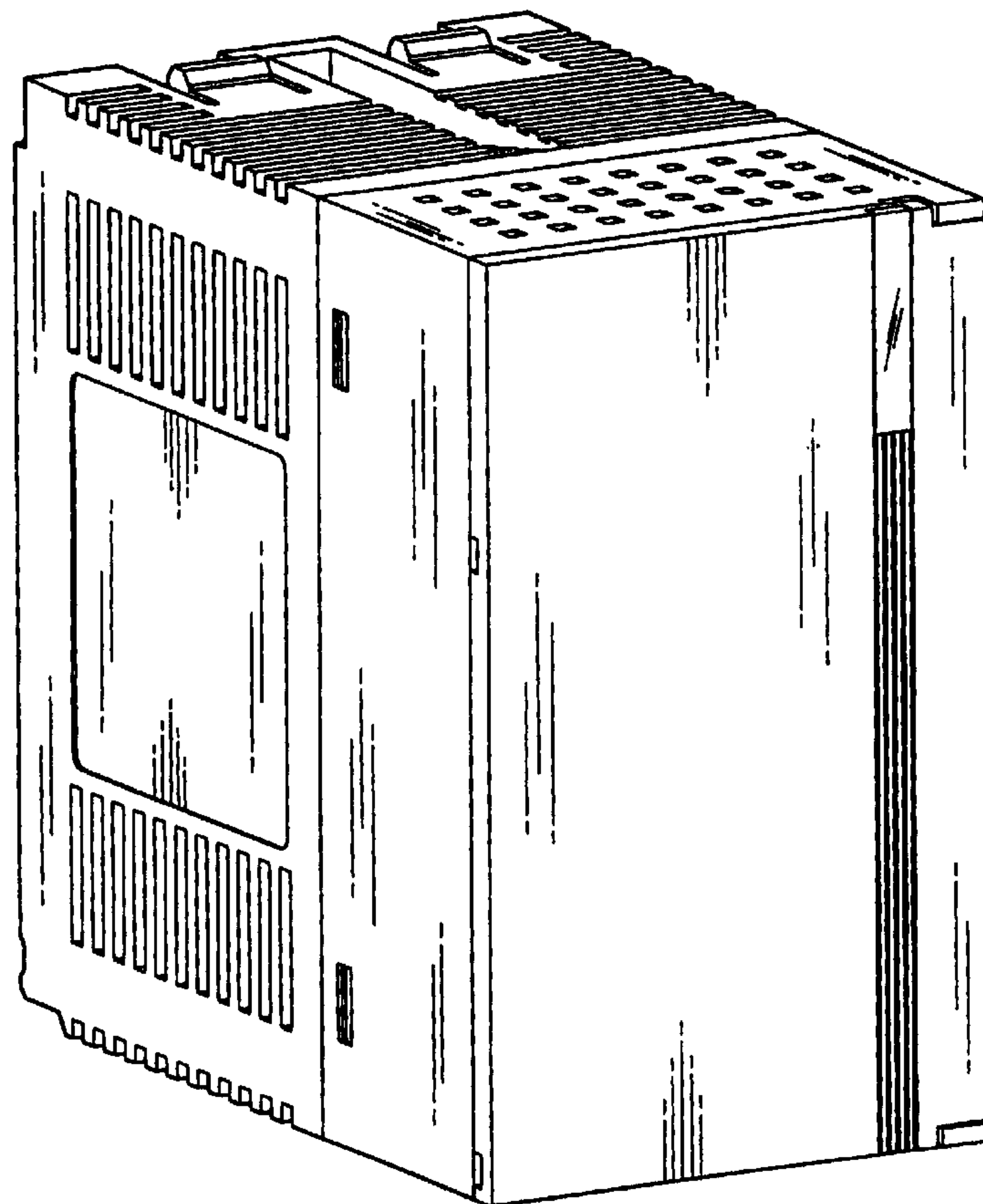


FIG. 1

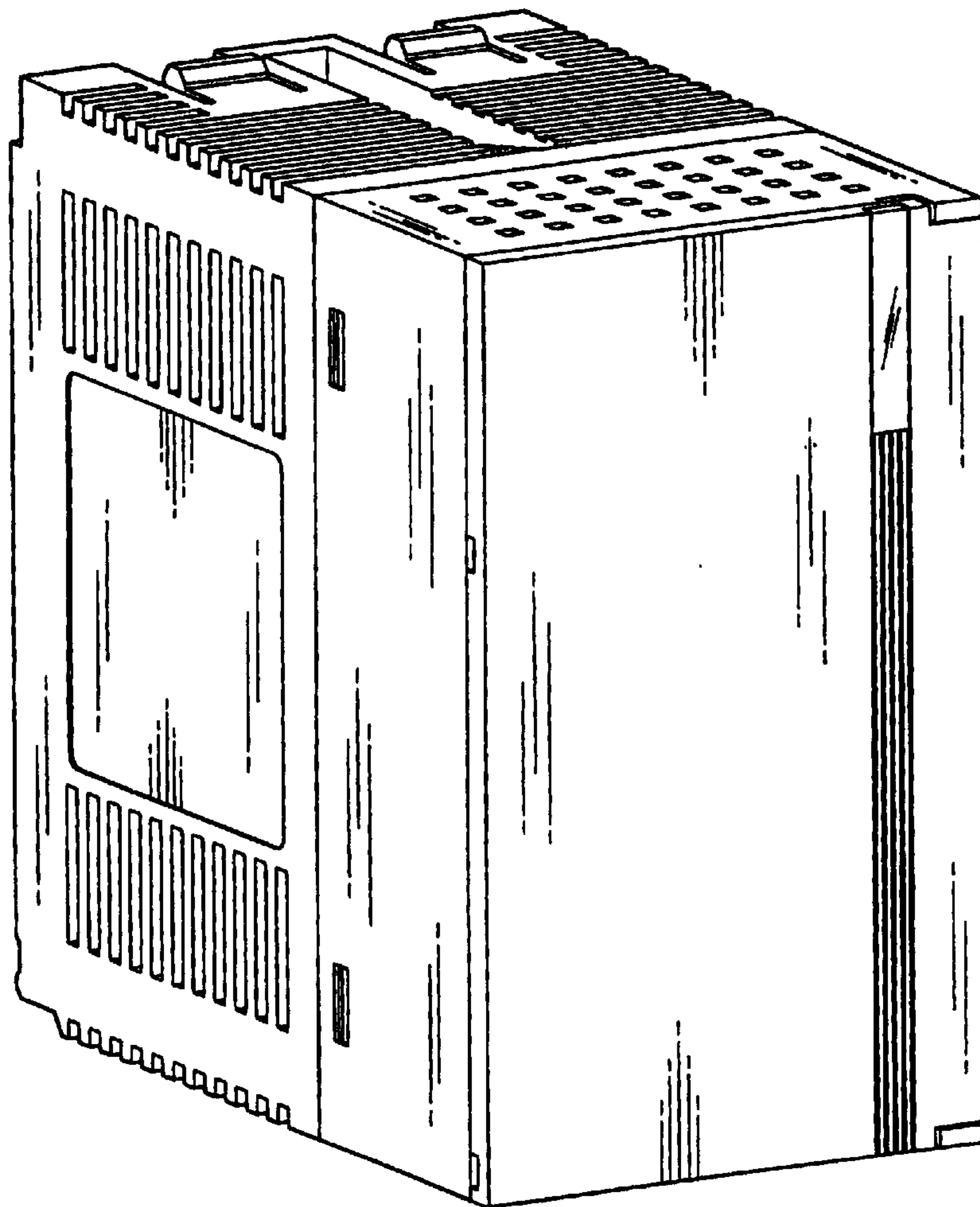


FIG. 2

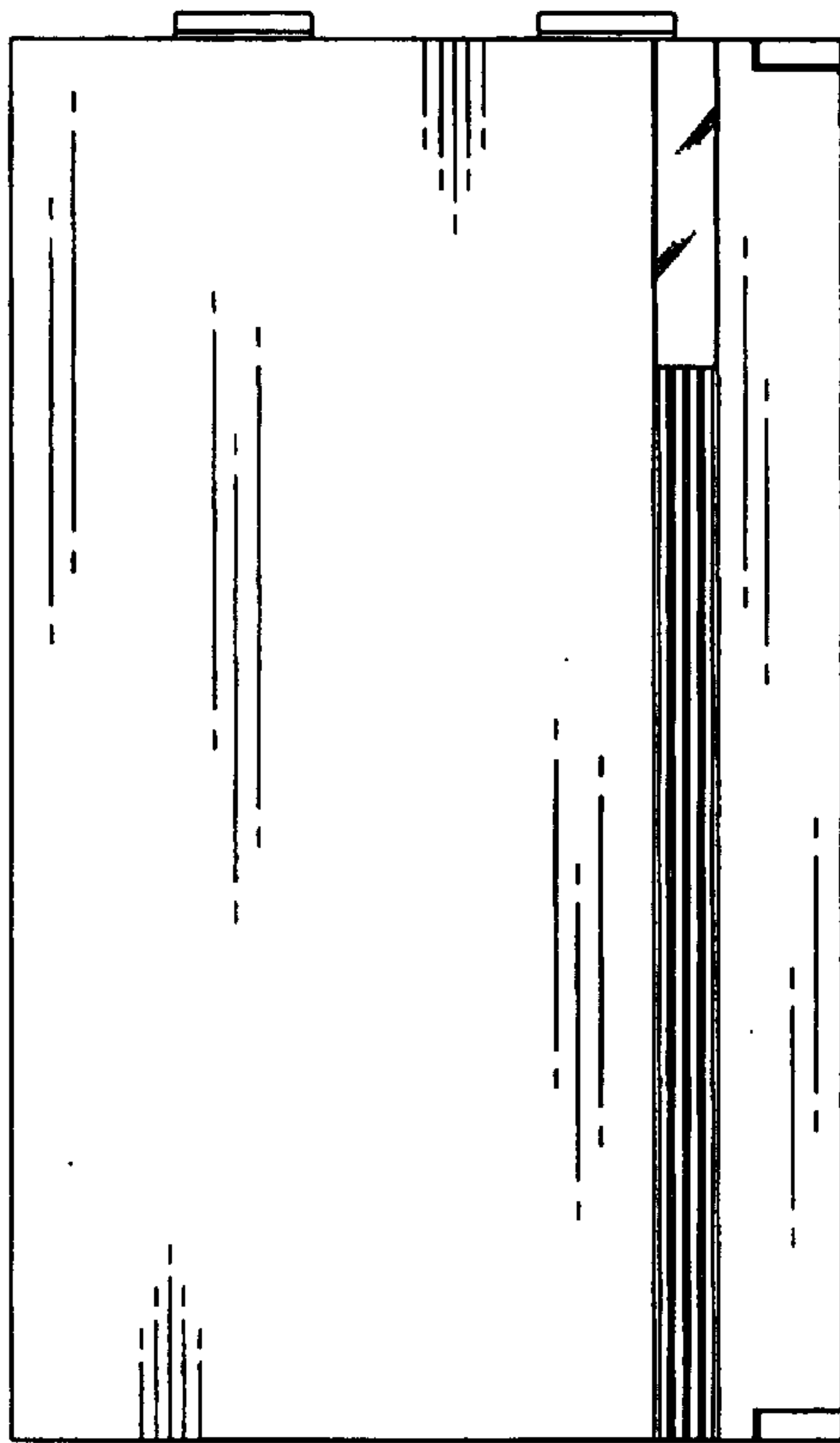


FIG. 3

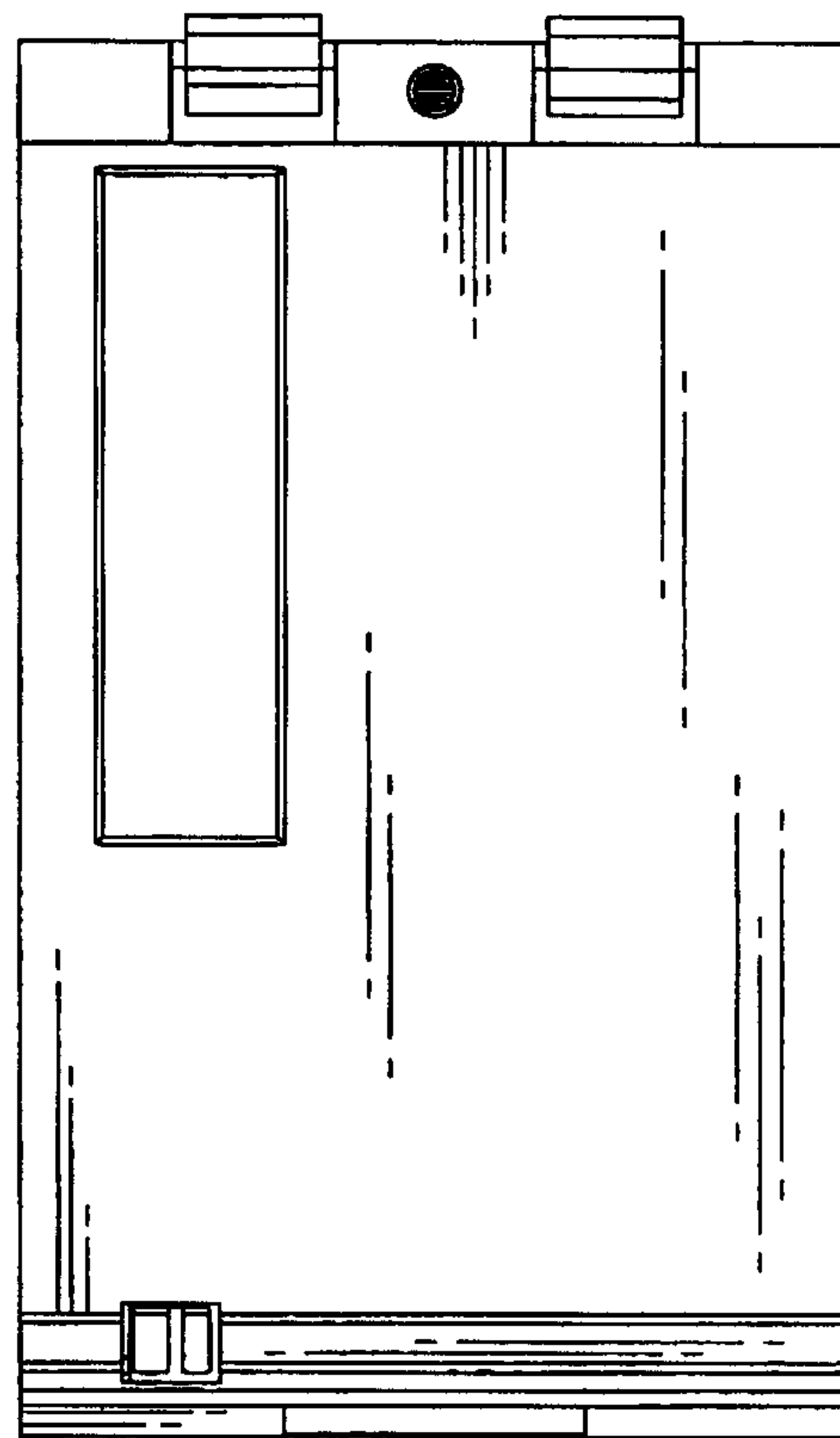


FIG. 4

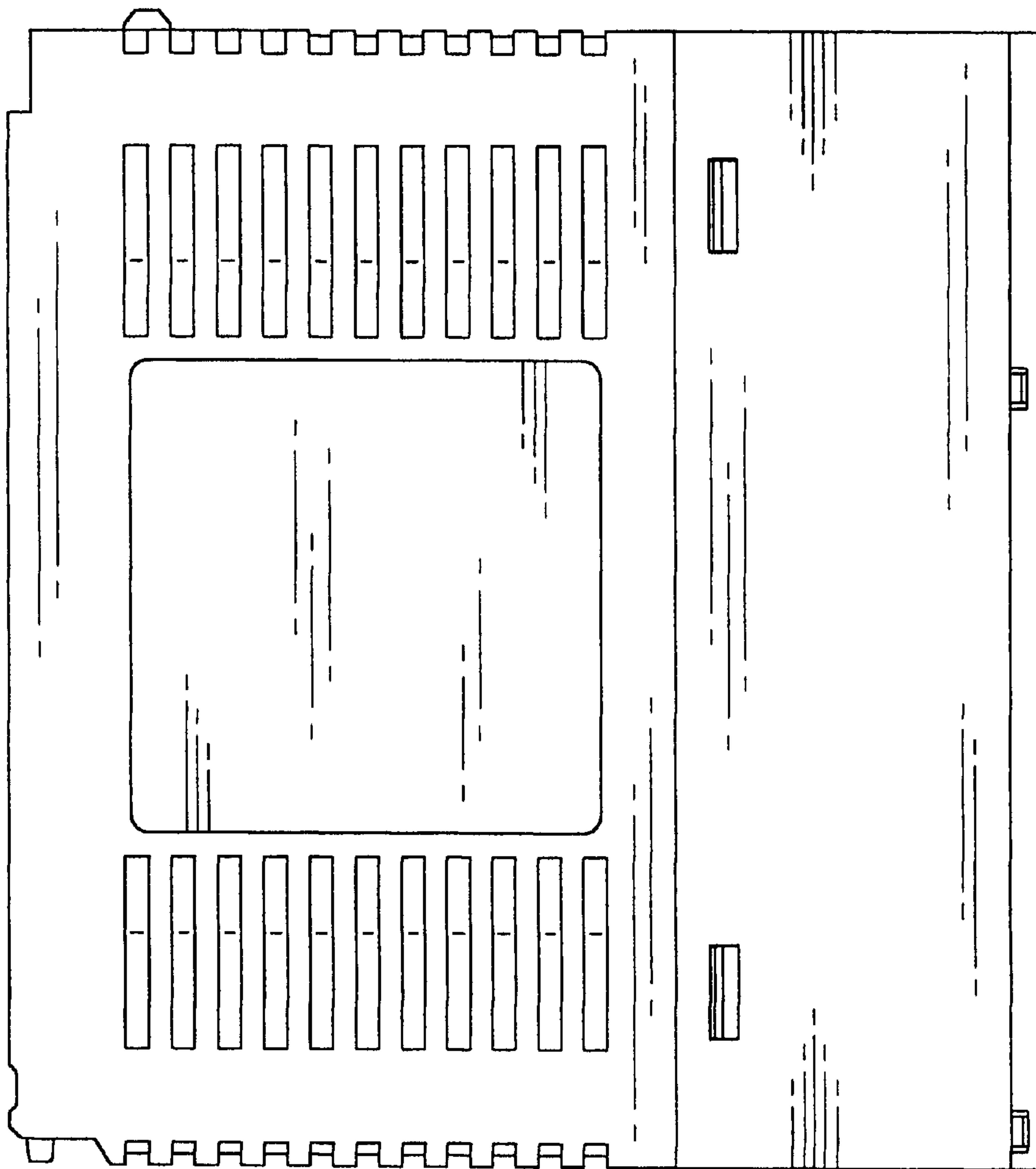


FIG. 5

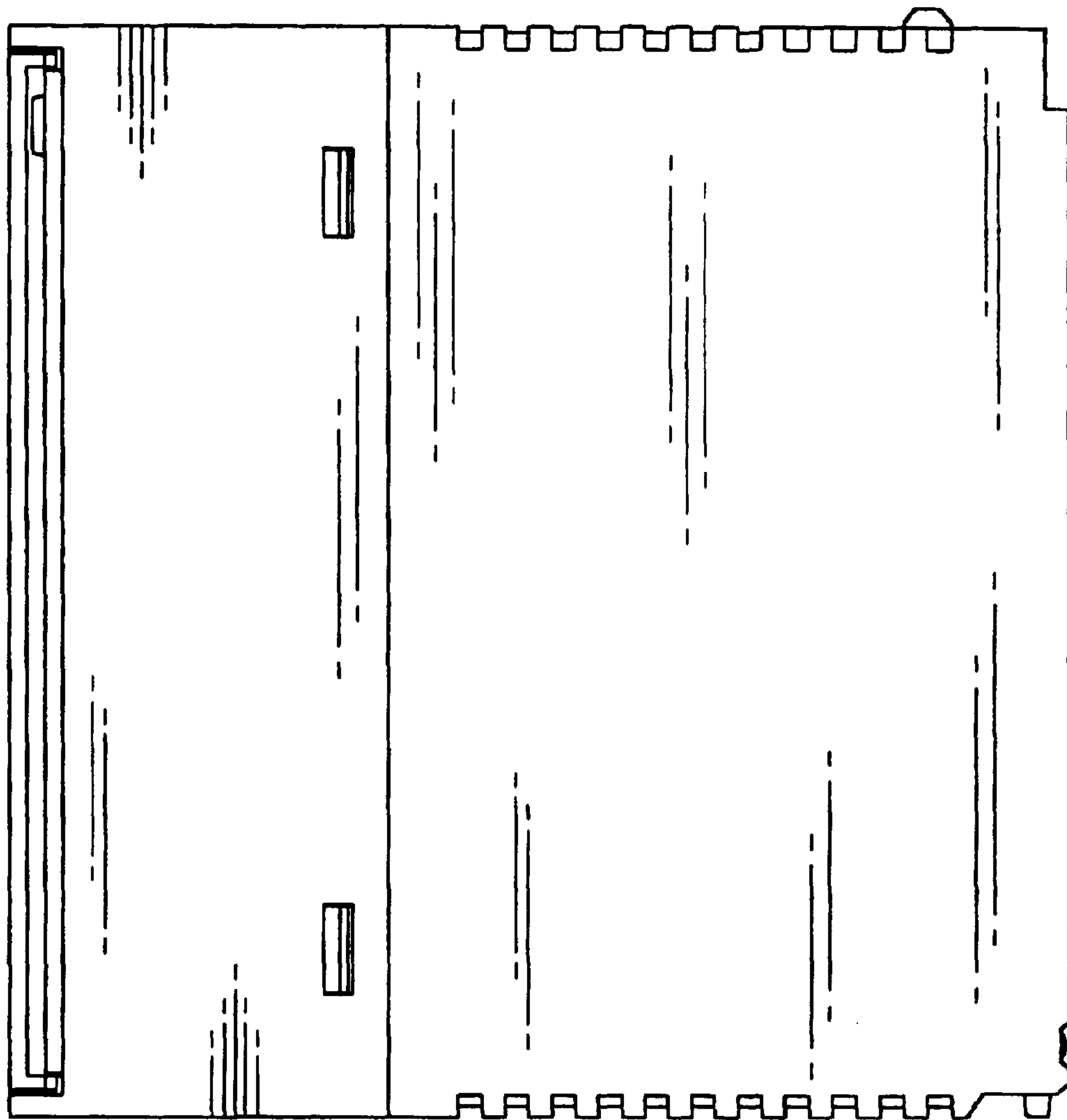


FIG. 6

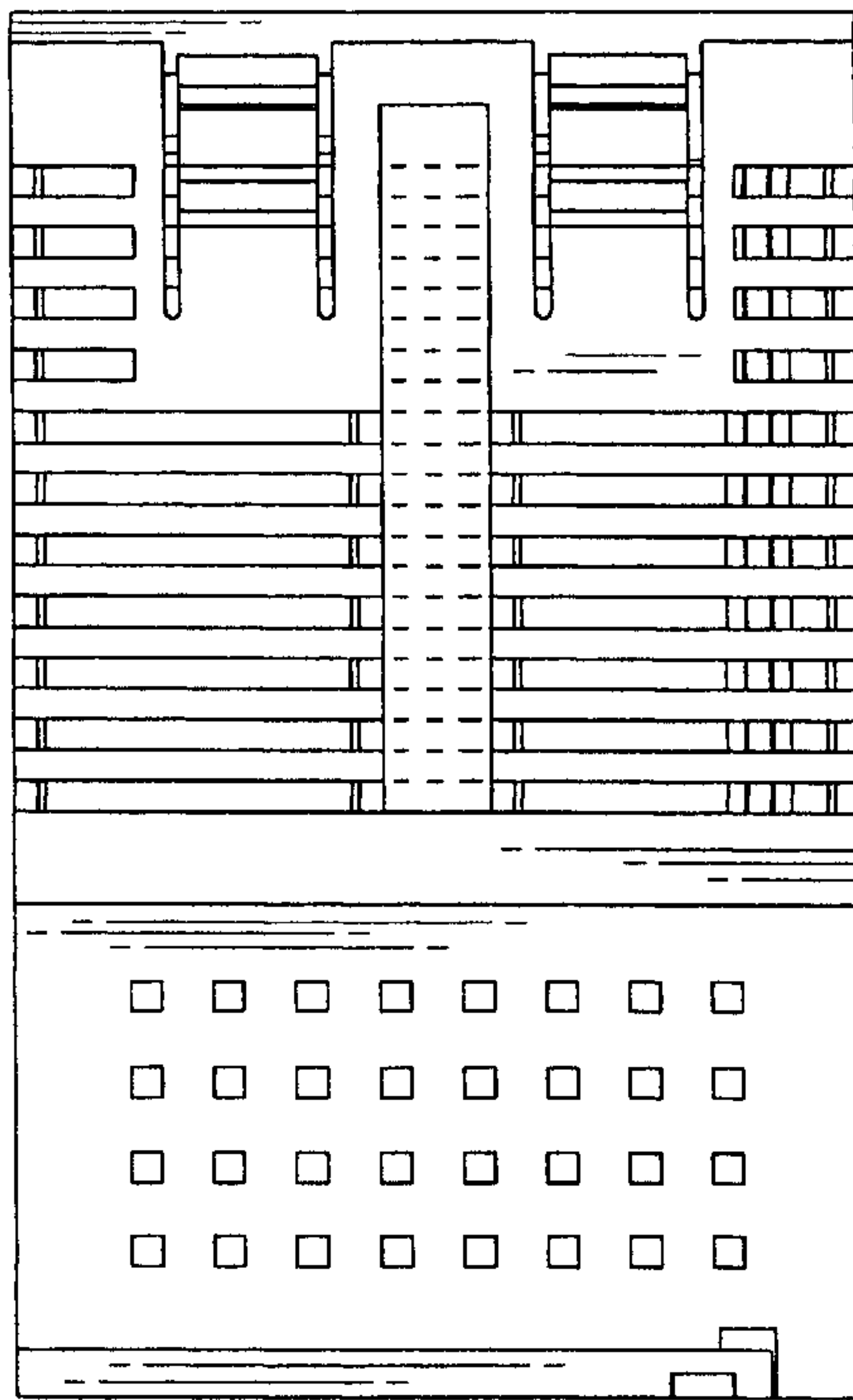


FIG. 7

