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(12) **United States Design Patent**
Kamada

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(54) **LIGHT EMITTING DIODE**

6,501,103 B1 12/2002 Jory et al.
6,541,800 B1 4/2003 Barnett et al.

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FOREIGN PATENT DOCUMENTS

(73) Assignee: **Nichia Corporation**, Anan (JP)

JP	1046442	8/1999
JP	1046565	8/1999
JP	1046442-1	8/1999
JP	1046442-2	8/1999
JP	1046565-1	8/1999
JP	1046565-2	8/1999

(**) Term: **14 Years**

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Related U.S. Application Data

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(30) **Foreign Application Priority Data**

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(51) **LOC (8) Cl.** **13-03**

(52) **U.S. Cl.** **D13/180**

(58) **Field of Classification Search** D13/104,
D13/180, 182; 257/88, 89, 98, 99; 313/498,
313/499, 506, 512; 362/800

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

4,316,208 A	2/1982	Kobayashi et al.
4,791,290 A	12/1988	Noone et al.
5,266,817 A	11/1993	Lin
6,084,252 A	7/2000	Isokawa et al.
6,429,464 B1	8/2002	Lin

(57) **CLAIM**

The ornamental design for a light emitting diode, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a light emitting diode showing my new design; FIG. 2 is a front elevational view thereof; the rear elevational view being a mirror image thereof; FIG. 3 is a top plan view thereof; FIG. 4 is a bottom plan view thereof; FIG. 5 is a left side elevational view thereof; the right side elevational view being a mirror image thereof; and, FIG. 6 is a sectional view taken along a line VI—VI in FIG. 3.

“The broken lines are shown in the views for illustrative purpose only, and form no part of the claimed design.”

1 Claim, 5 Drawing Sheets

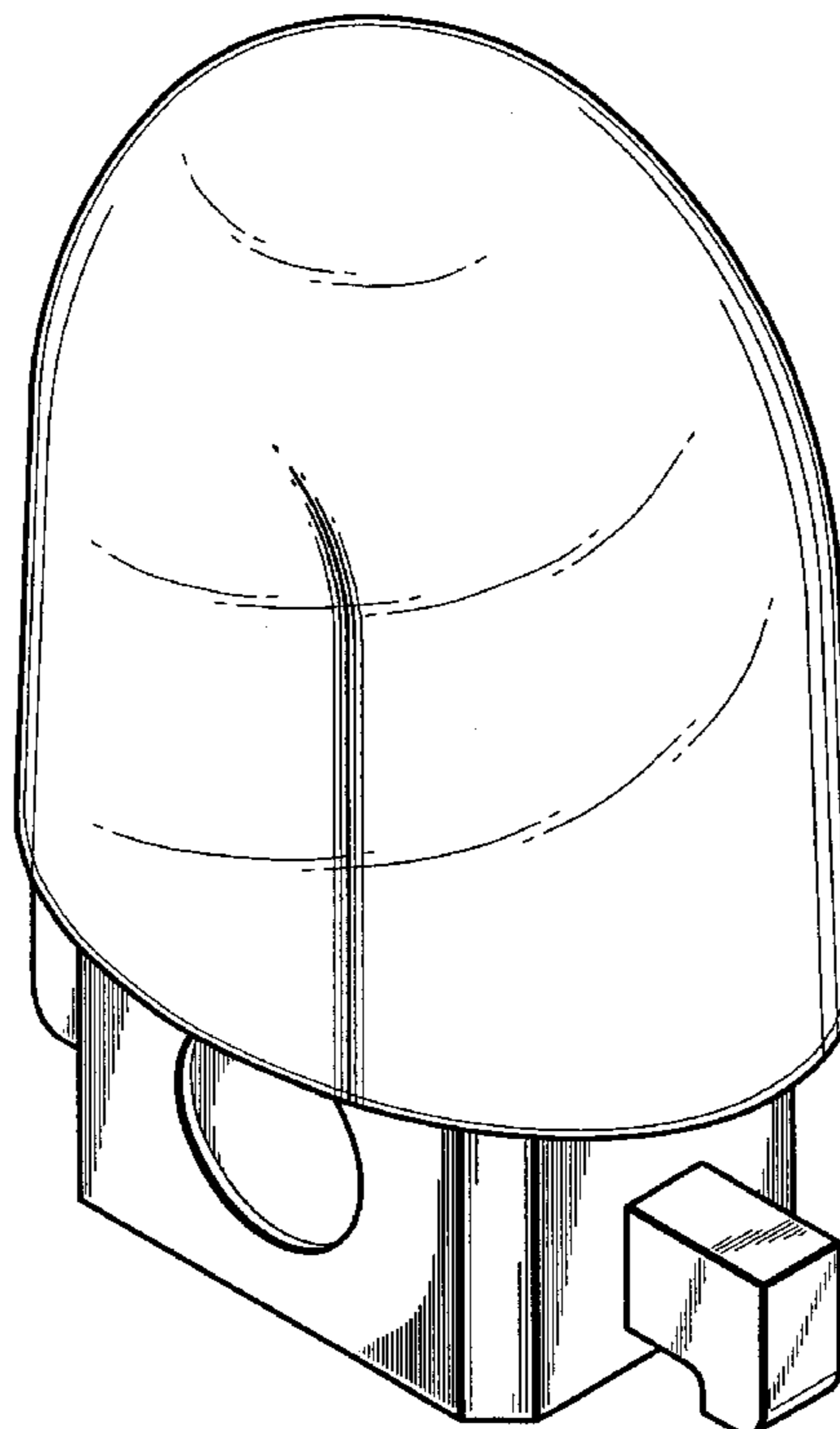


FIG. 1

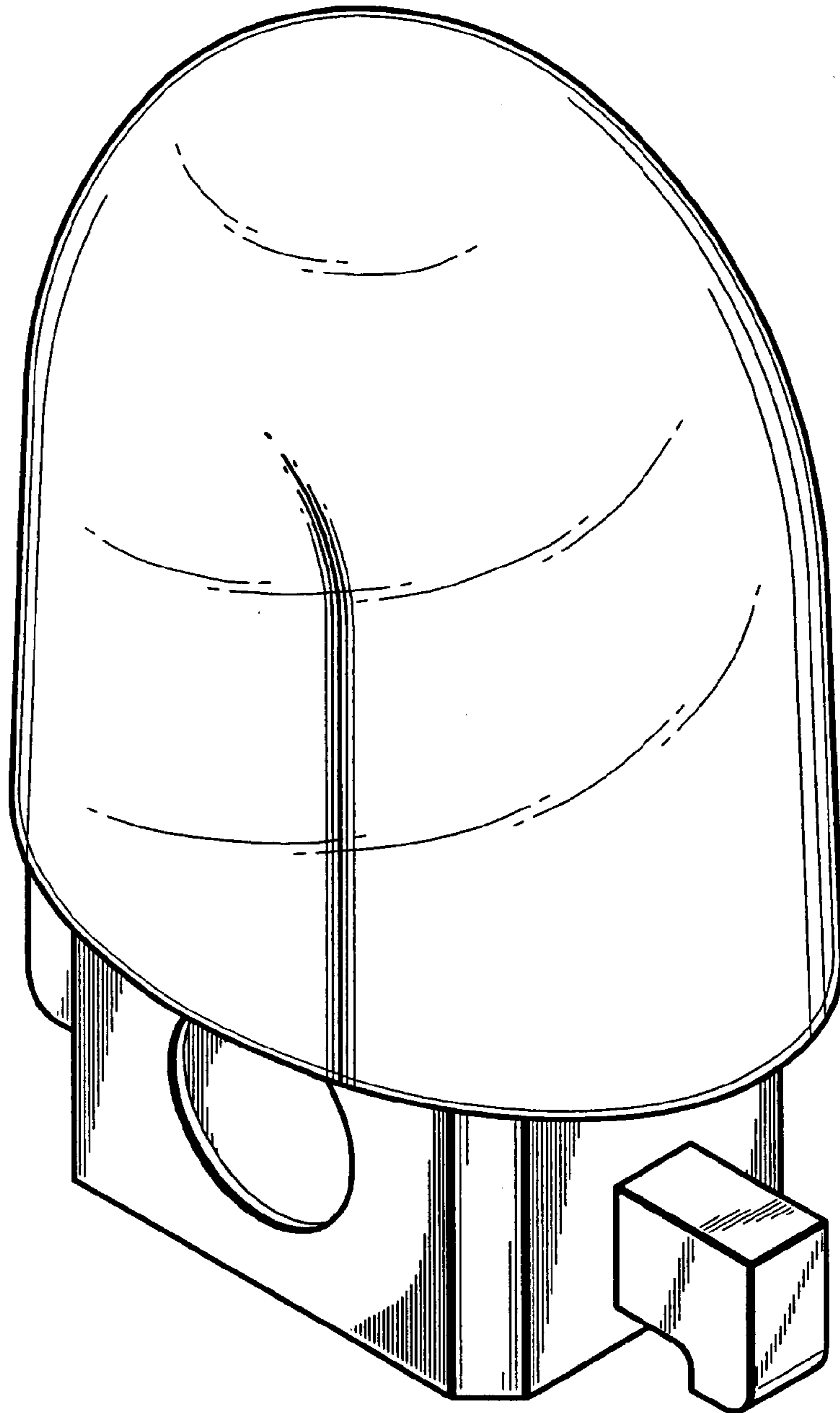


FIG. 2

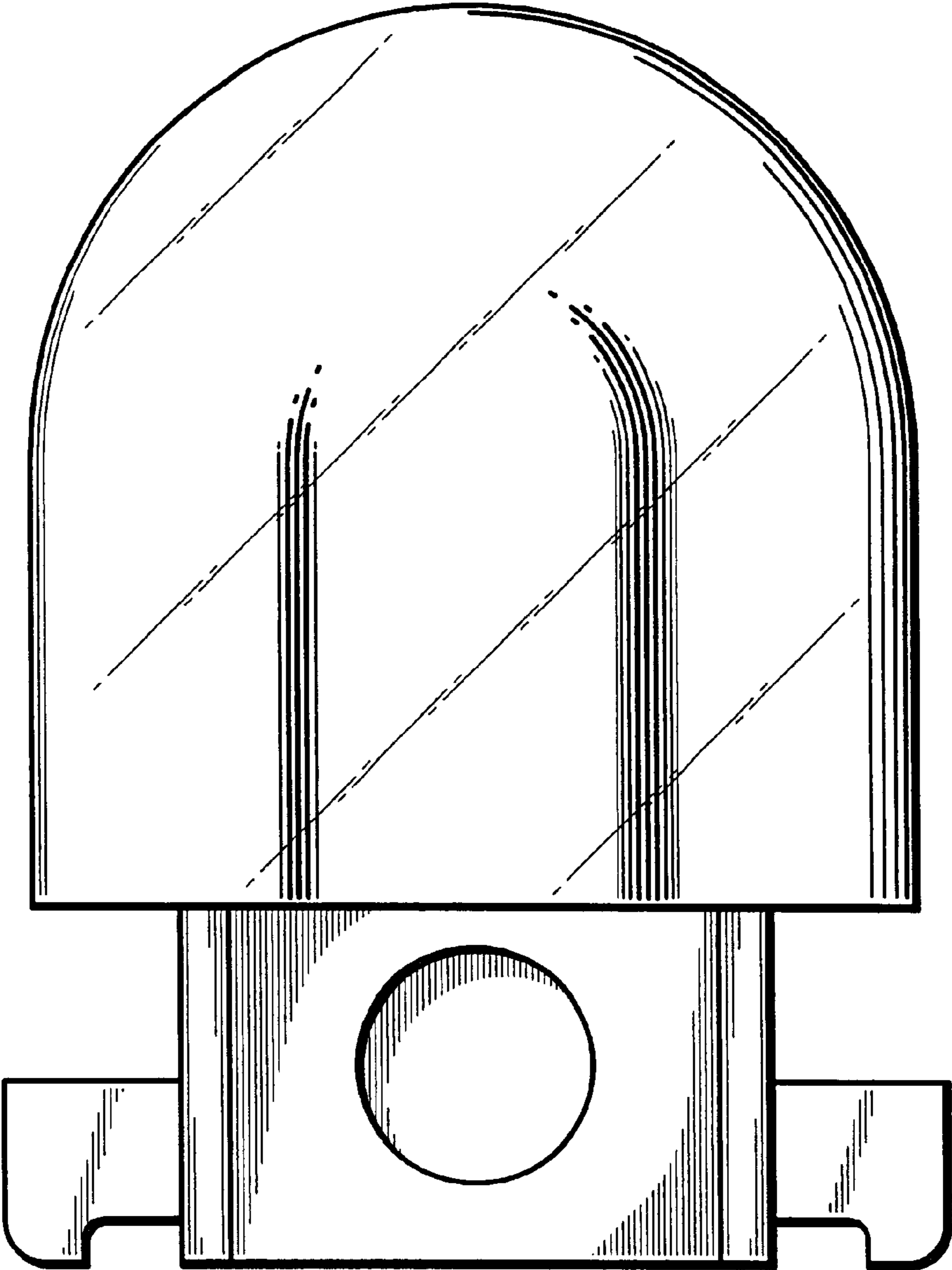


FIG. 3

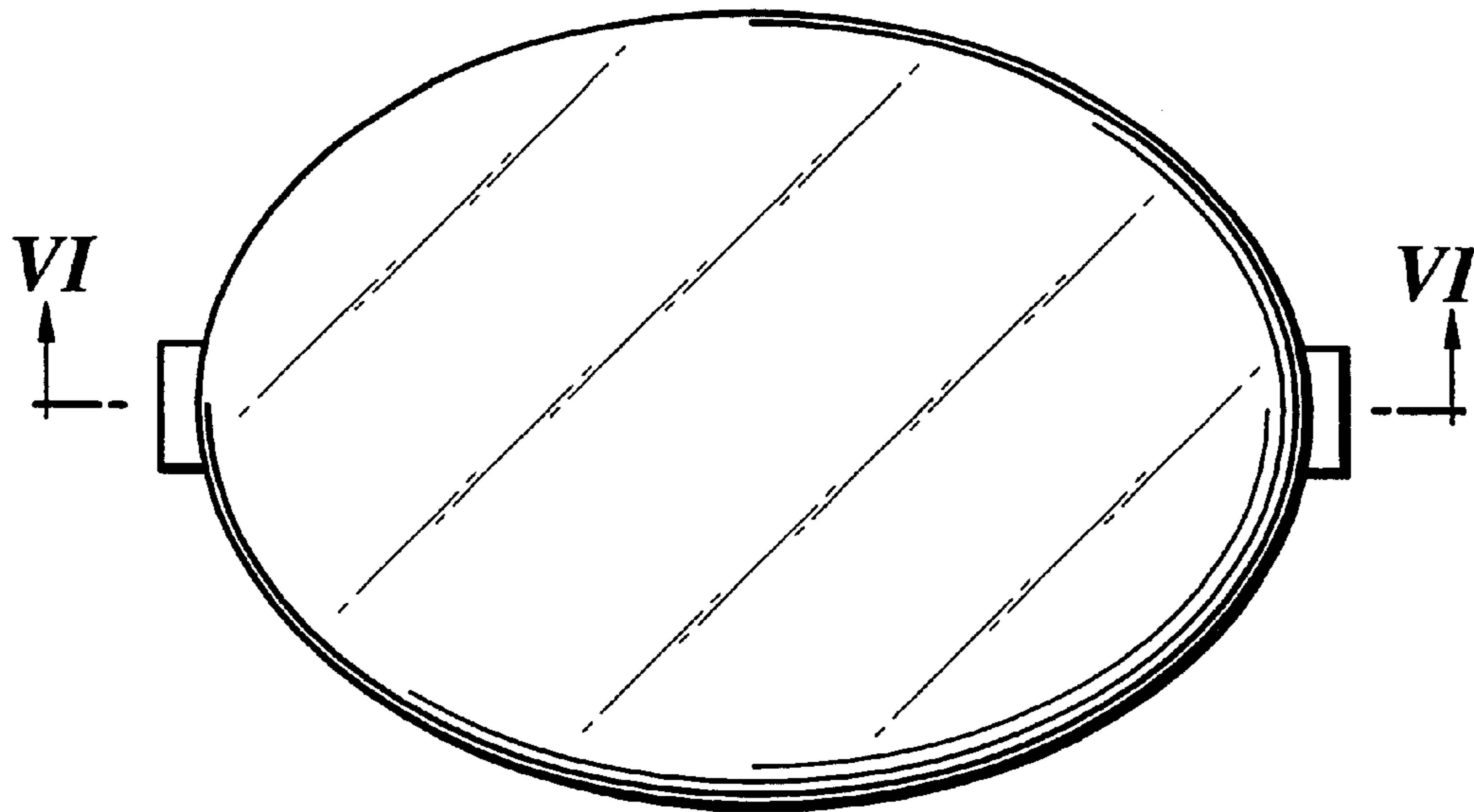


FIG. 4

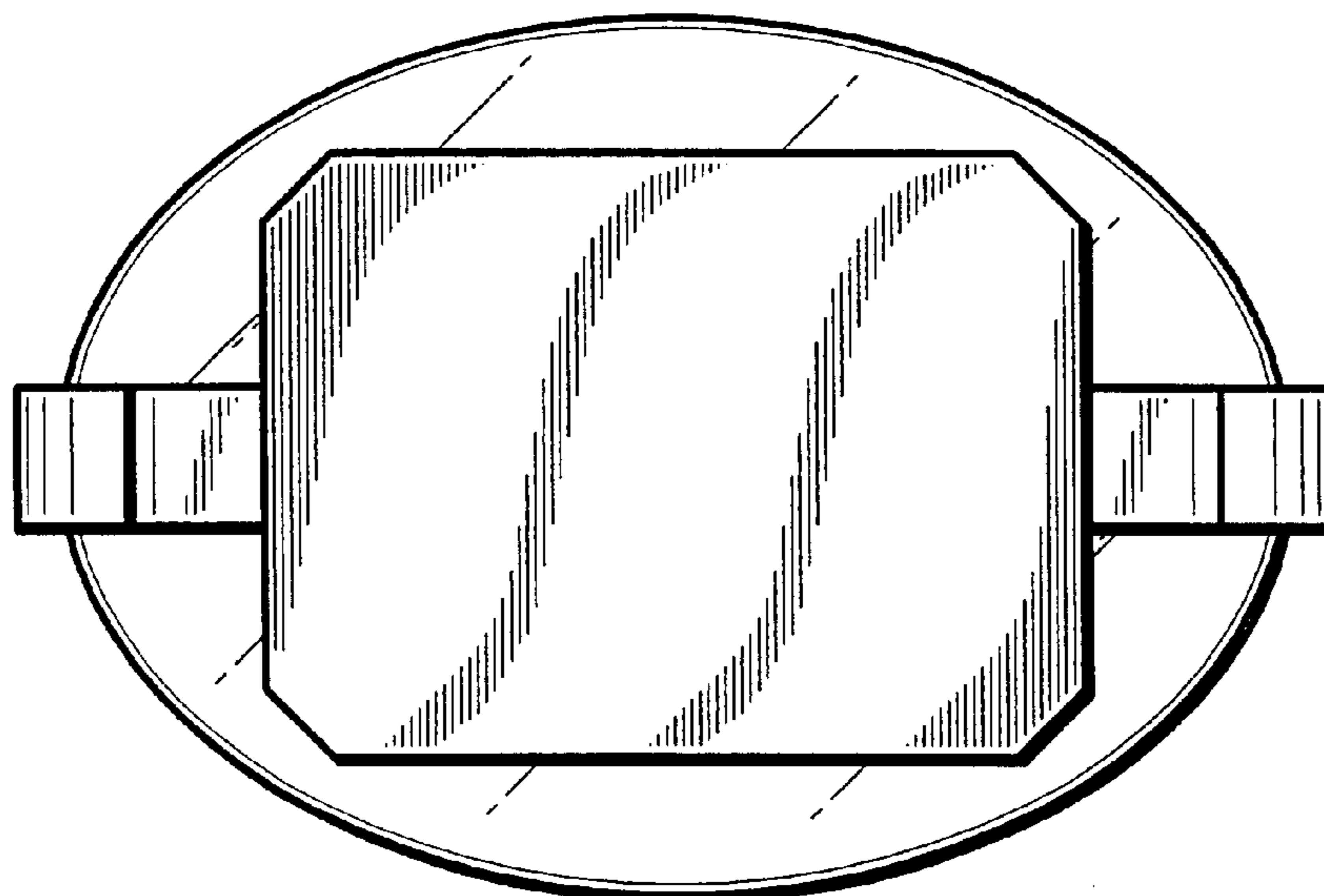


FIG. 5

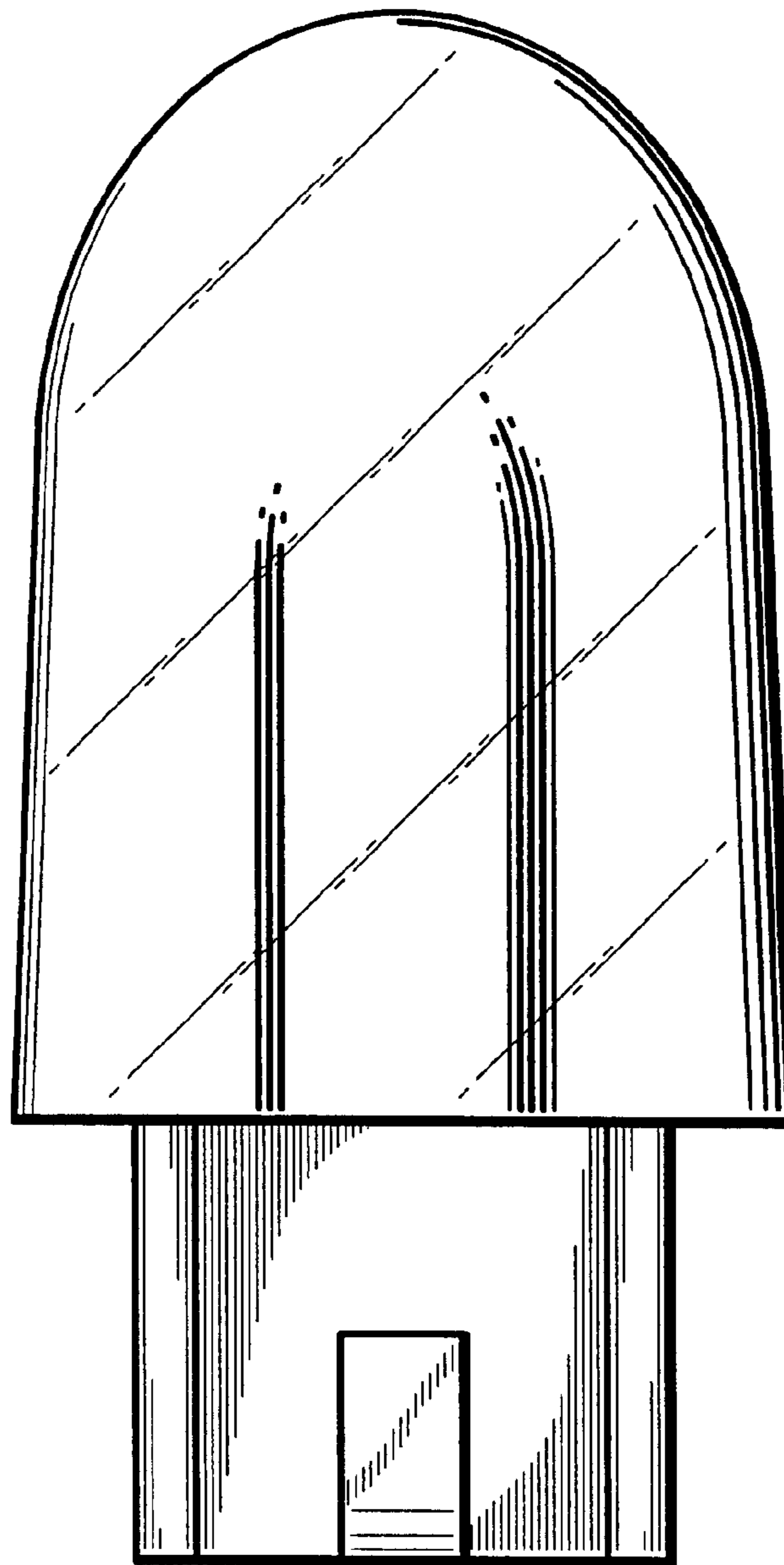


FIG. 6

