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(12) **United States Design Patent** (10) **Patent No.:** **US D525,594 S**
Bailey et al. (45) **Date of Patent:** **** Jul. 25, 2006**

(54) **ELECTRONICS ENCLOSURE**

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(**) Term: **14 Years**

(21) Appl. No.: **29/222,712**

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(51) **LOC (8) Cl.** **13-03**

(52) **U.S. Cl.** **D13/184**

(58) **Field of Classification Search** D13/152,
D13/154, 156, 177, 184; D14/240; 174/48,
174/50, 52.1, 58, 66, 67; 220/3.2, 3.3, 3.8,
220/4.02, 242; 361/679, 796
See application file for complete search history.

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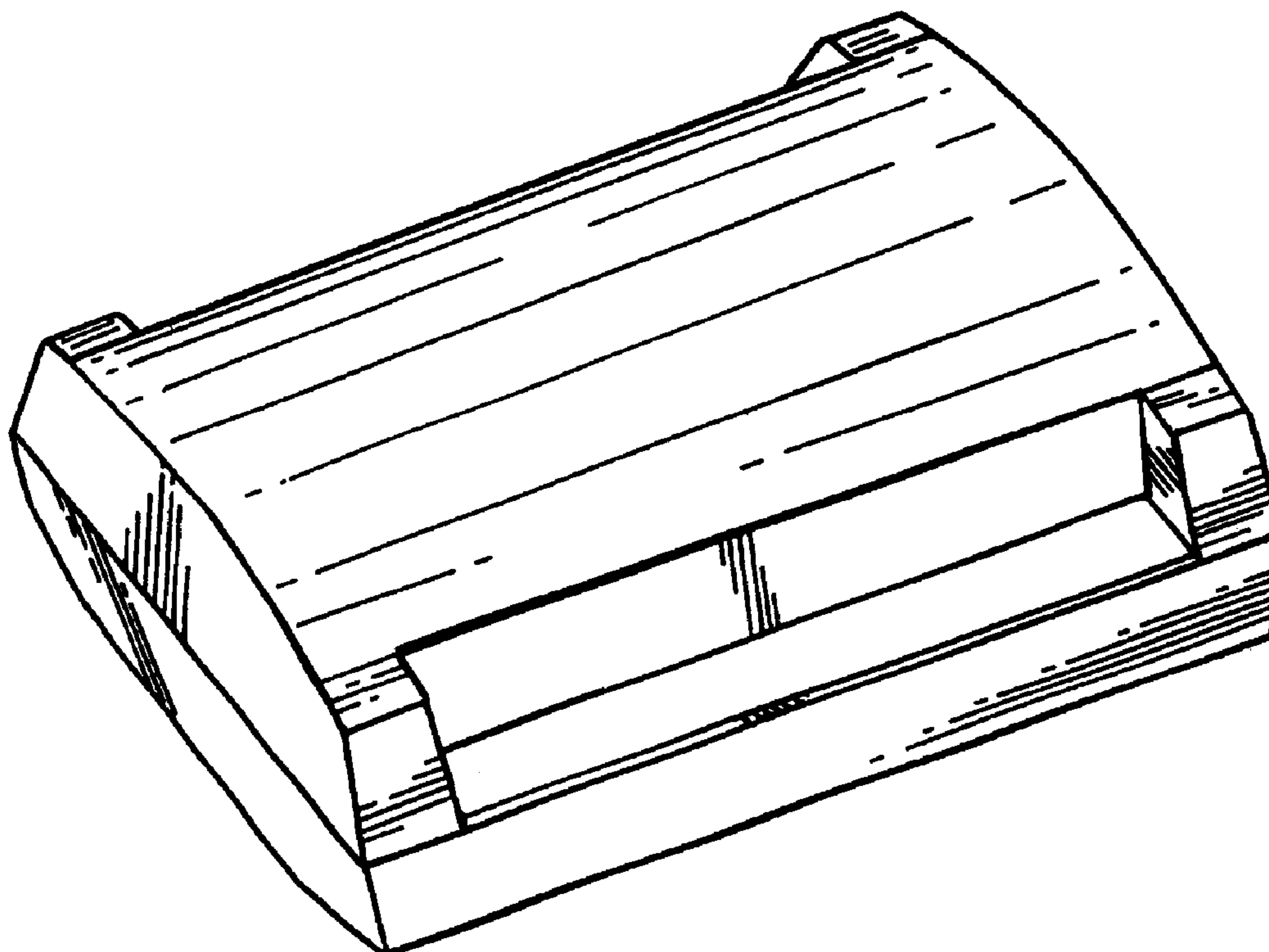
(57) **CLAIM**

The ornamental design for a electronics enclosure, substan-
tially as shown.

DESCRIPTION

FIG. 1 is an isometric view from the front of a electronics
enclosure incorporating the design;
FIG. 2 is a top view thereof;
FIG. 3 is a bottom view thereof;
FIG. 4 is a front view thereof;
FIG. 5 is a back view view thereof;
FIG. 6 is a left-side view thereof; and,
FIG. 7 is a right side view thereof.

1 Claim, 2 Drawing Sheets



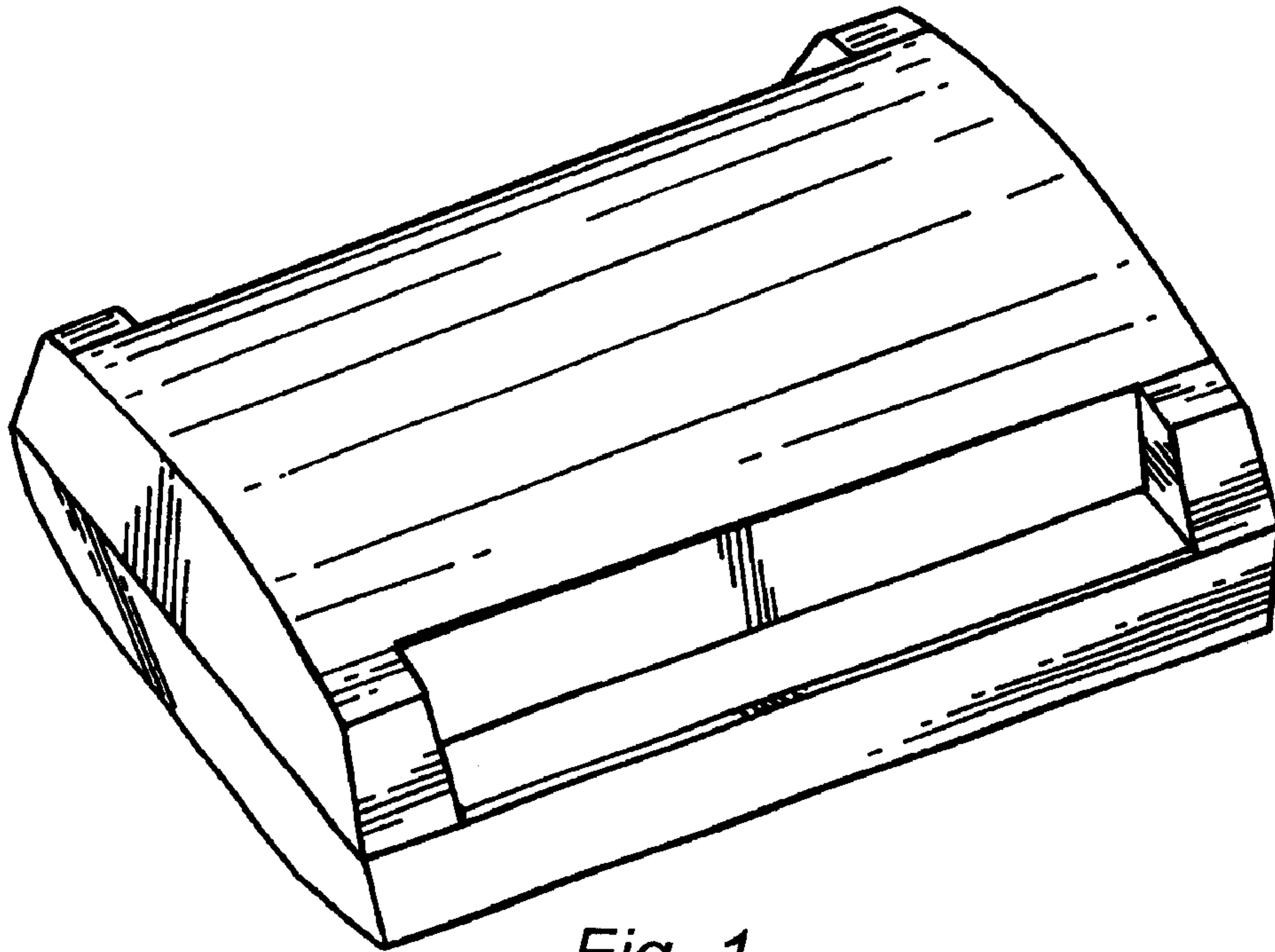


Fig. 1

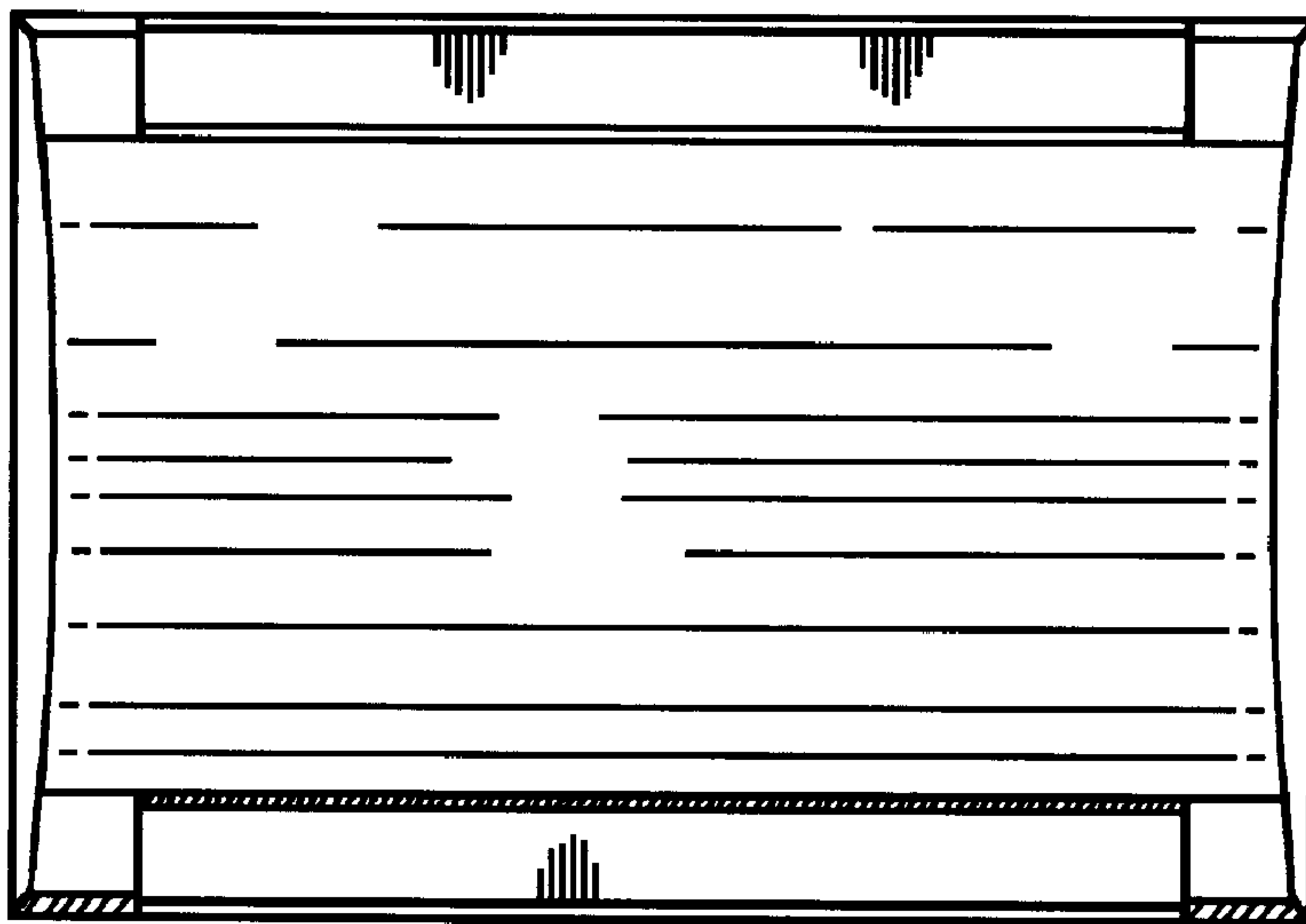


Fig. 2

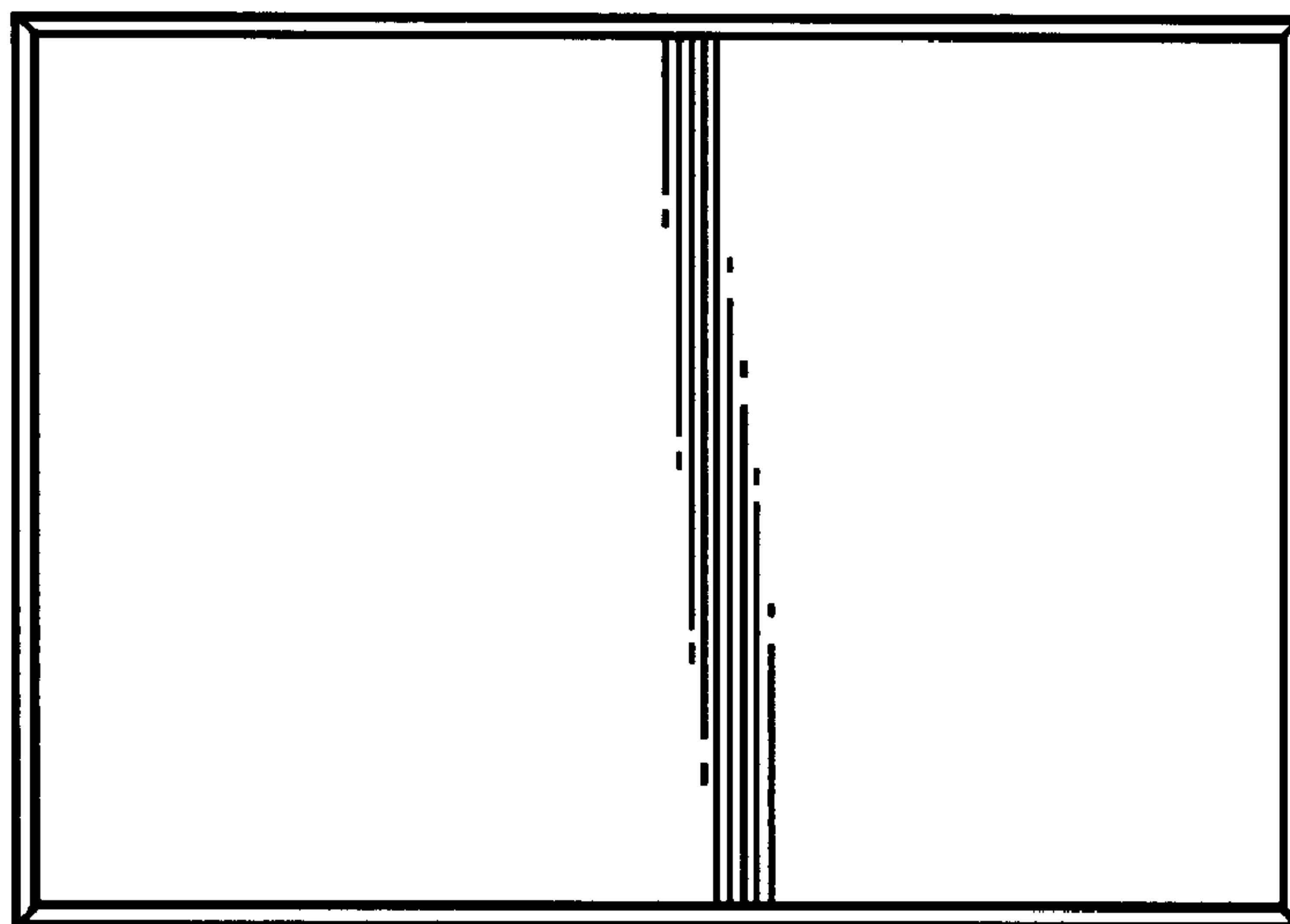


Fig. 3

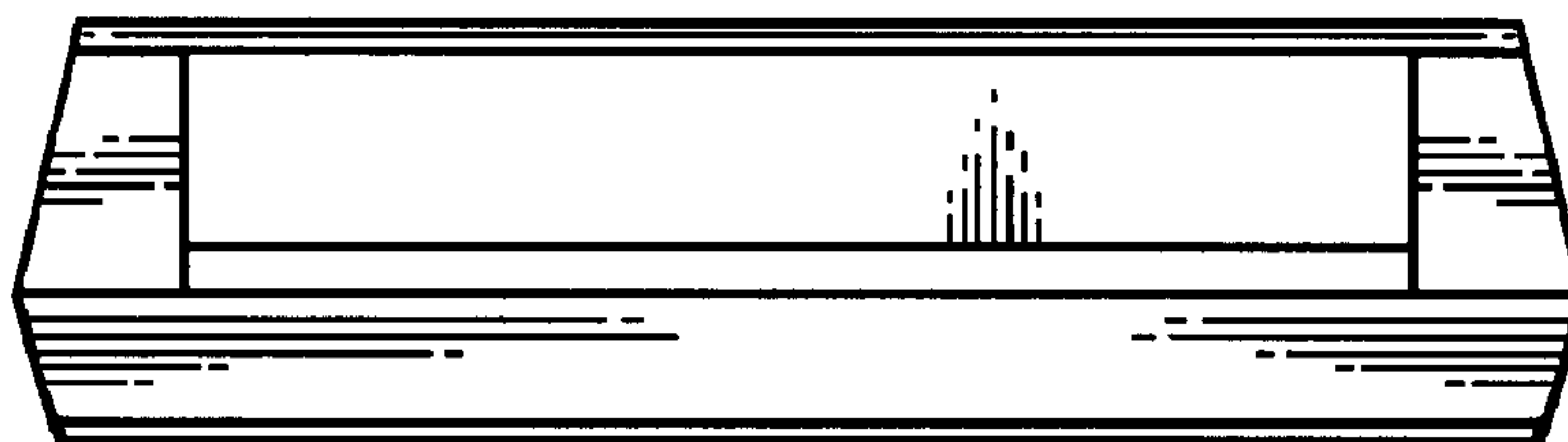


Fig. 4

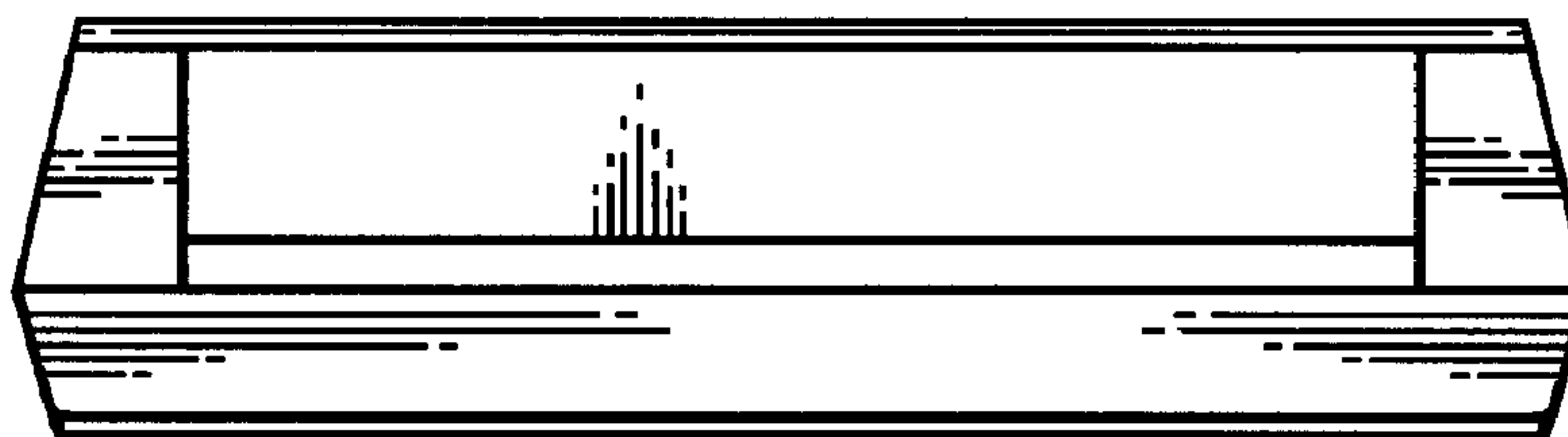


Fig. 5

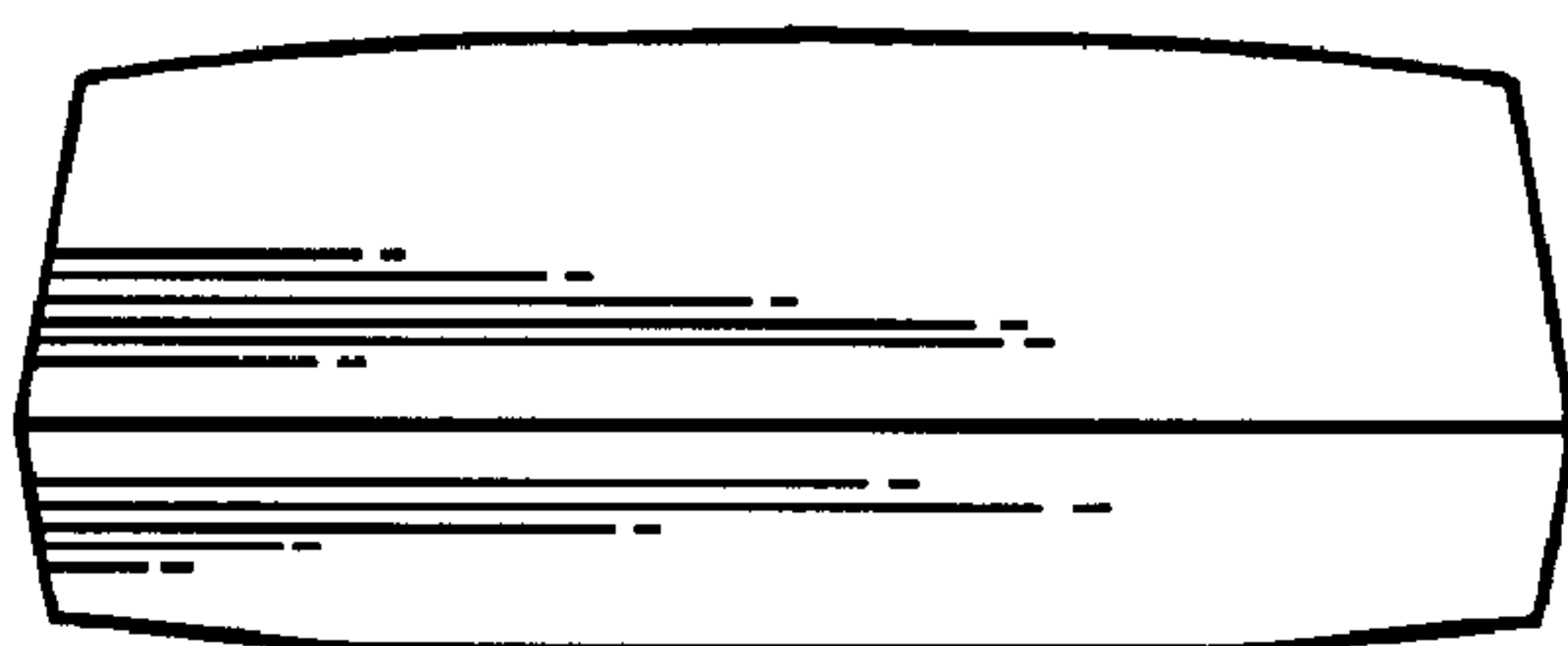


Fig. 6

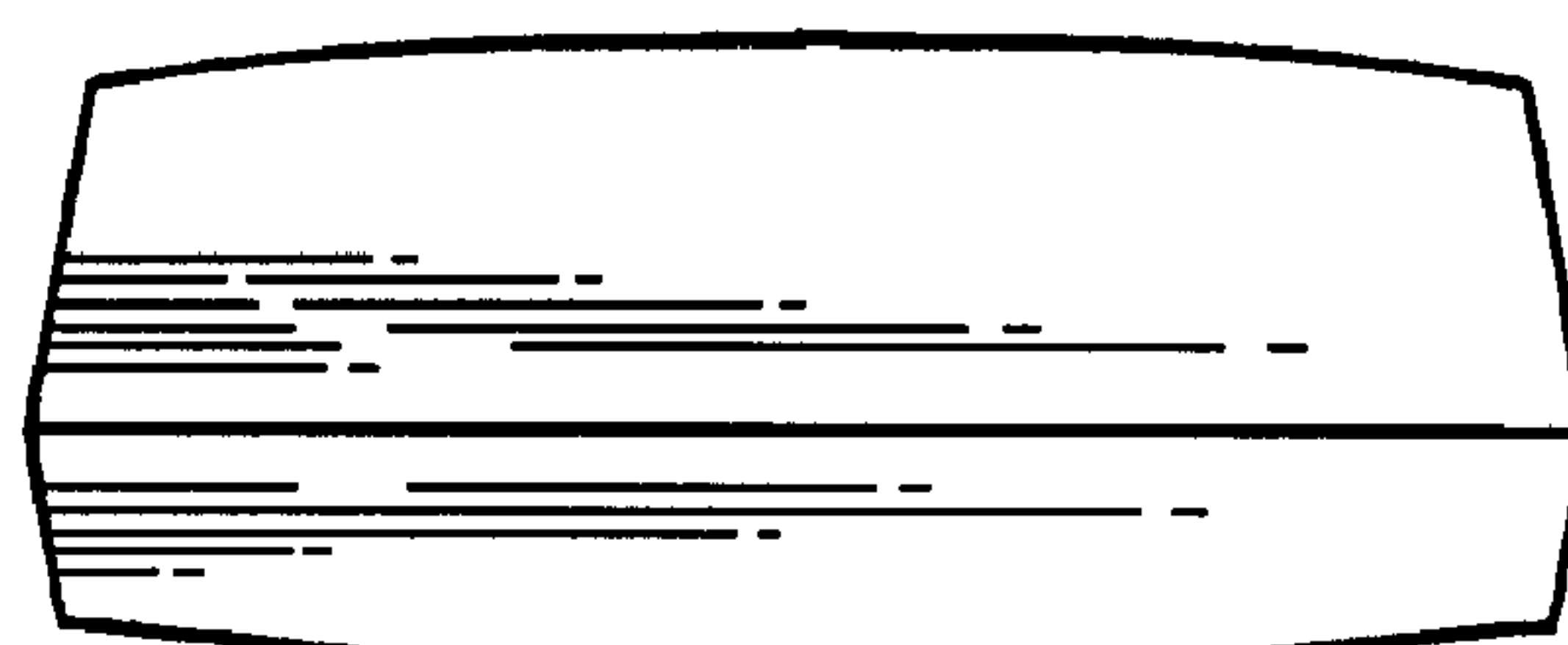


Fig. 7