



US00D524339S

(12) **United States Design Patent**  
**Waring, II et al.**

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(54) **DIGITALLY ENHANCED NIGHT VISION DEVICE**

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(73) Assignee: **Litton Systems, Inc.**, Los Angeles, CA (US)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/225,203**

(22) Filed: **Mar. 11, 2005**

(51) **LOC (8) Cl.** ..... **16-06**

(52) **U.S. Cl.** ..... **D16/132**

(58) **Field of Classification Search** ..... D16/130, D16/132; D22/109; 250/330, 353; 359/808, 359/811, 819; 361/752; 2/422

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a digitally enhanced night vision device, as shown and described.

**DESCRIPTION**

FIG. 1 is a side view of the digitally enhanced night vision device of the present invention mounted on a ballistic helmet.

FIG. 2 is a top frontal perspective view of the digitally enhanced night vision device sensor module shown separately for purposes of illustration;

FIG. 3 is a side view of the sensor module of the present invention.

FIG. 4 is a top view of the sensor module.

FIG. 5 is an upper right rear perspective view of the digitally enhanced night vision device battery module shown separately for purposes of illustration;

FIG. 6 is an upper left rear perspective view of the battery module.

FIG. 7 is an upper right frontal perspective view of the battery module

FIG. 8 is a right side view of the battery module.

FIG. 9 is a top view of the battery module.

FIG. 10 is a frontal view of the battery module.

FIG. 11 is a top right frontal perspective view of the digitally enhanced night vision device electronic circuitry module shown separately for purposes of illustration;

FIG. 12 is a top left frontal perspective view of the electronic circuitry module.

FIG. 13 is a right lower perspective view of the electronic circuitry module.

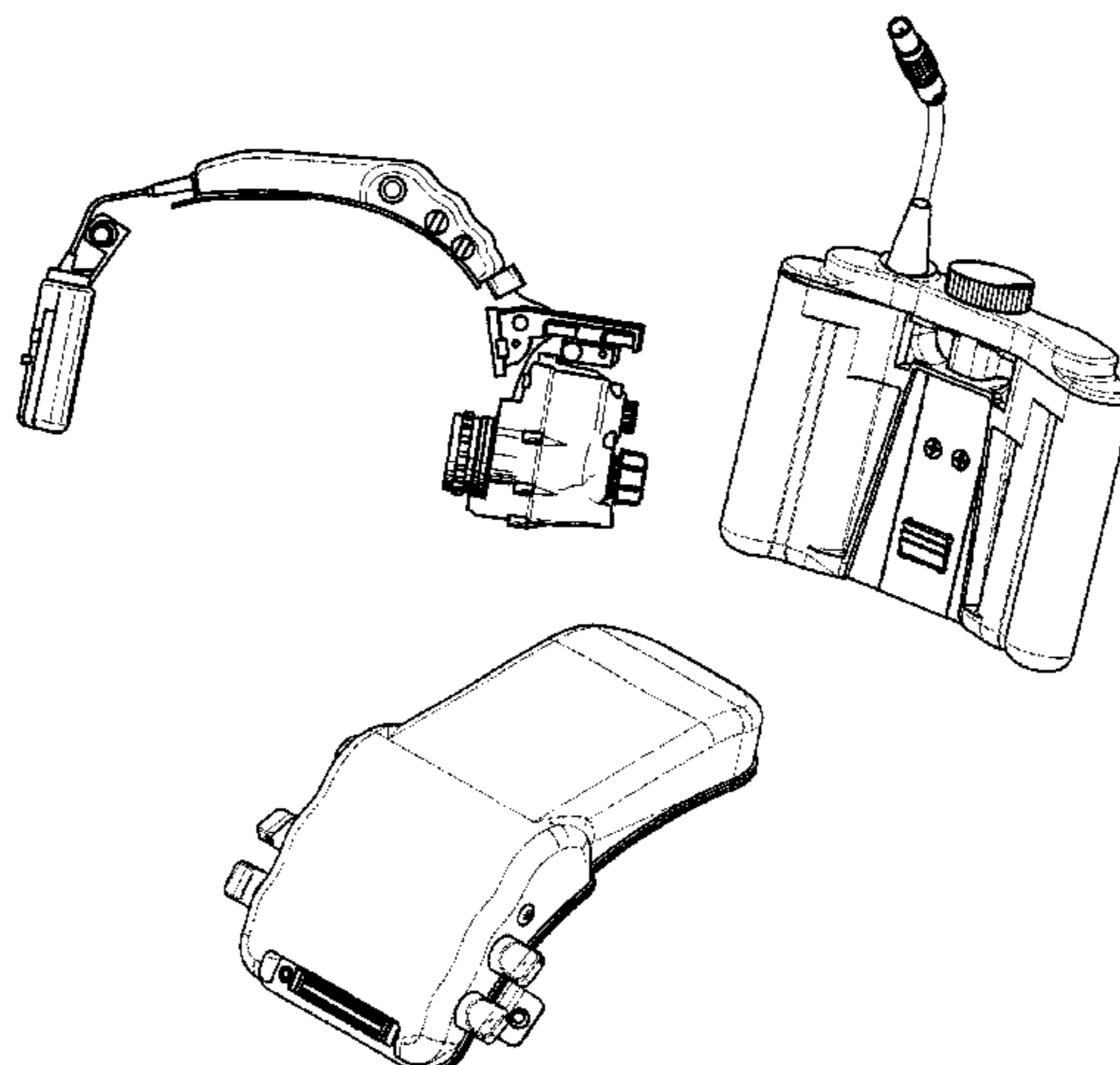
FIG. 14 is a top view of the electronic circuitry module.

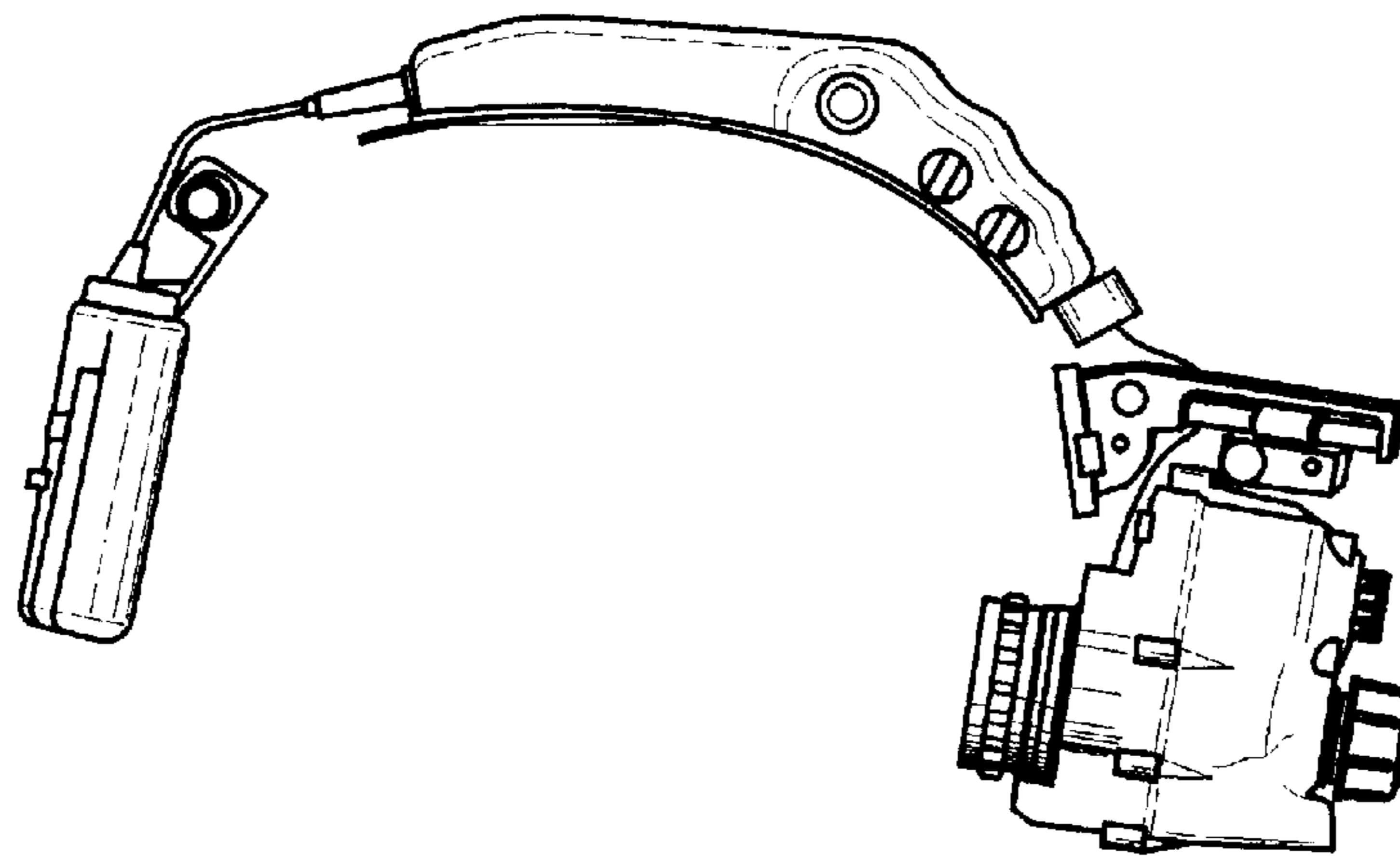
FIG. 15 is a rear view of the electronic circuitry module.

FIG. 16 is a front view of the electronic circuitry module; and,

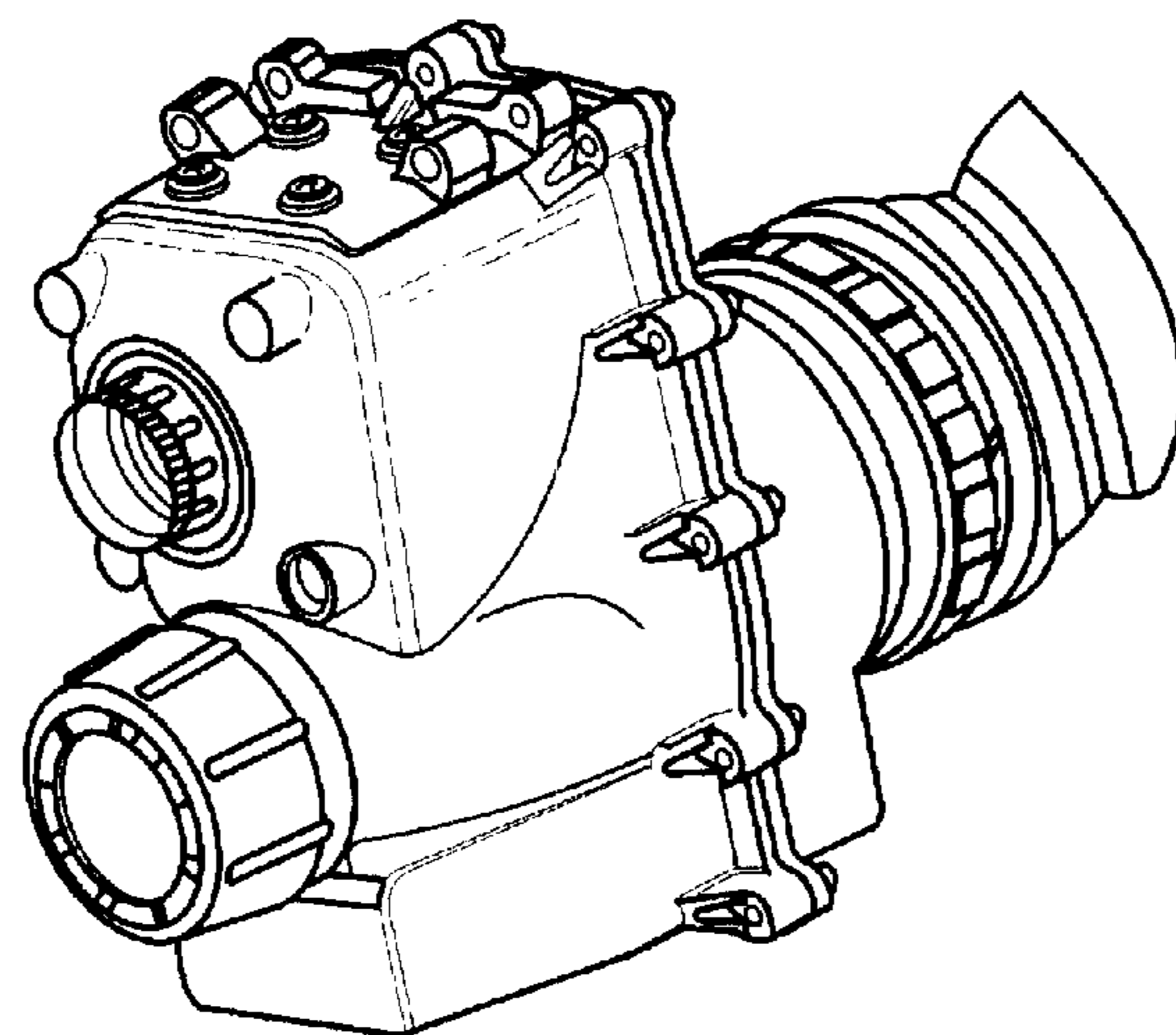
FIG. 17 is a right side view of the electronic circuitry module.

**1 Claim, 8 Drawing Sheets**

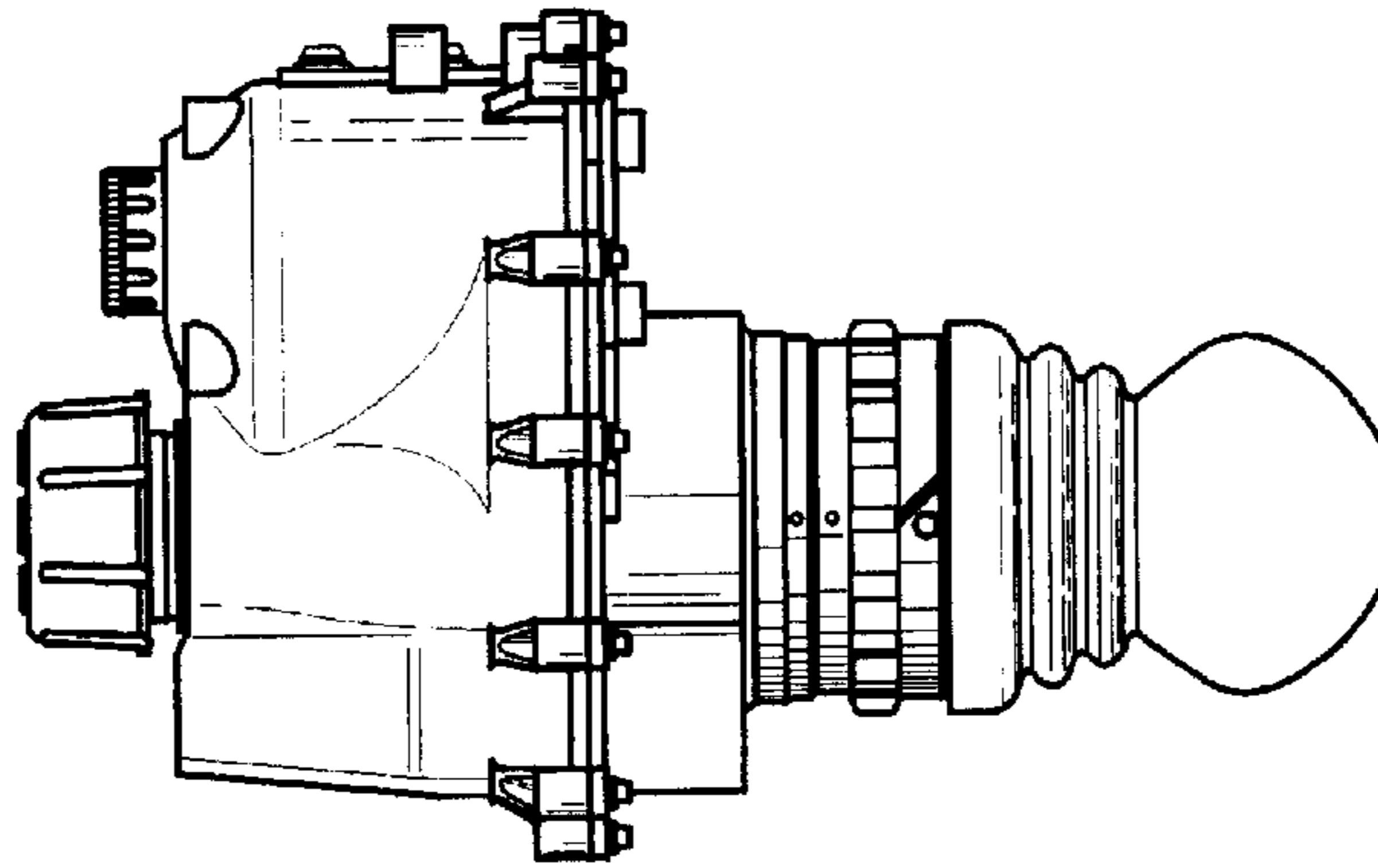




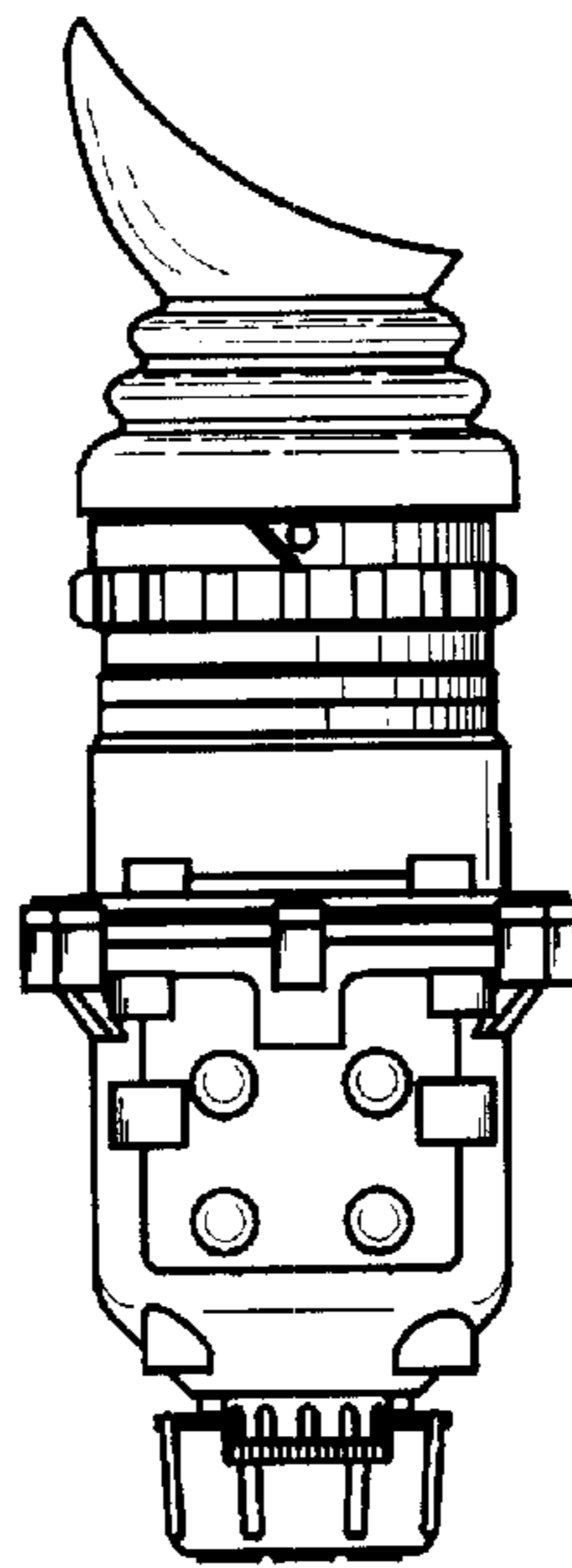
*FIG. 1*



*FIG. 2*



*FIG. 3*



*FIG. 4*

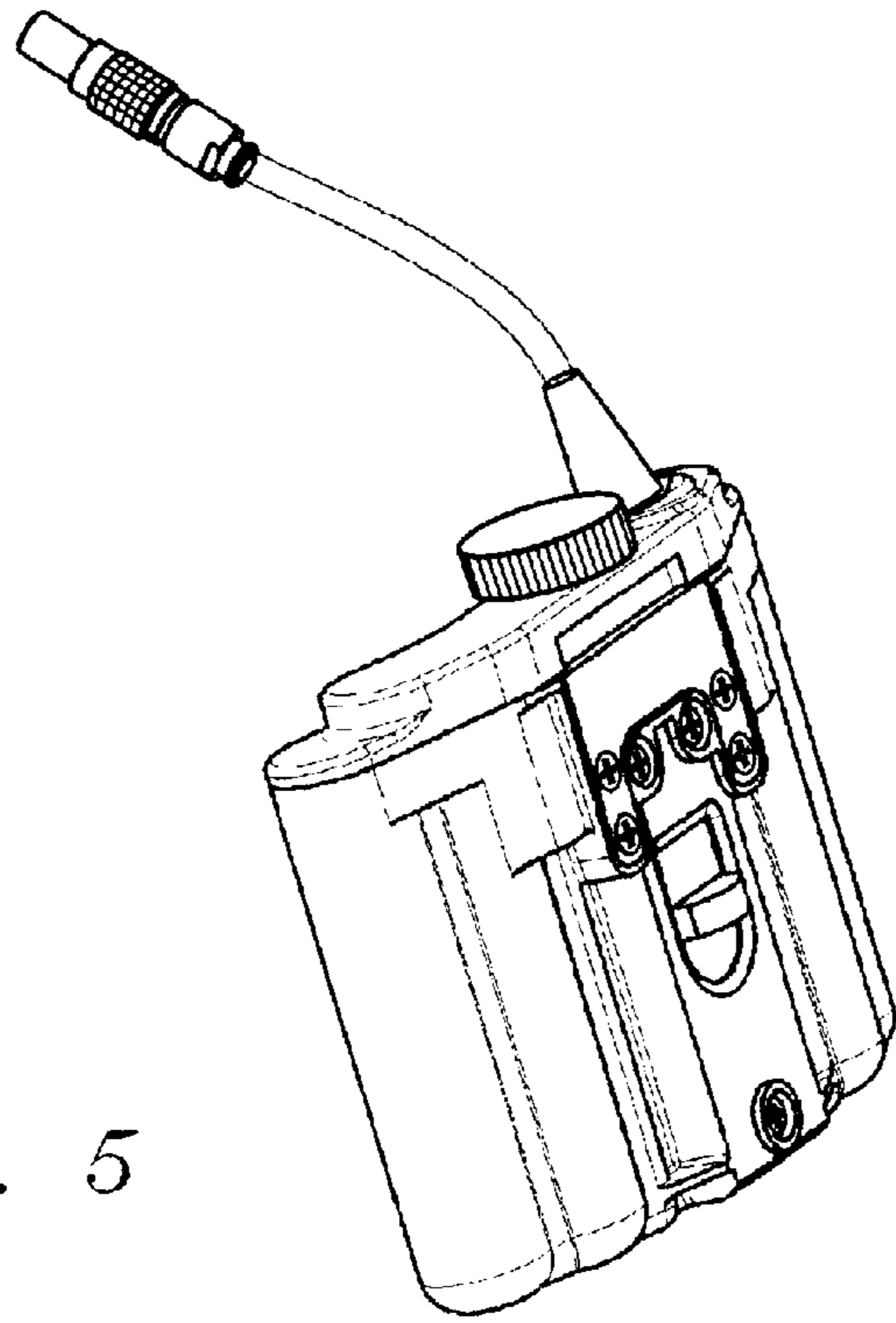


FIG. 5

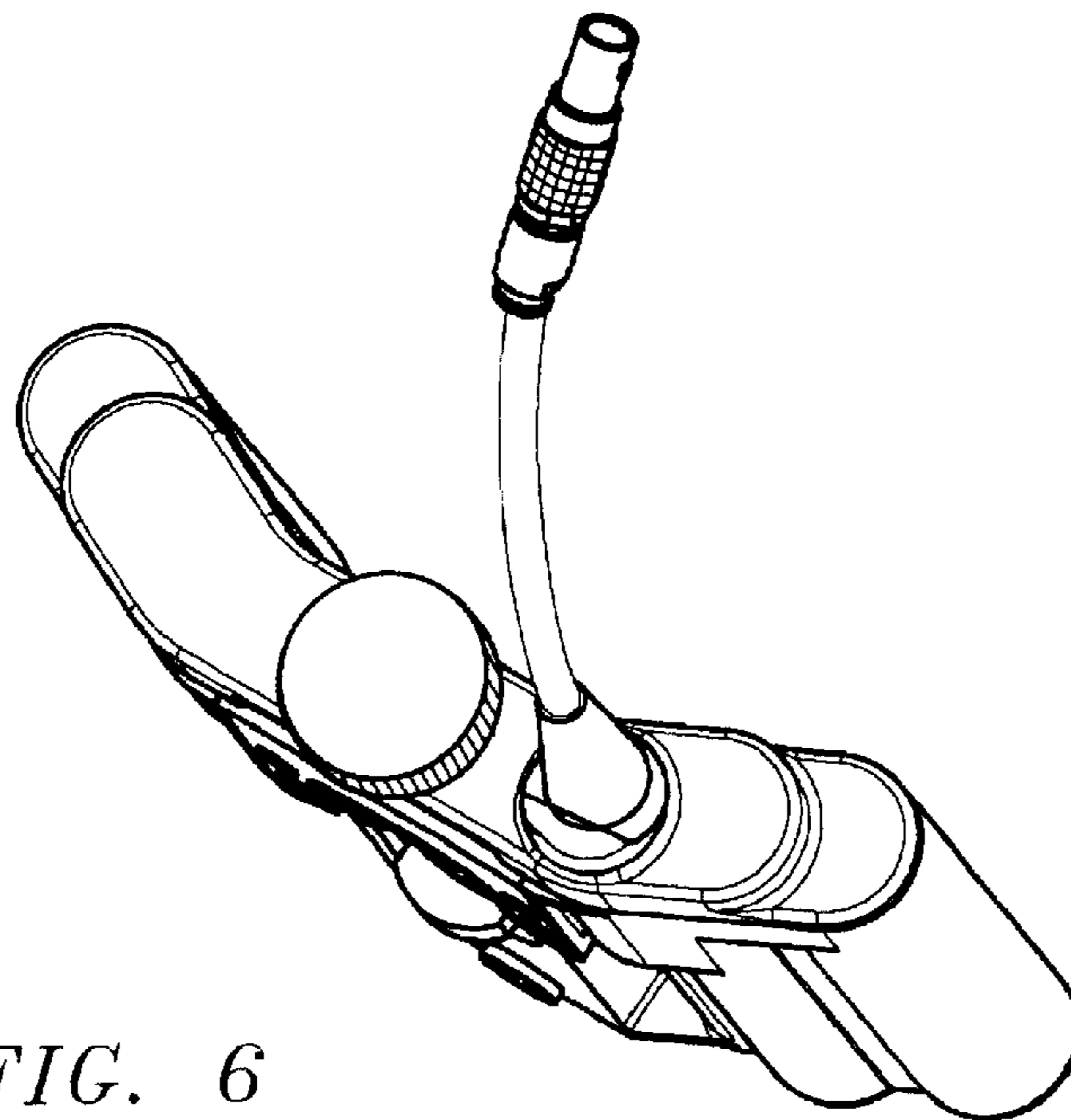


FIG. 6

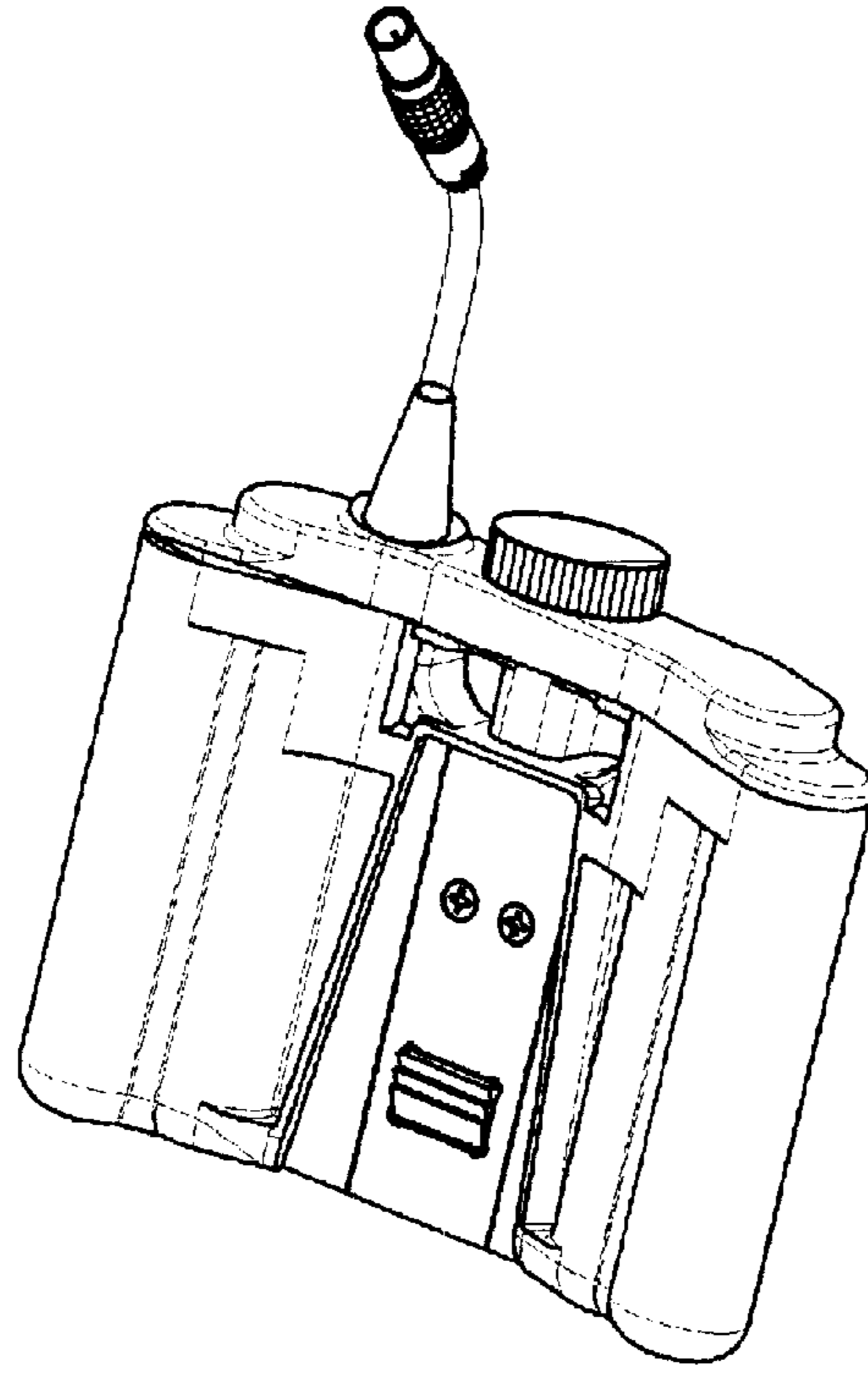


FIG. 7

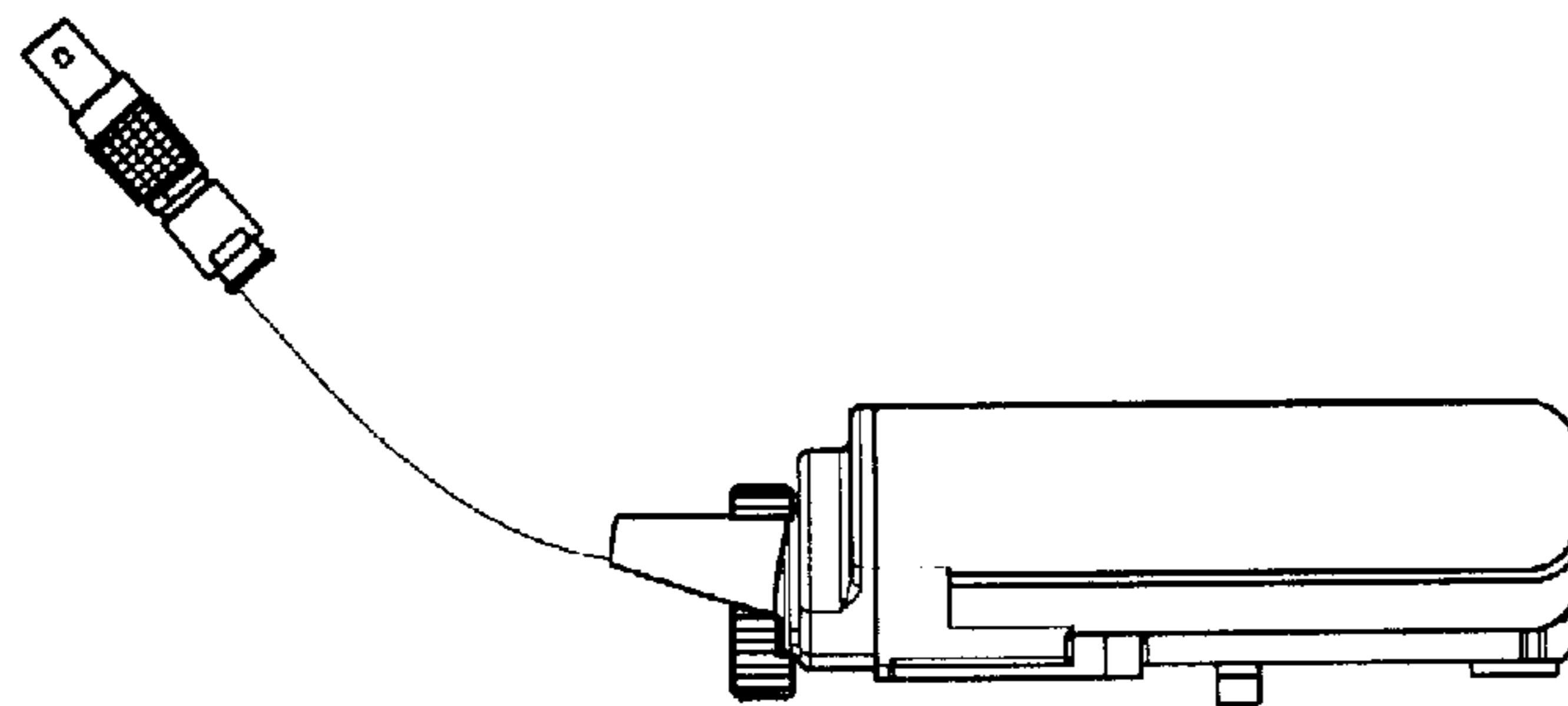


FIG. 8

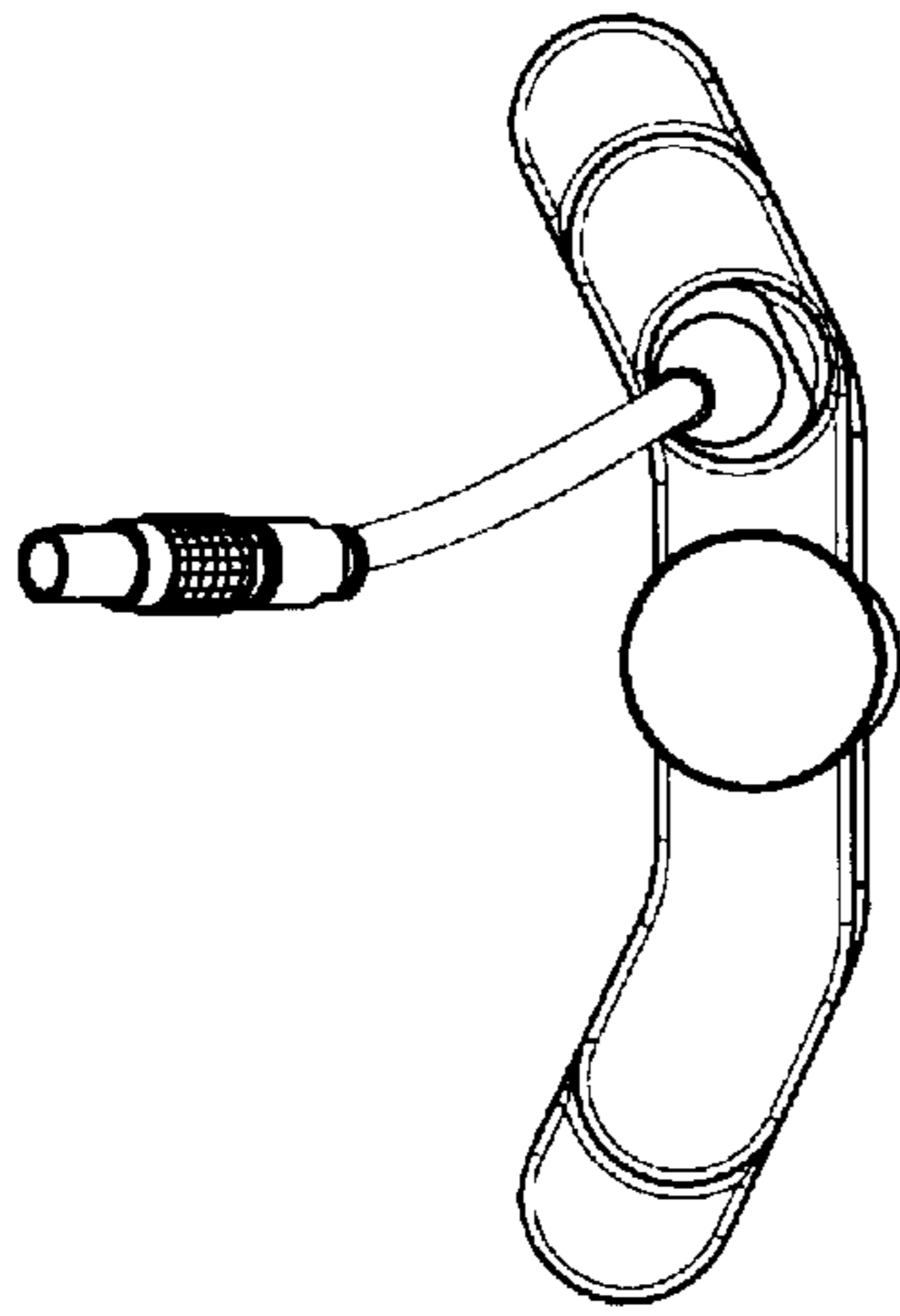


FIG. 9

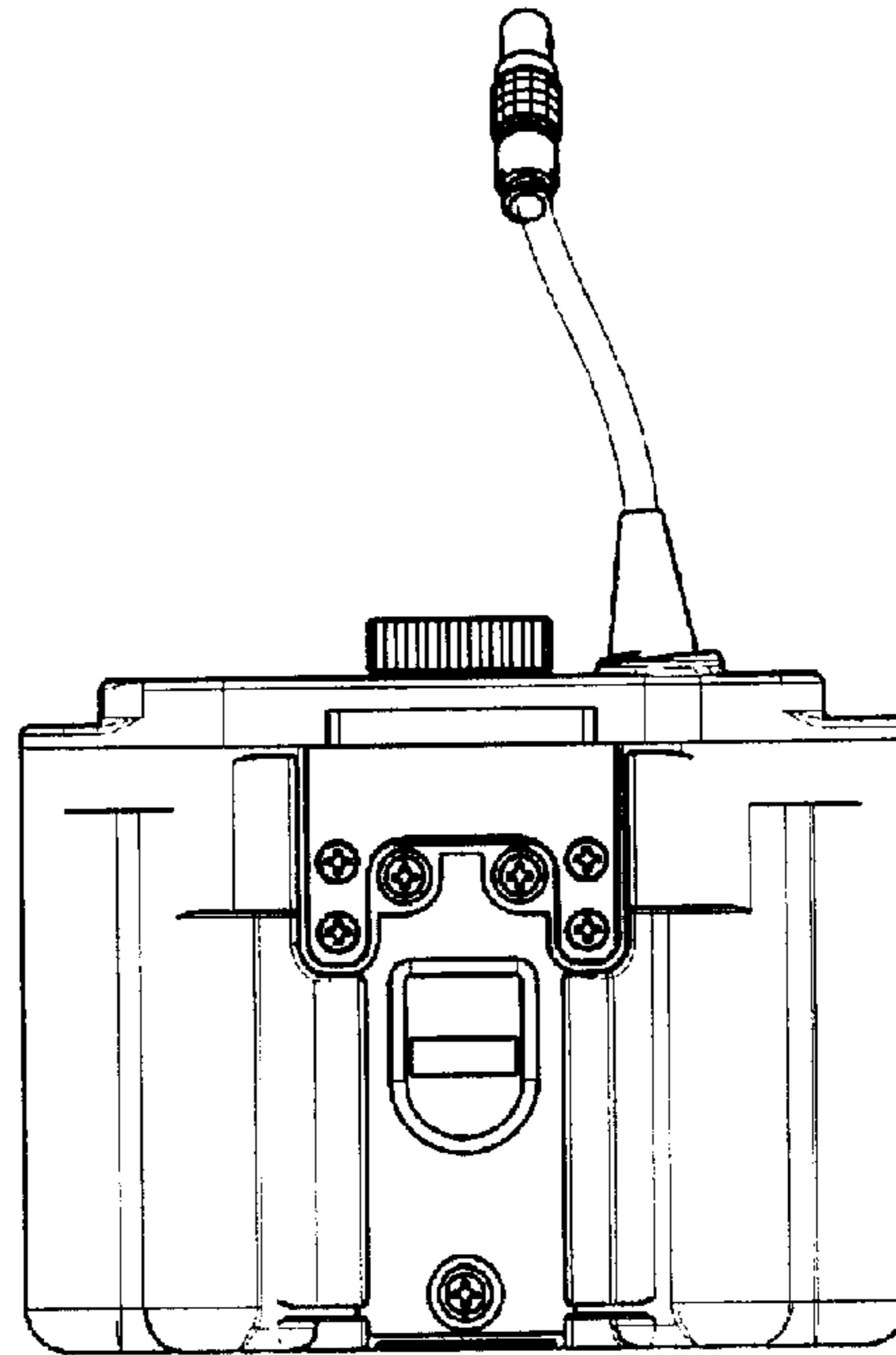


FIG. 10

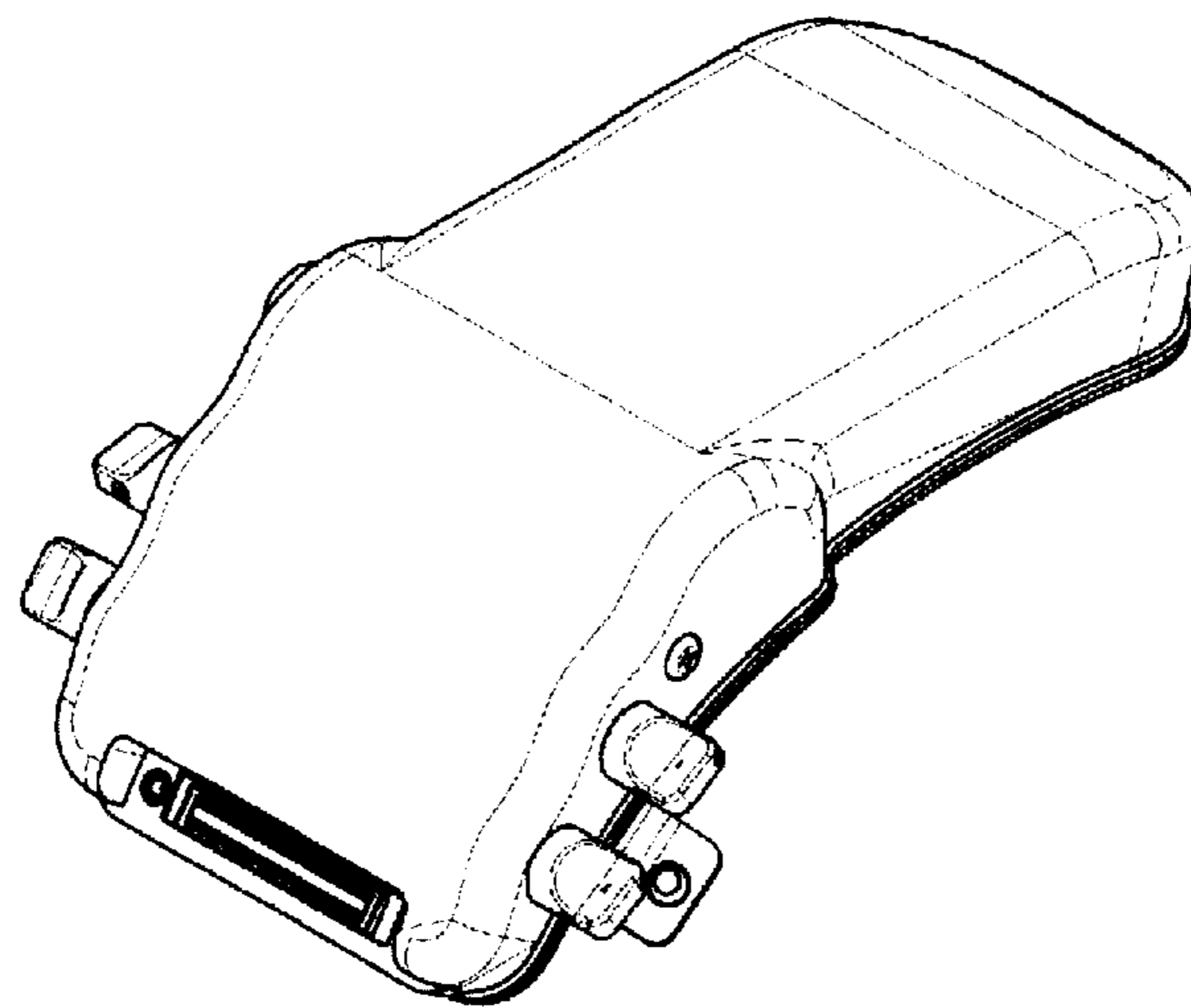
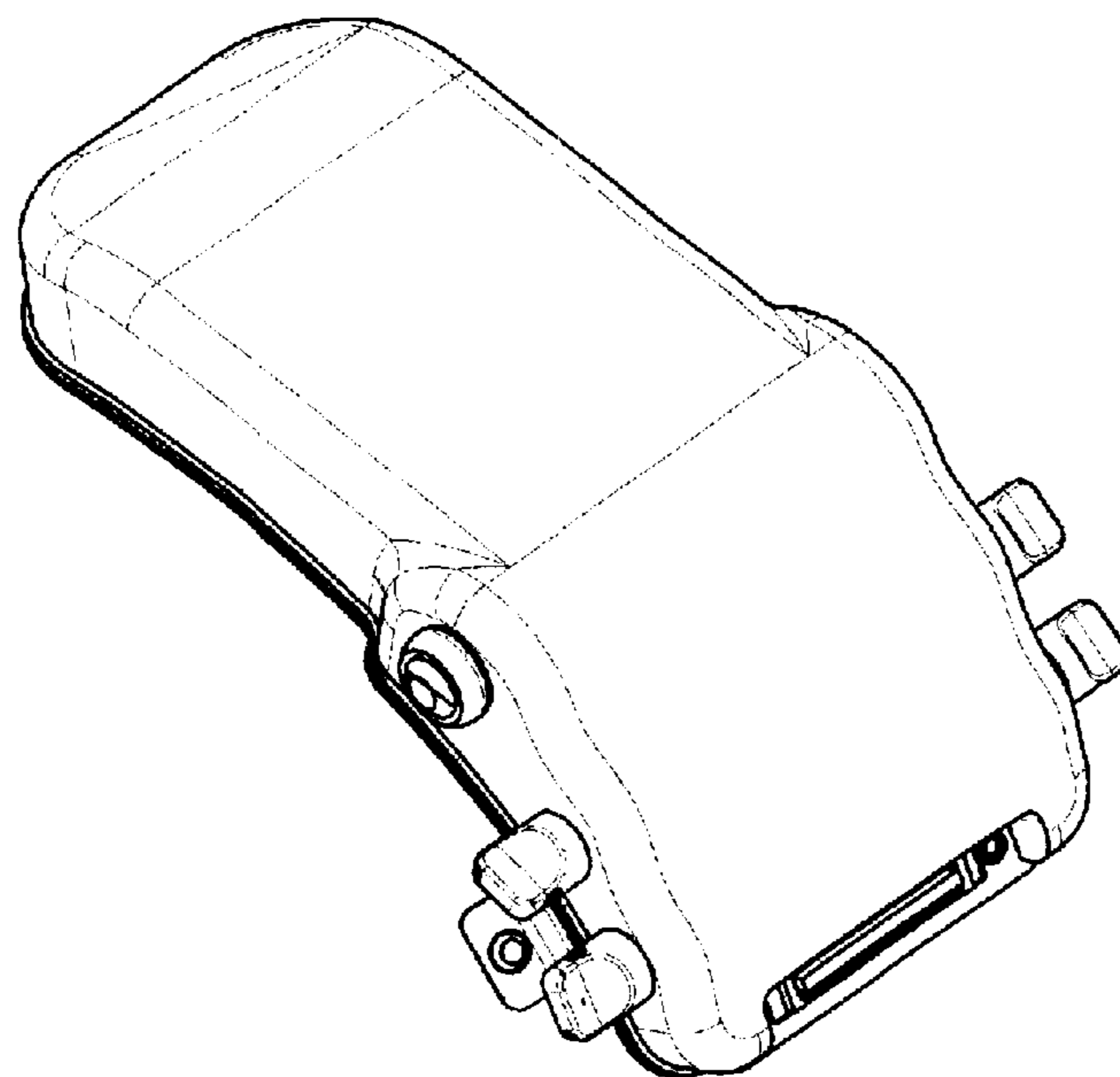
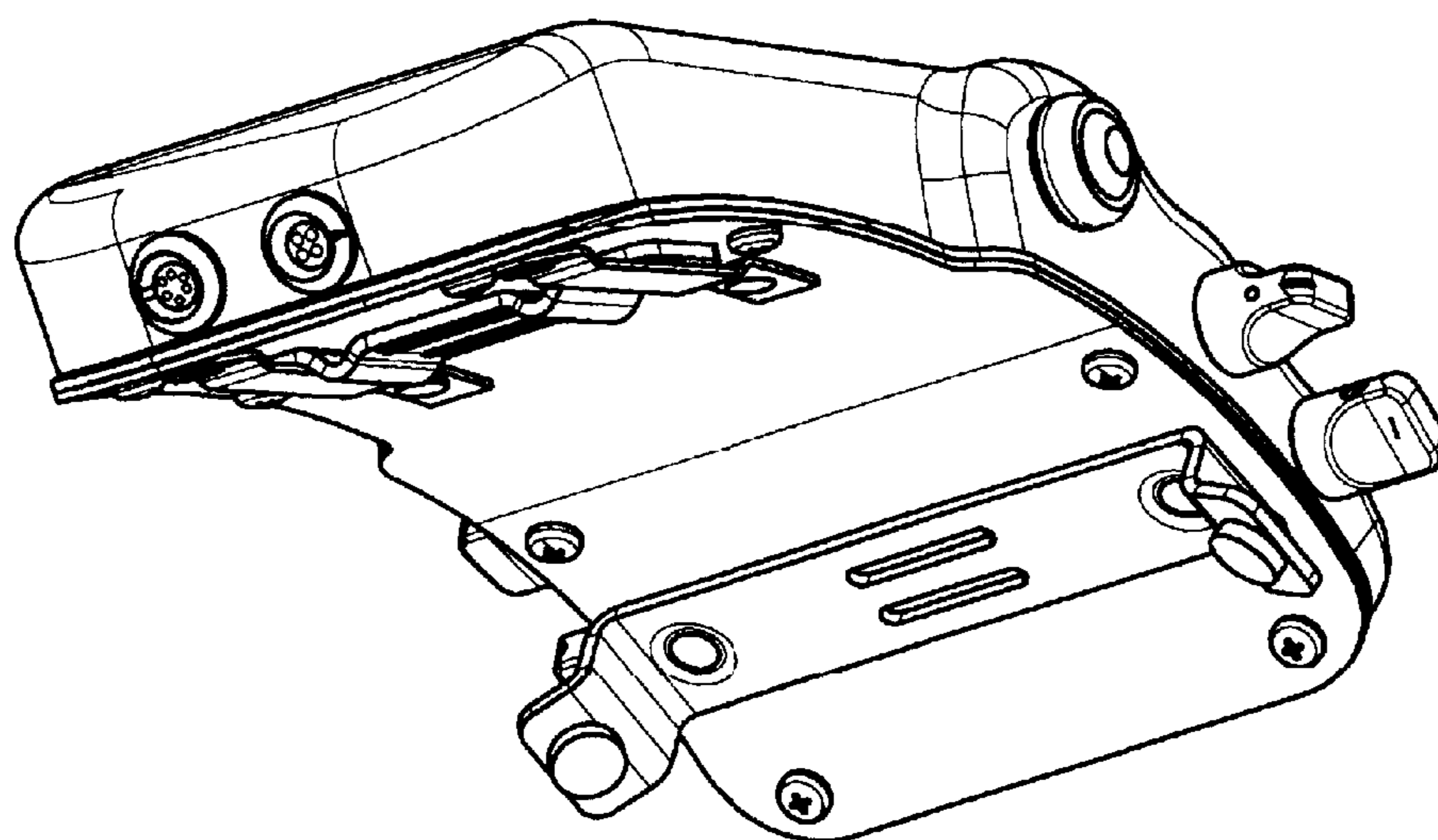


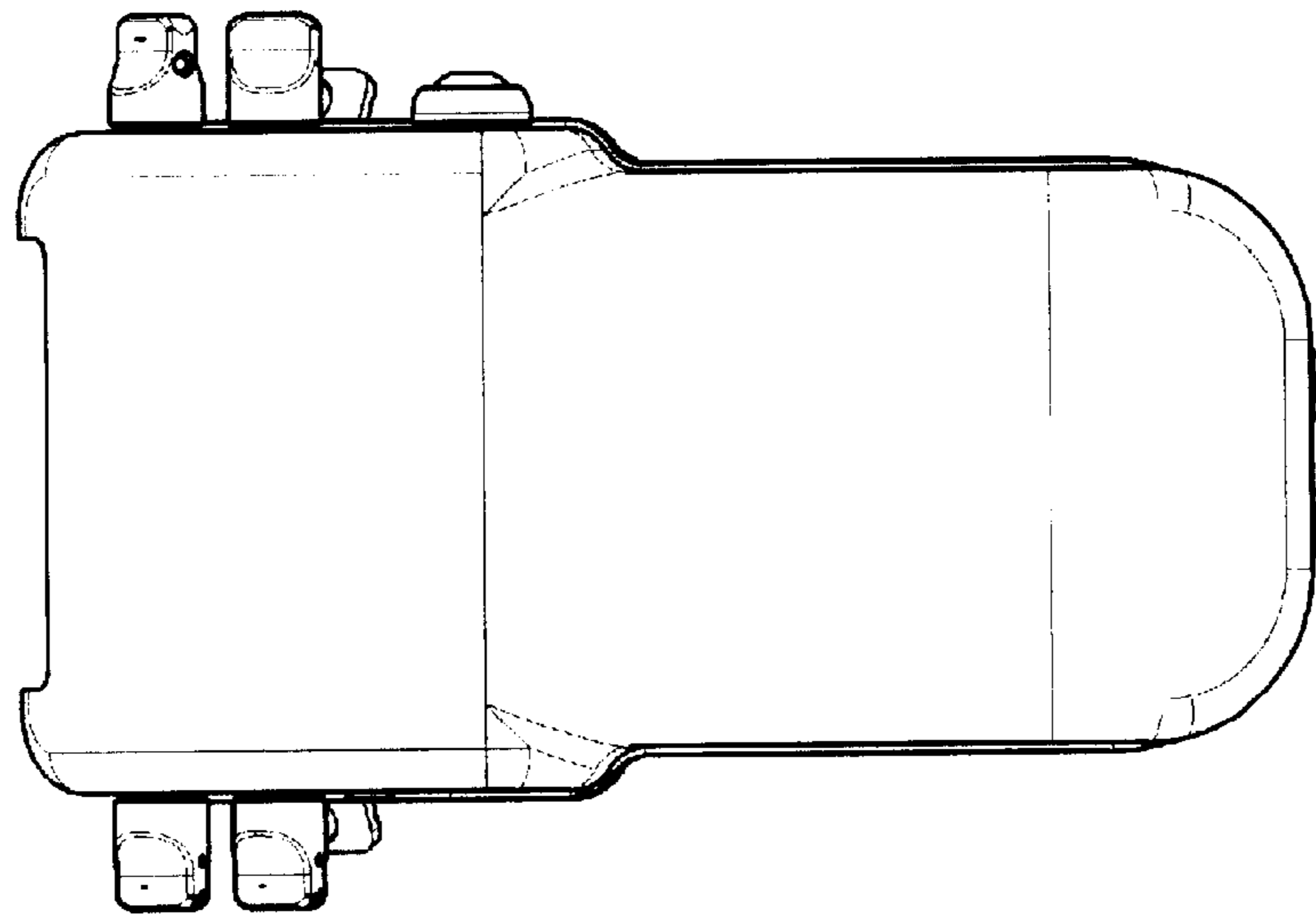
FIG. 11



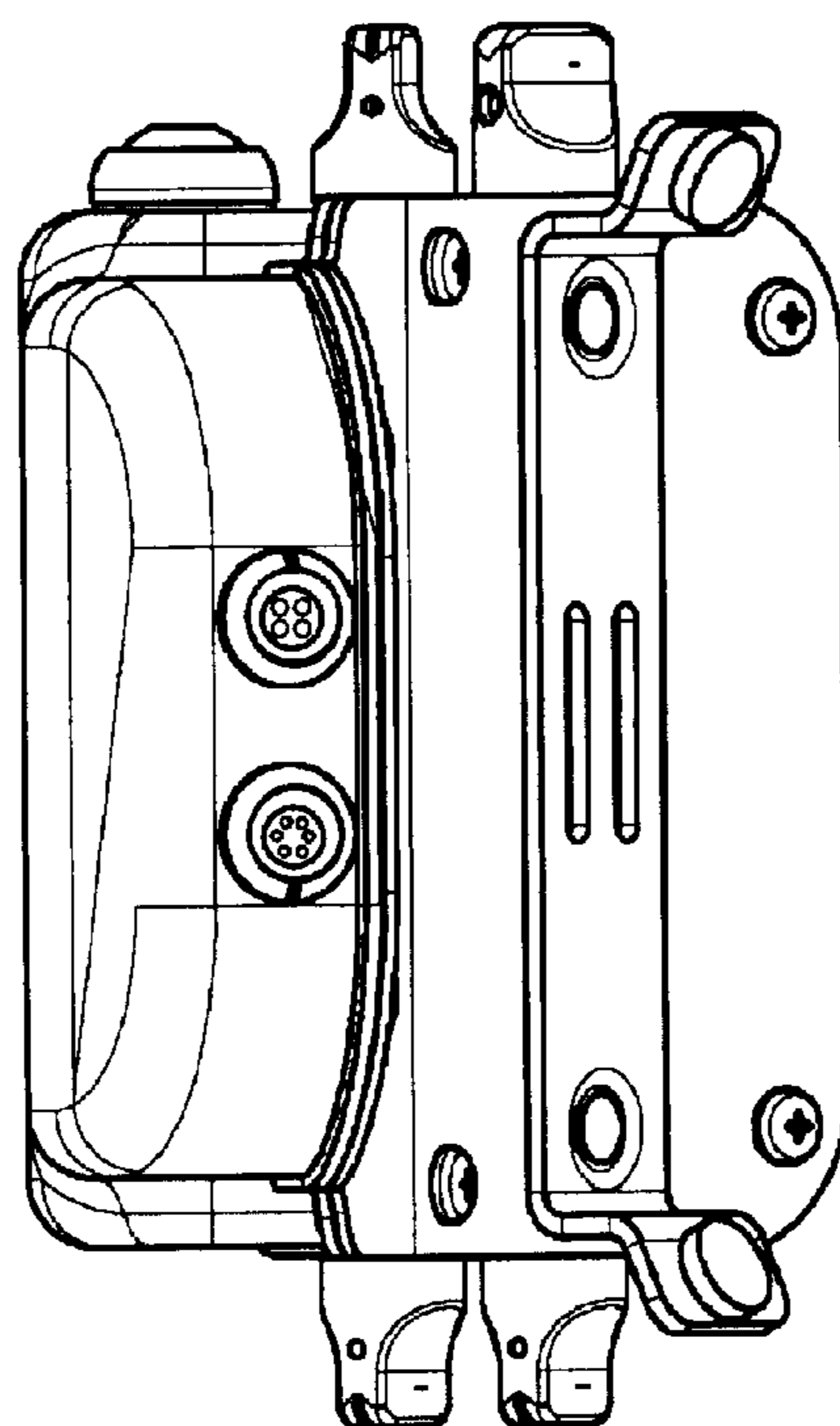
*FIG. 12*



*FIG. 13*

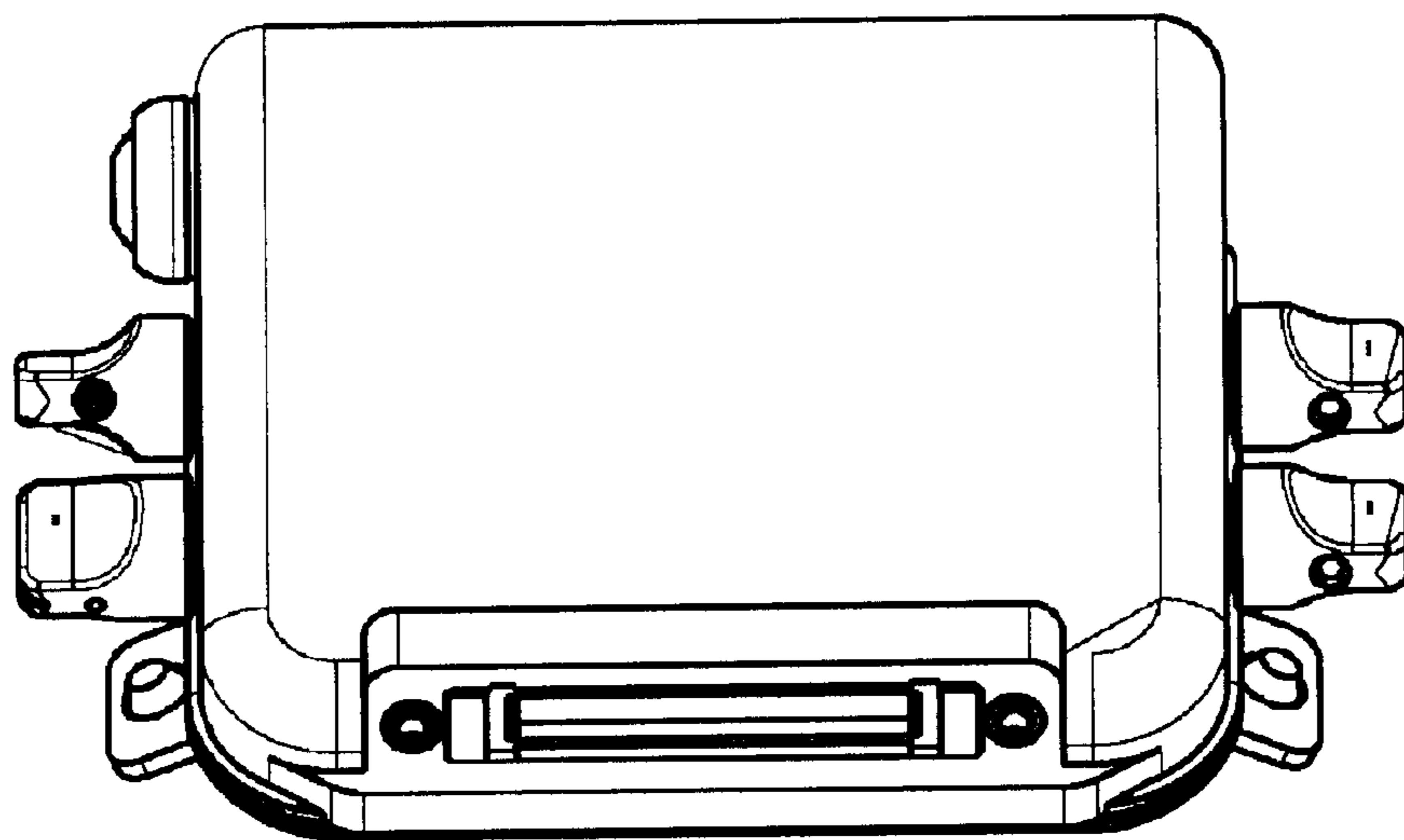


*FIG. 14*

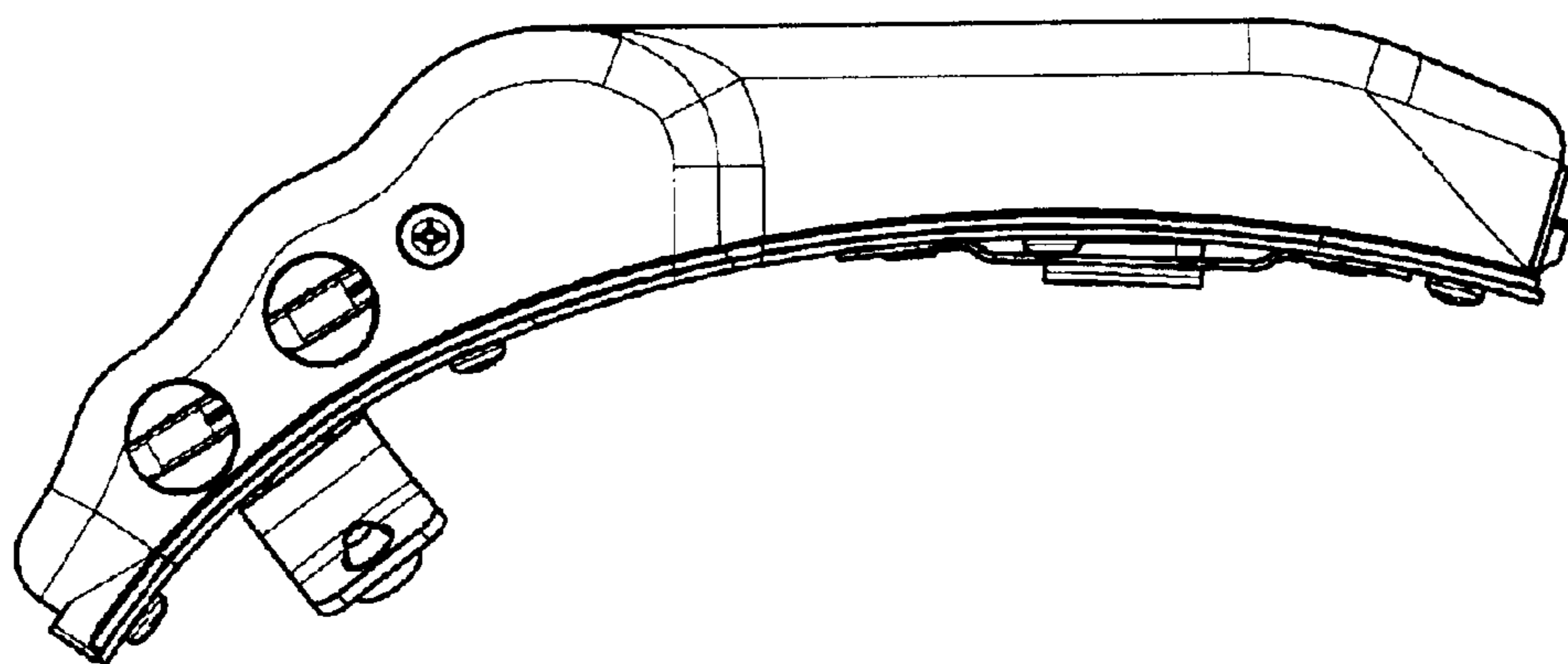


*FIG. 15*





*FIG. 16*



*FIG. 17*