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(12) **United States Design Patent**
Liu et al.

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(54) **SINGLE HEIGHT HOUSING**

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D14/356-58, 432-34, 242, 138, 140, 140.1,
D14/125, 256, 299; 361/732, 735, 683, 622,
361/629; D13/110, 118, 133, 152, 184; 220/23.6,
220/4.02; 206/511; 439/752.5, 701, 928,
439/928.1; 455/550.1, 555, 552.1; D10/104
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a single height housing, substan-
tially as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of the front and first side of the
single height housing, including phantom views of a plural-
ity of light emitting diodes (LEDs);

FIG. 2 is a perspective view of the back and first side of the
single height housing, including phantom views of a power
port, two telephone ports, an Ethernet port, a universal serial
bus (USB) port, and a cable port;

FIG. 3 is a top view of the single height housing, including
phantom views of a plurality of light emitting diodes
(LEDs);

FIG. 4 is a bottom view of the single height housing;

FIG. 5 is a front view of the single height housing, including
phantom views of a plurality of light emitting diodes
(LEDs);

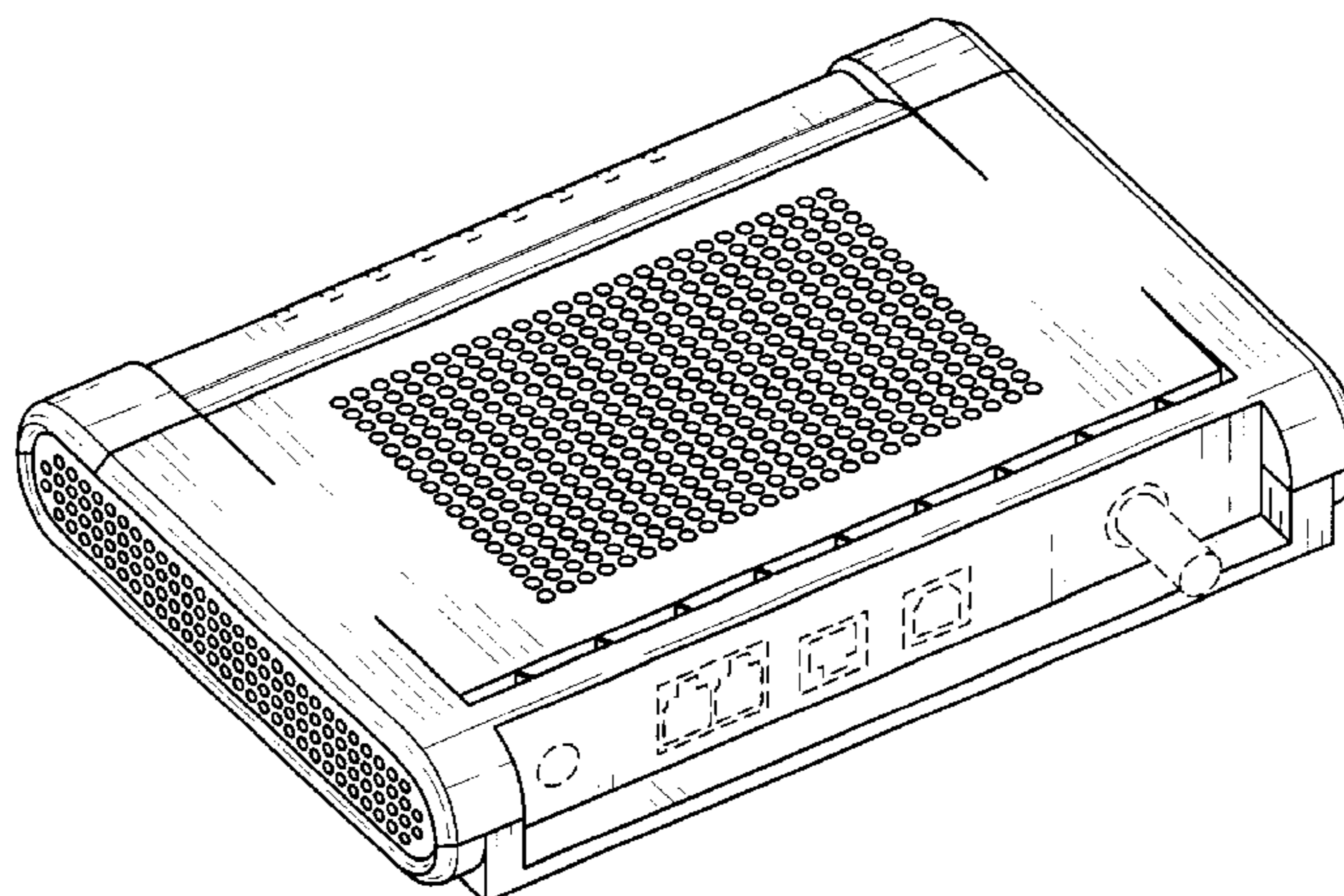
FIG. 6 is a back view of the single height housing, including
phantom views of a power port, two telephone ports, an
Ethernet port, a universal serial bus (USB) port, and a cable
port;

FIG. 7 is a view of the first side of the single height housing;
and,

FIG. 8 is a view of the second side of the single height
housing.

Elements rendered in broken lines are for illustrative pur-
poses only and form no part of the claimed design.

1 Claim, 3 Drawing Sheets



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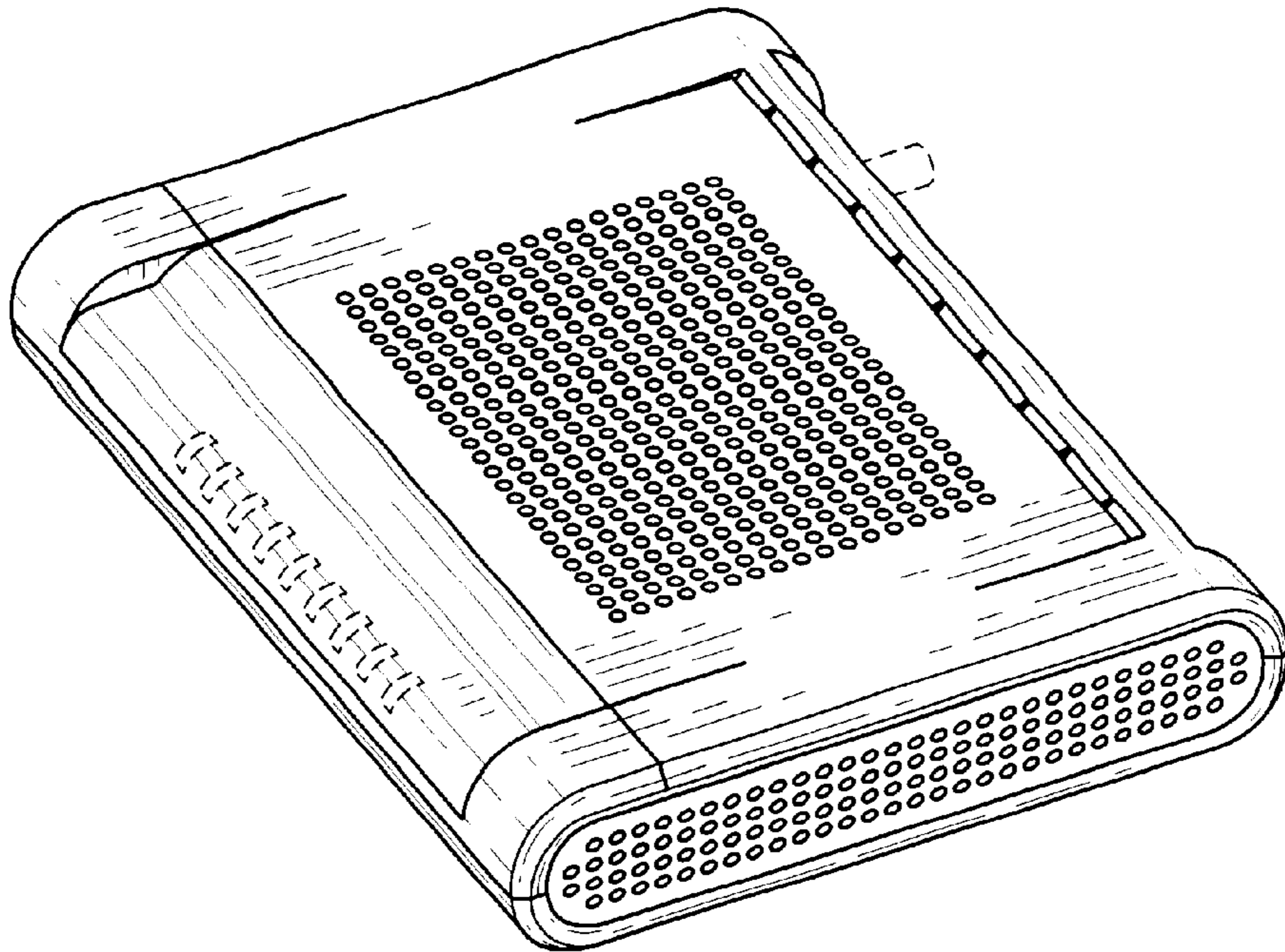


Fig. 1

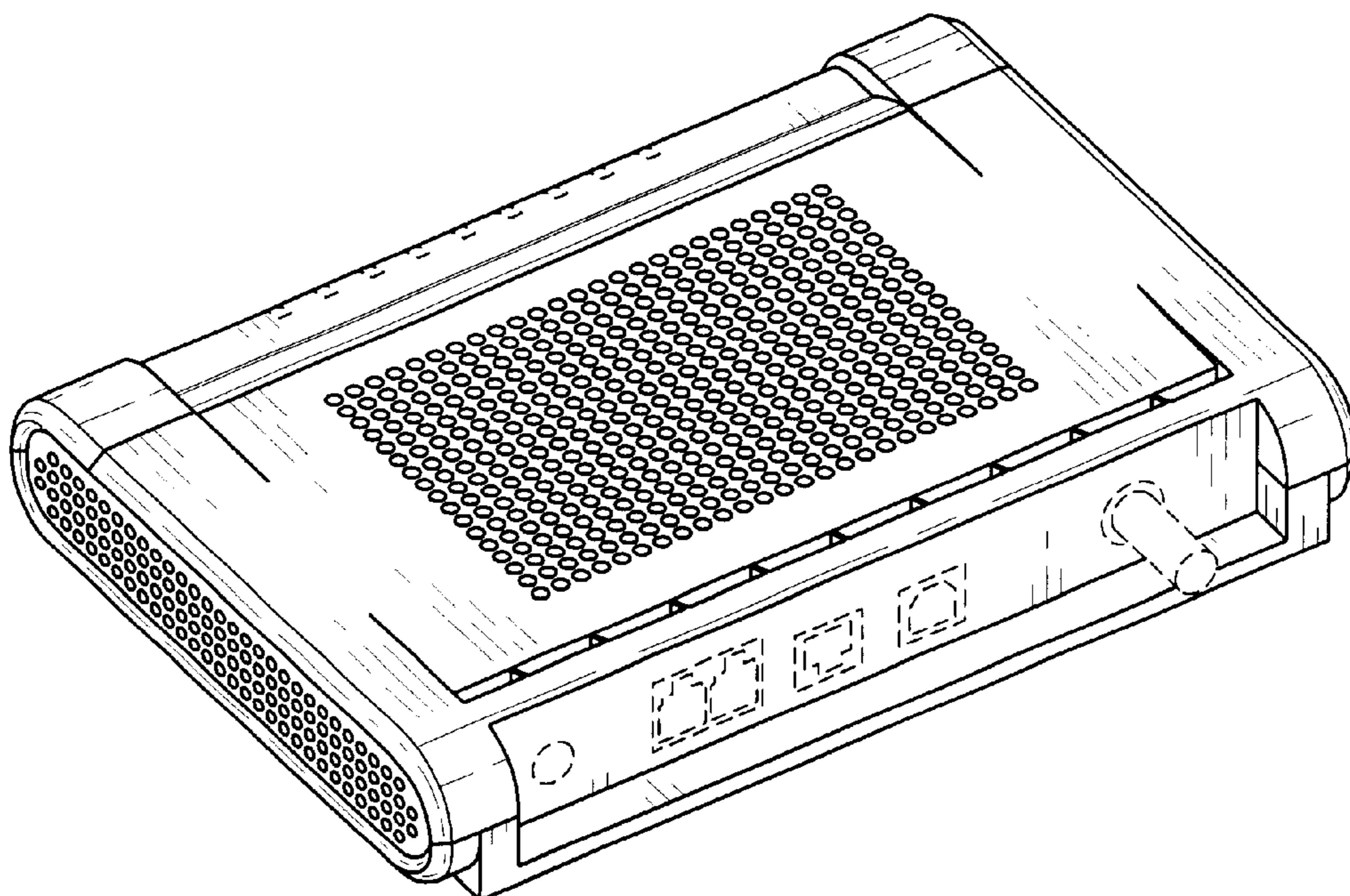


Fig. 2

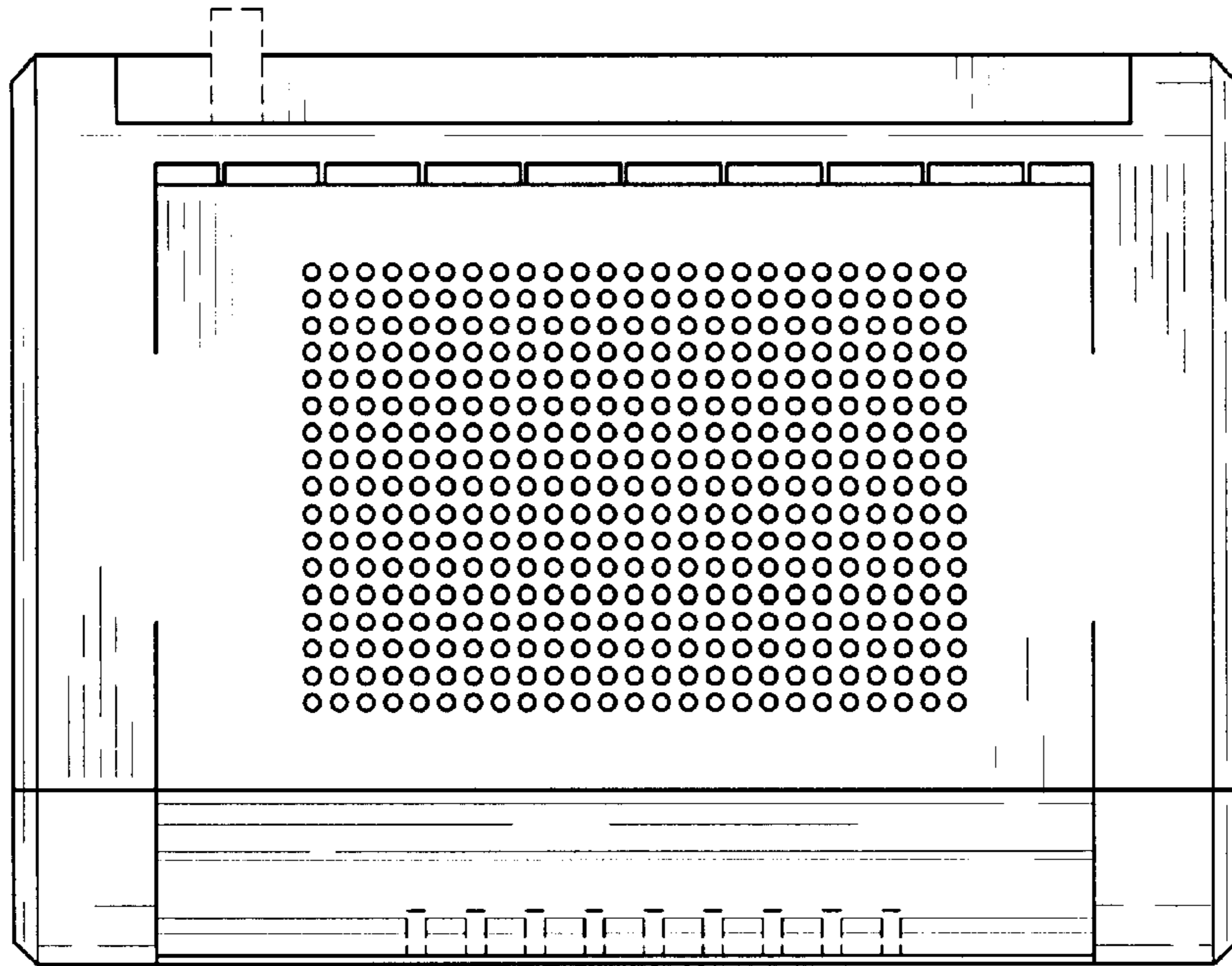


Fig. 3

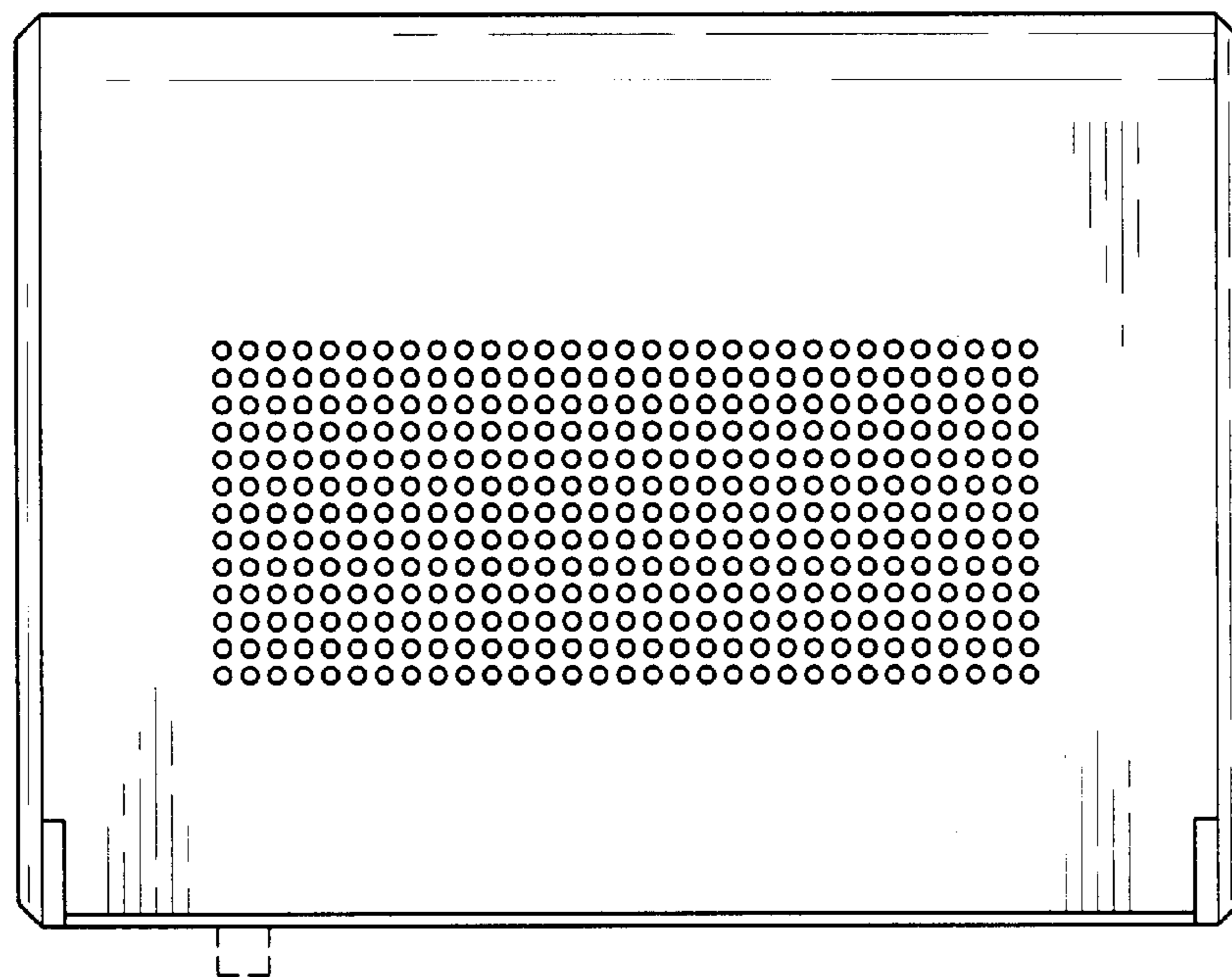


Fig. 4

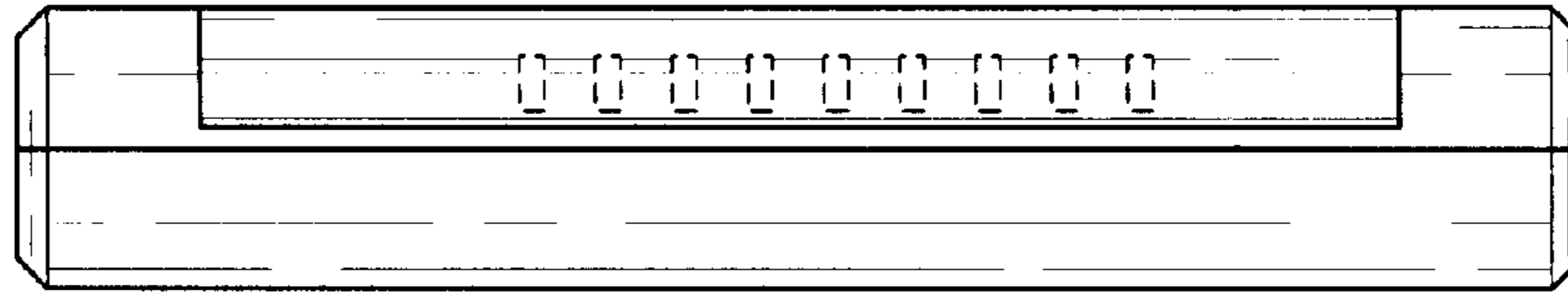


Fig. 5

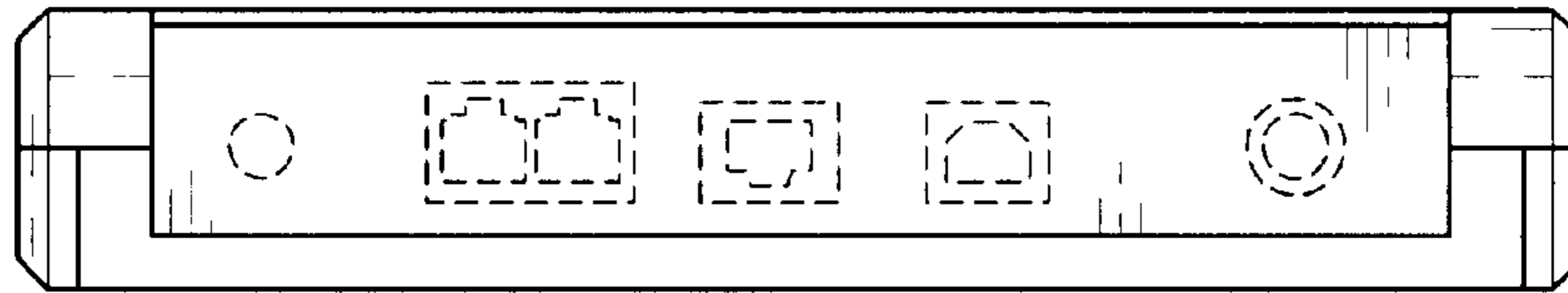


Fig. 6

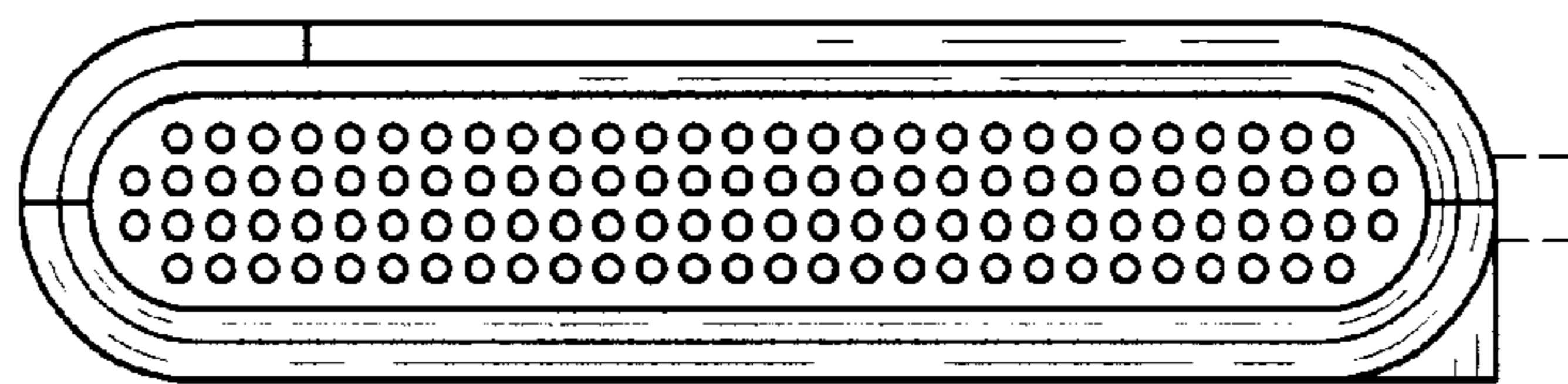


Fig. 7

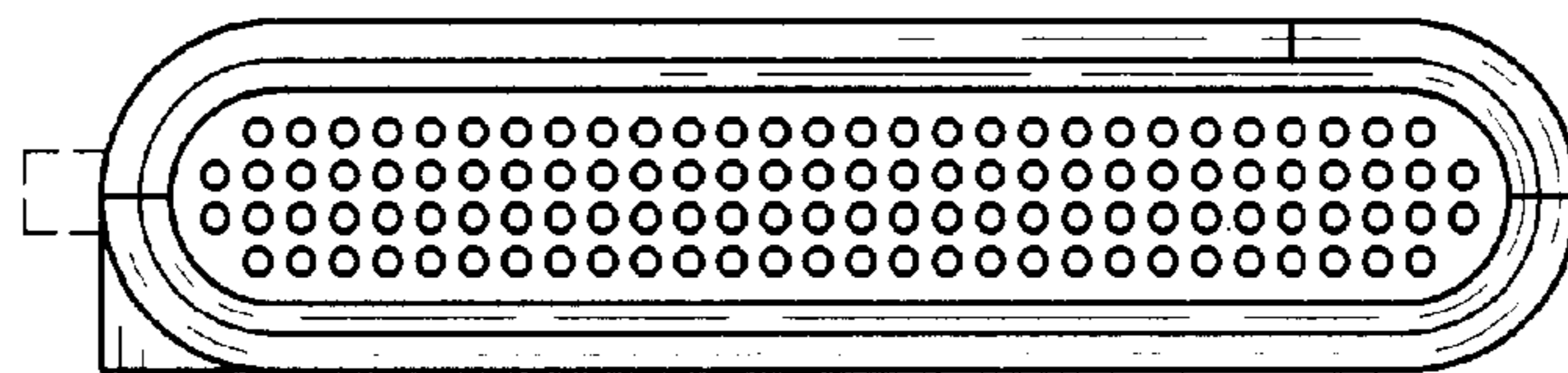


Fig. 8