

US00D520992S

(12) **United States Design Patent**
Lee

(10) **Patent No.:** **US D520,992 S**

(45) **Date of Patent:** **** May 16, 2006**

(54) **CENTRAL PROCESSING UNIT MODULE
FOR PROGRAMMABLE LOGIC
CONTROLLER**

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D436,953 S * 1/2001 Matsuoka D14/349
D480,723 S * 10/2003 Lee et al. D14/349

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* cited by examiner

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(**) Term: **14 Years**

(57) **CLAIM**

(21) Appl. No.: **29/204,277**

The ornamental design for central processing unit module
for programmable logic controller, as shown and described.

(22) Filed: **Apr. 28, 2004**

DESCRIPTION

(30) **Foreign Application Priority Data**

Nov. 10, 2003 (KR) 30-2003-0031741

FIG. 1 is a perspective view of central processing unit
module for programmable logic controller showing the new
design:

(51) **LOC (8) Cl.** **14-02**

(52) **U.S. Cl.** **D14/301**

FIG. 2 is a front view thereof;

(58) **Field of Classification Search** D14/300–302,
D14/308, 311; D13/162, 184, 199, 99.01–99.12;

FIG. 3 is a rear view thereof;

369/34.01, 36.01; 361/680–686, 690–696
See application file for complete search history.

FIG. 4 is a left-side view thereof;

FIG. 5 is a right-side view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

(56) **References Cited**

U.S. PATENT DOCUMENTS

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1 Claim, 5 Drawing Sheets

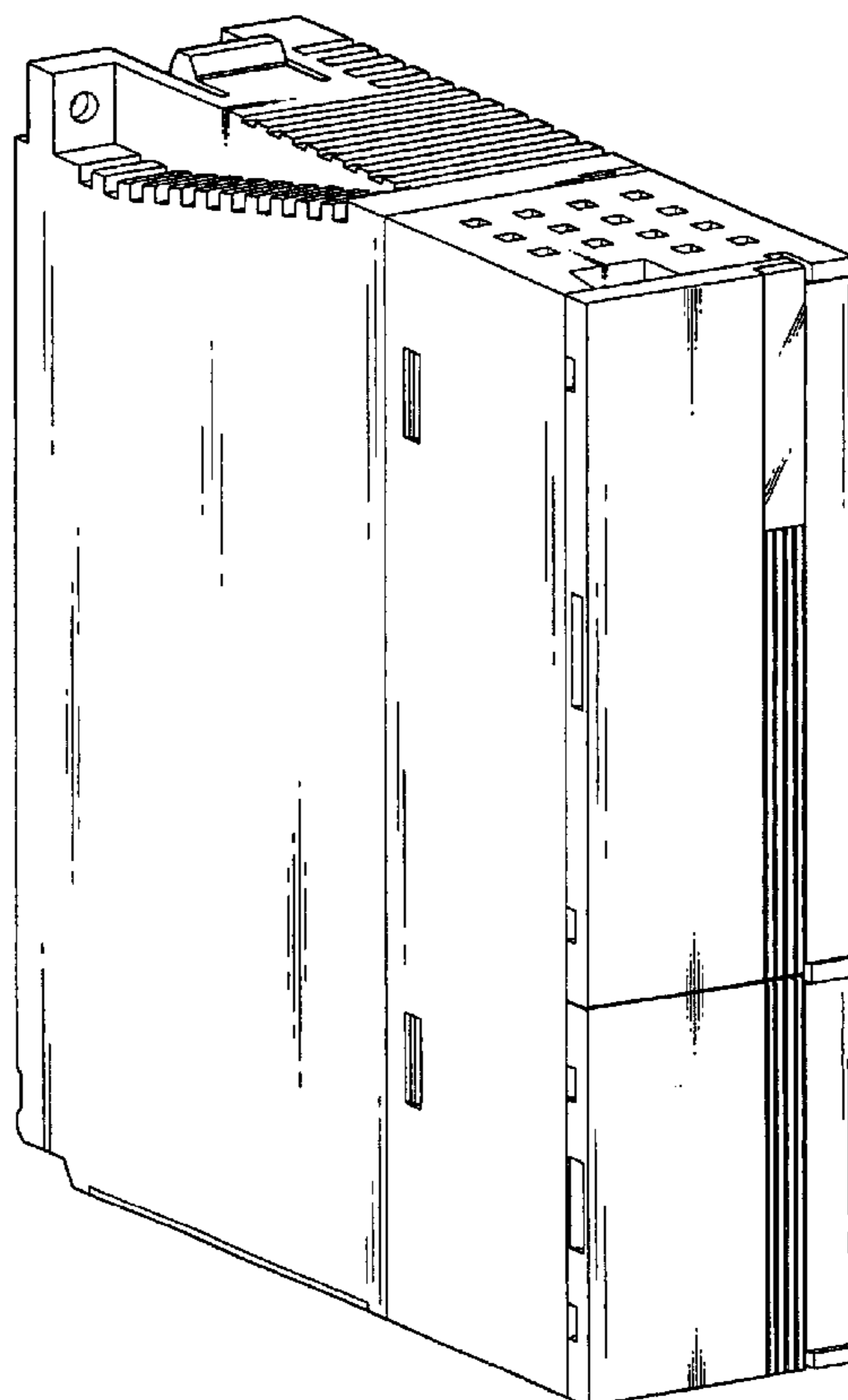


FIG. 1

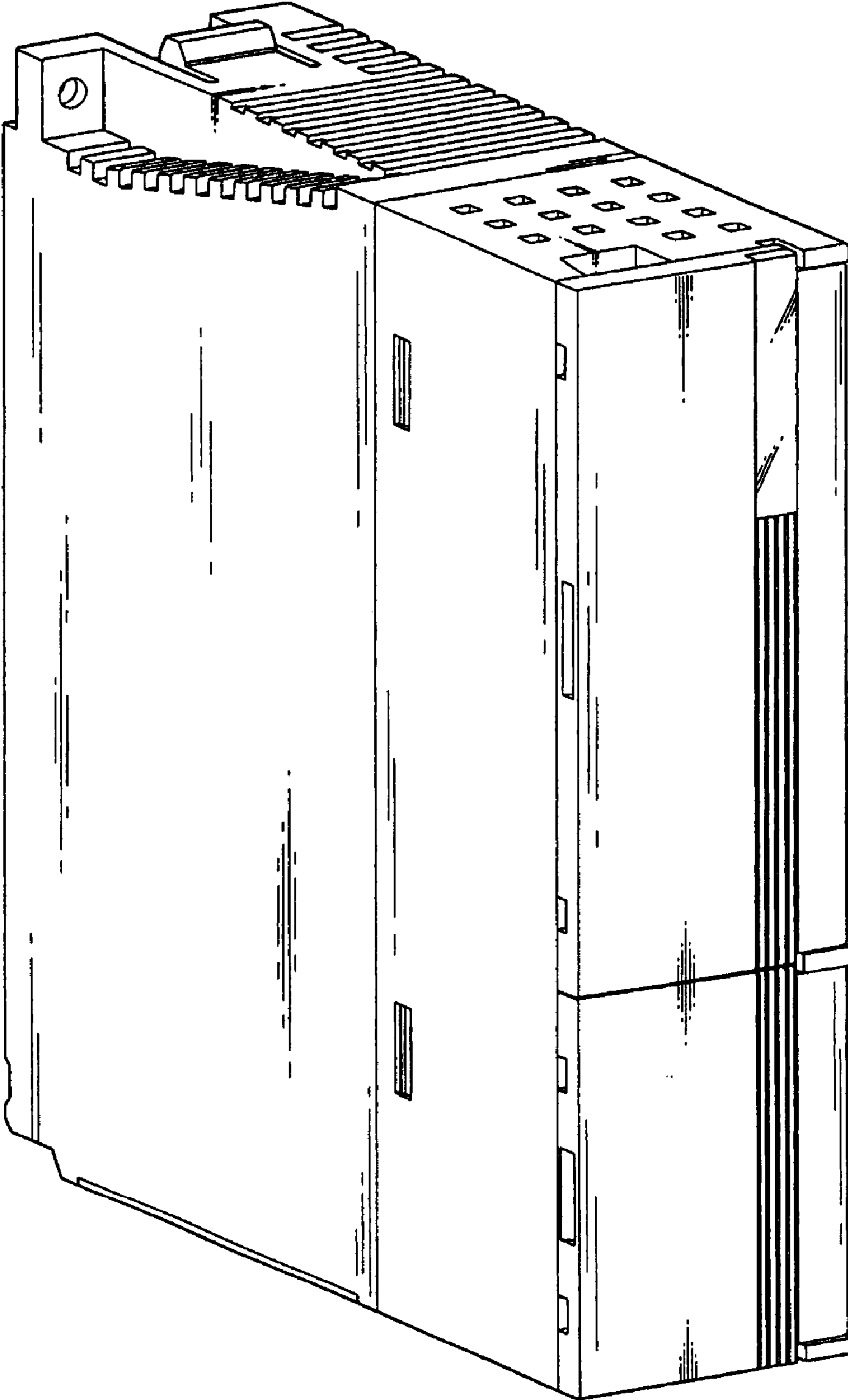


FIG. 2

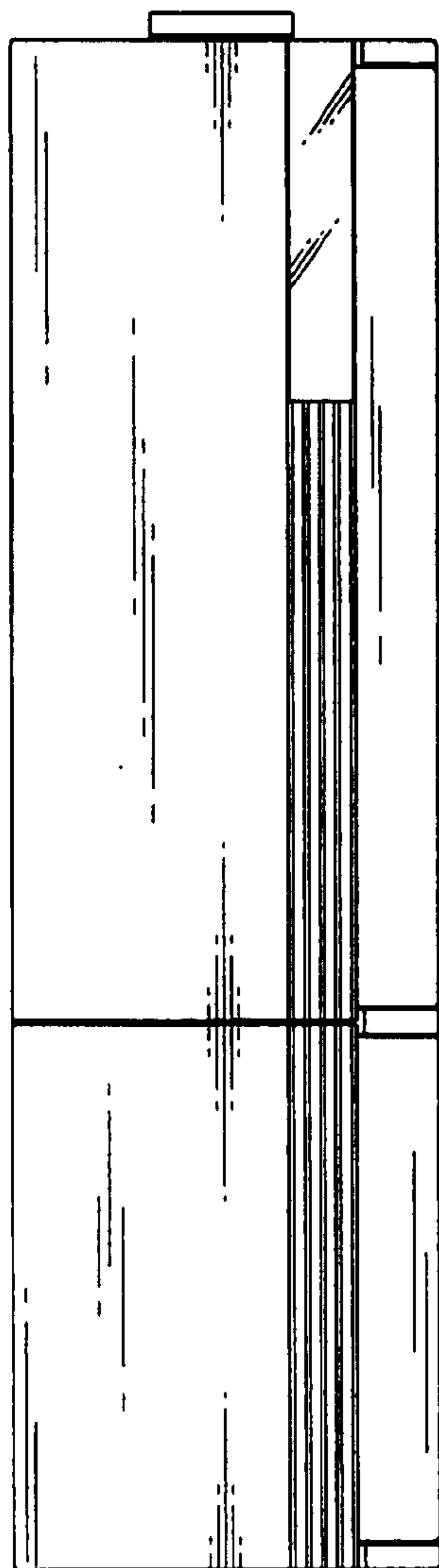


FIG. 3

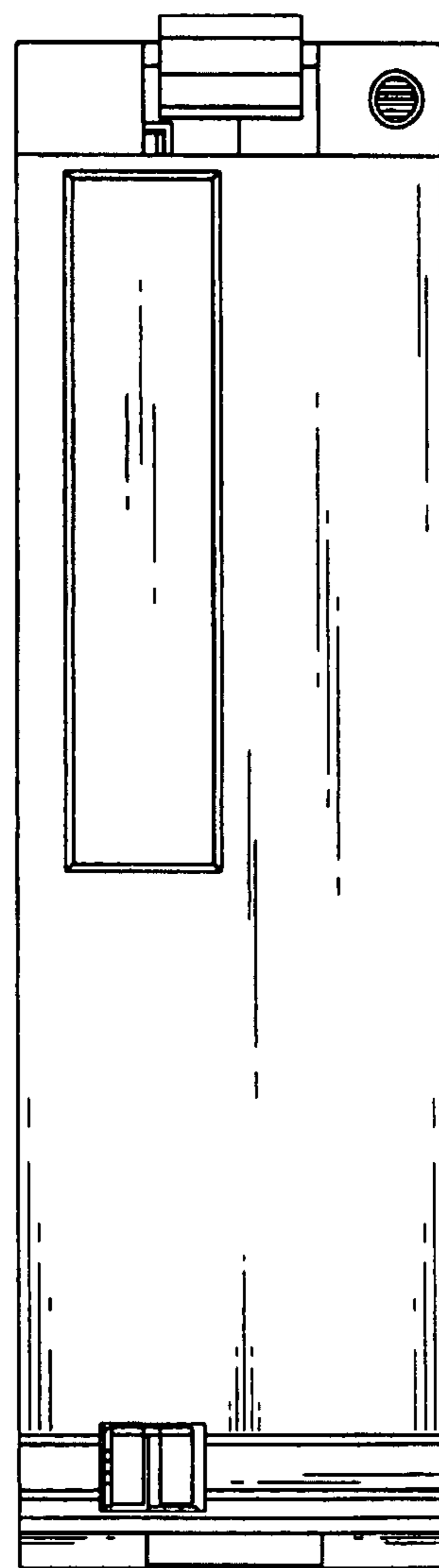


FIG. 4

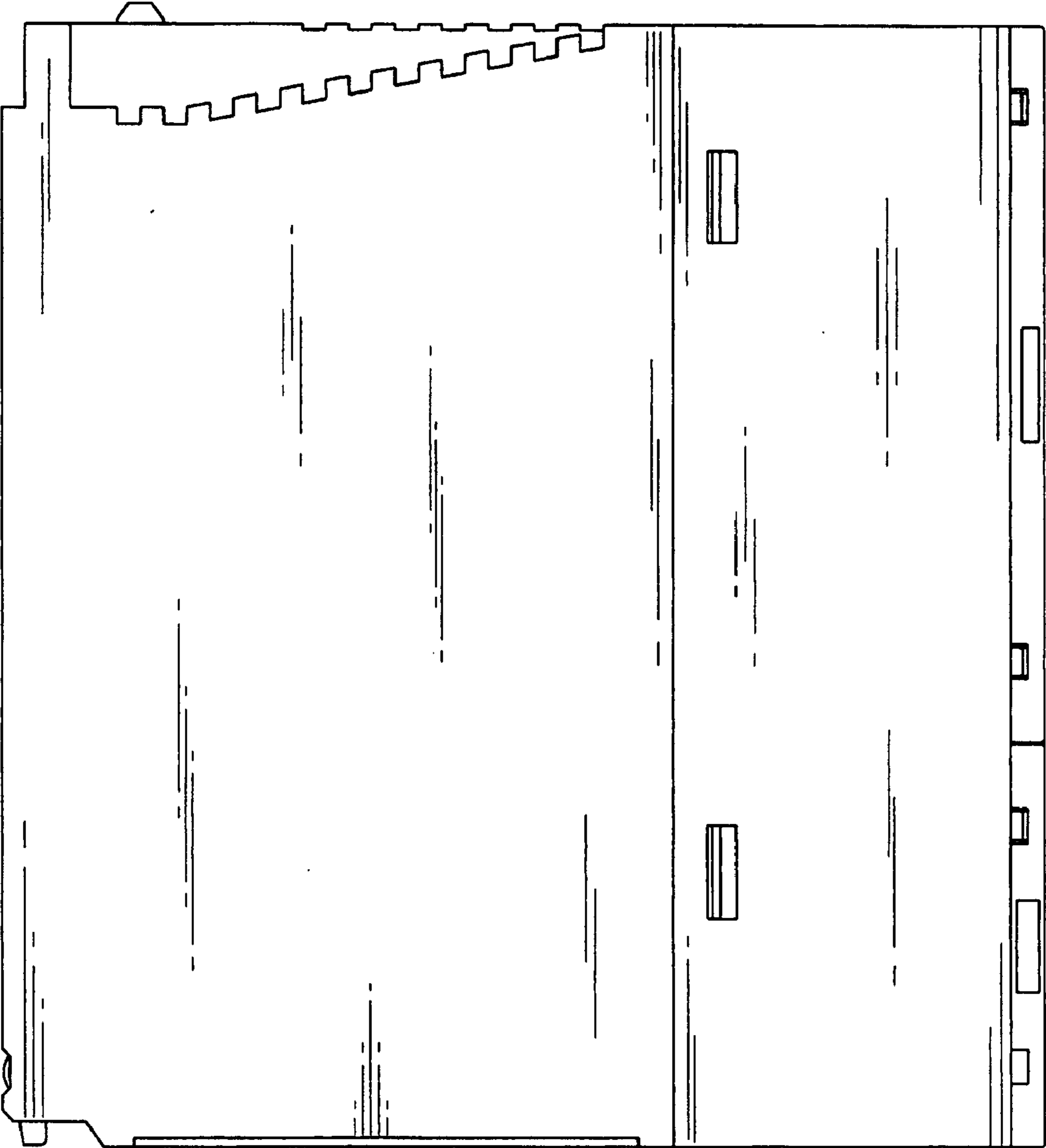


FIG. 5

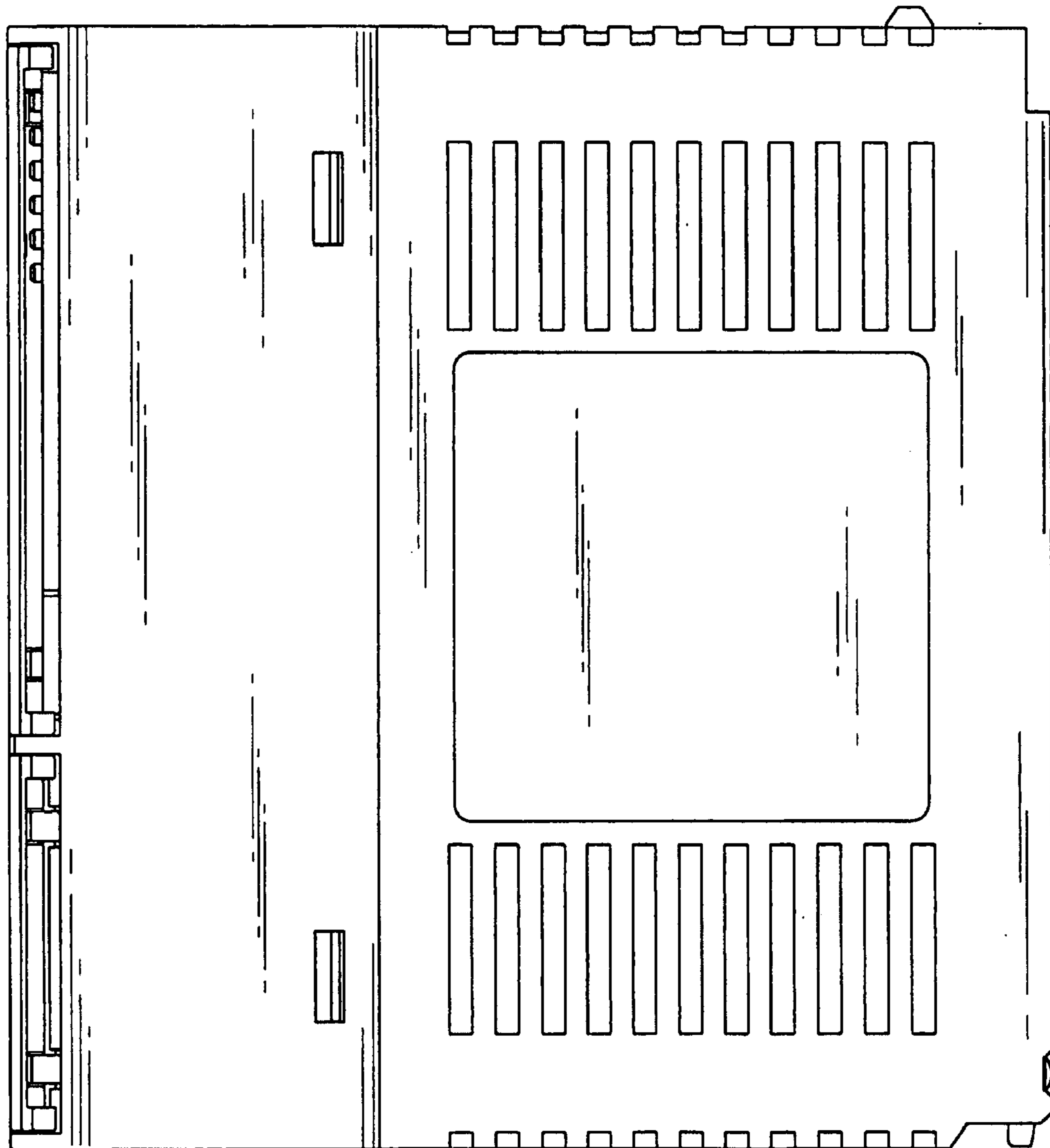


FIG. 6

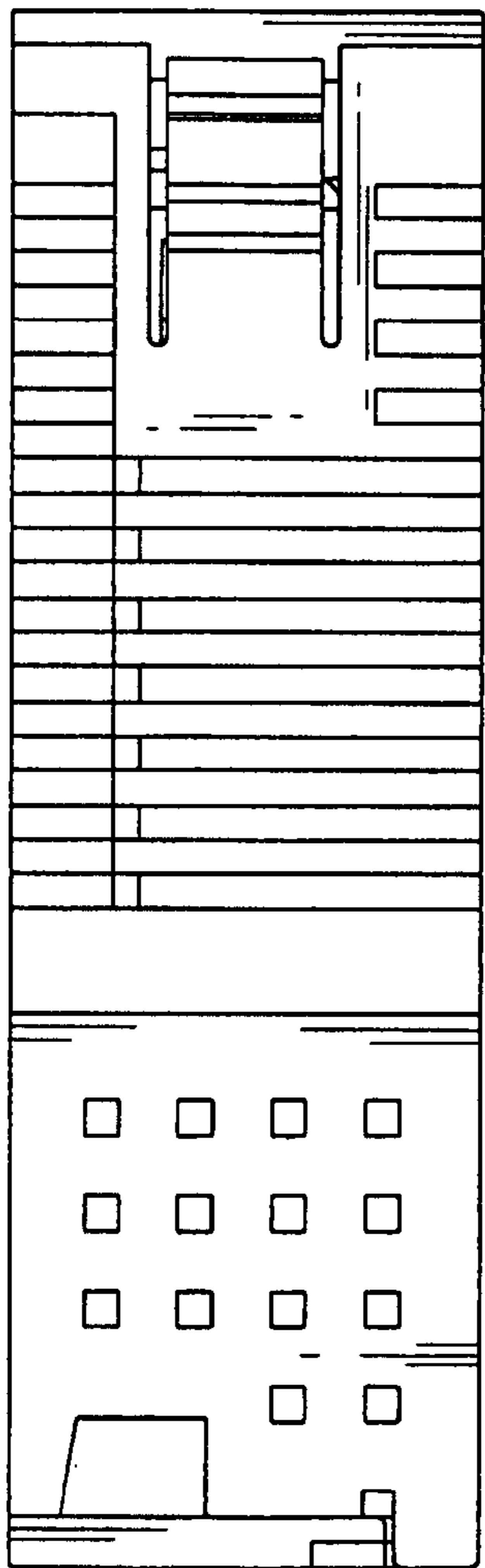


FIG. 7

