

(12) **United States Design Patent** (10) **Patent No.:** **US D520,954 S**  
**Moriwake et al.** (45) **Date of Patent:** **\*\* May 16, 2006**

(54) **ELECTRICAL CONNECTOR**

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(73) Assignee: **J.S.T. Mfg. Co., Ltd.**, Osaka (JP)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/205,628**

(22) Filed: **May 17, 2004**

**Related U.S. Application Data**

(62) Division of application No. 29/163,078, filed on Jun. 25, 2002, now Pat. No. Des. 491,892.

(30) **Foreign Application Priority Data**

Jan. 8, 2002 (JP) ..... 2002-119  
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Jan. 8, 2002 (JP) ..... 2002-121  
Jan. 8, 2002 (JP) ..... 2002-118

(51) **LOC (8) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/147**

(58) **Field of Classification Search** ..... D13/146,  
D13/147; 439/63, 78, 79, 108, 188, 540.1,  
439/540.5, 544, 553, 557, 607-610, 638,  
439/620, 676, 717, 677-681

See application file for complete search history.

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(Continued)

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(74) *Attorney, Agent, or Firm*—W. F. Fasse; W. G. Fasse

(57) **CLAIM**

The ornamental design for an electrical connector, as shown and described.

**DESCRIPTION**

FIG. 1 is a front elevation view of an electrical connector showing our new design;

FIG. 2 is a rear elevation view of the electrical connector;

FIG. 3 is a top plan view of the electrical connector;

FIG. 4 is a bottom plan view of the electrical connector;

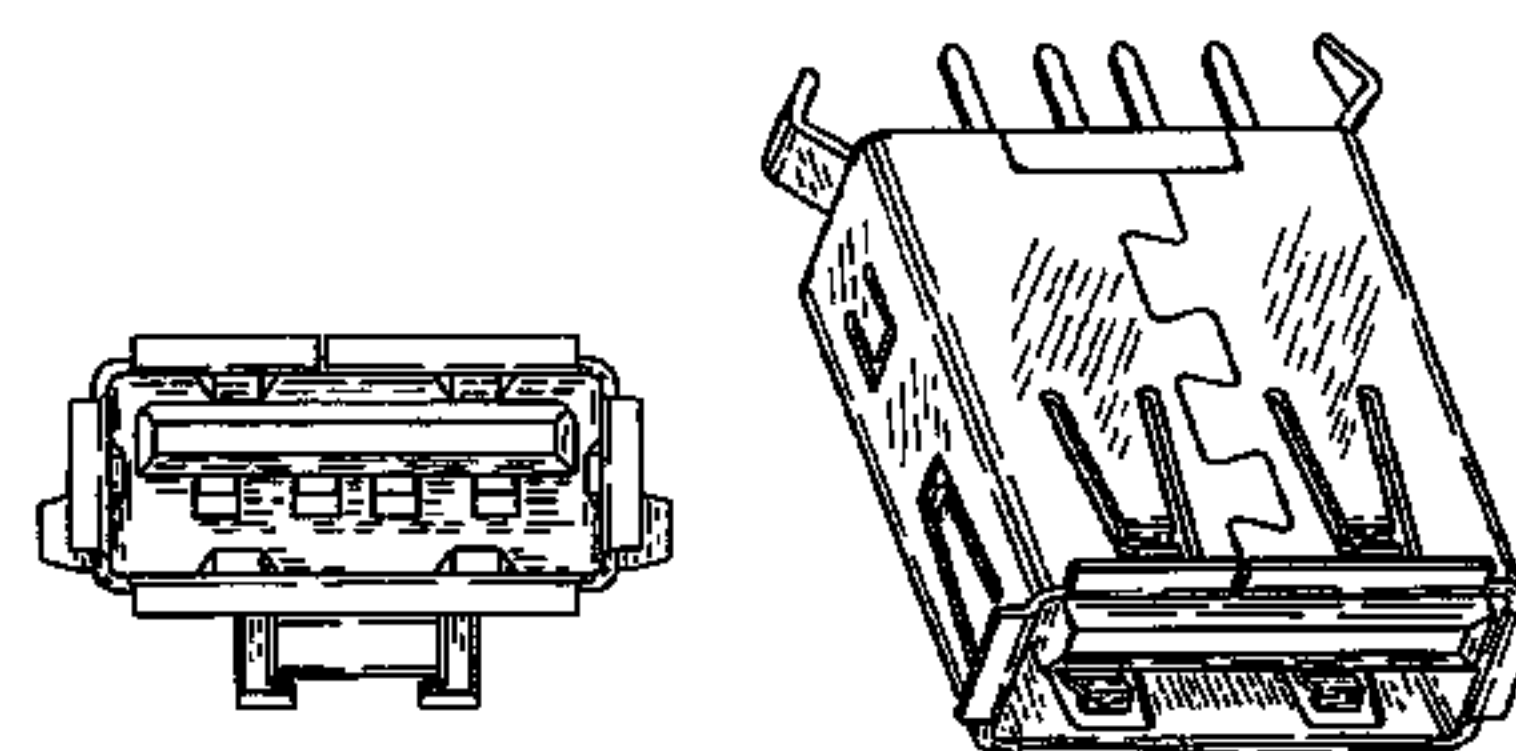
FIG. 5 is a left side elevation view of the electrical connector;

FIG. 6 is a right side elevation view of the electrical connector;

FIG. 7 is a perspective view of the front, top and left side of the electrical connector; and,

FIG. 8 is a reduced top plan view of the electrical connector in a connecting condition. A printed circuit board and a counterpart electrical connector are drawn in broken lines for illustrative purposes only and form no part of the claimed design.

**1 Claim, 4 Drawing Sheets**



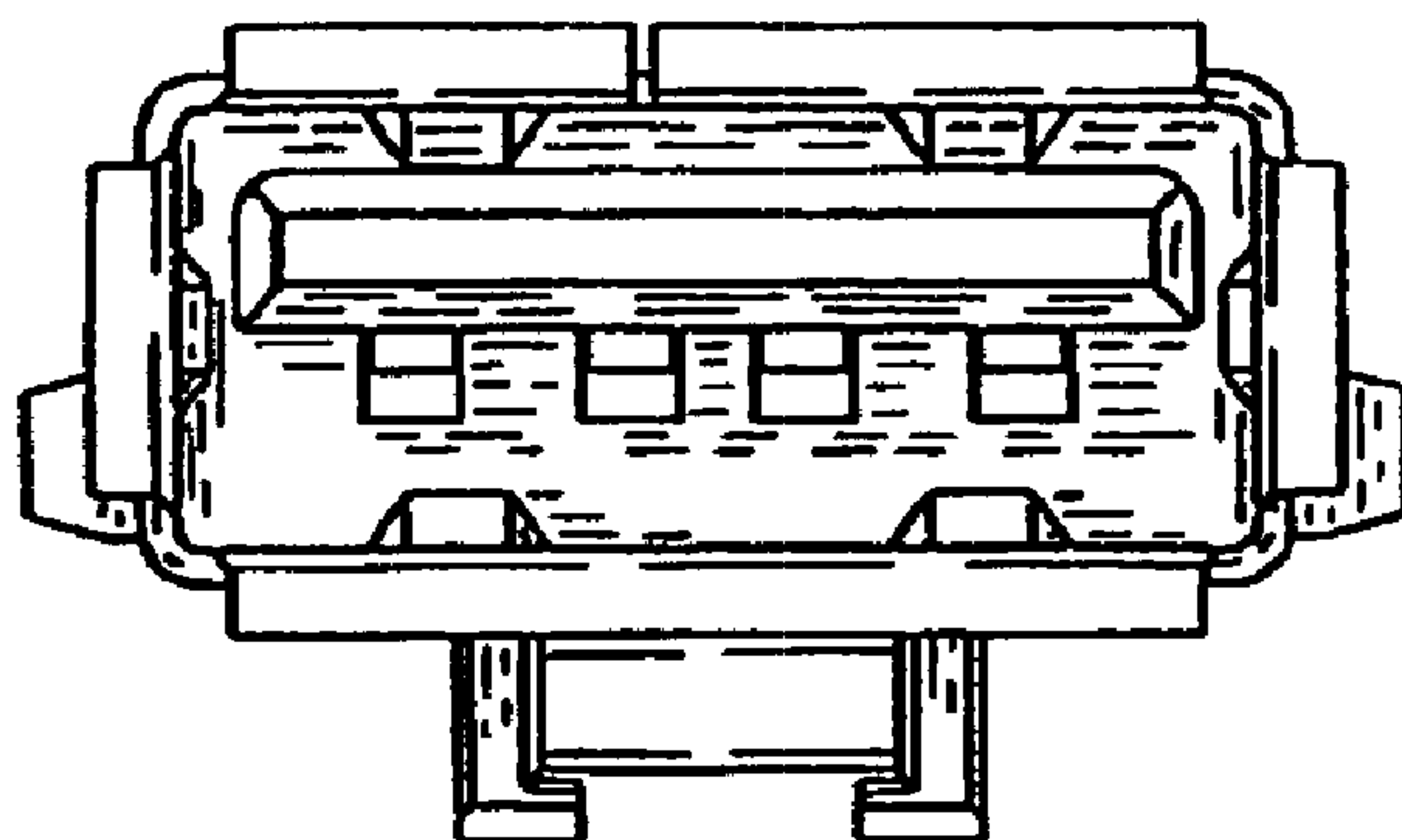
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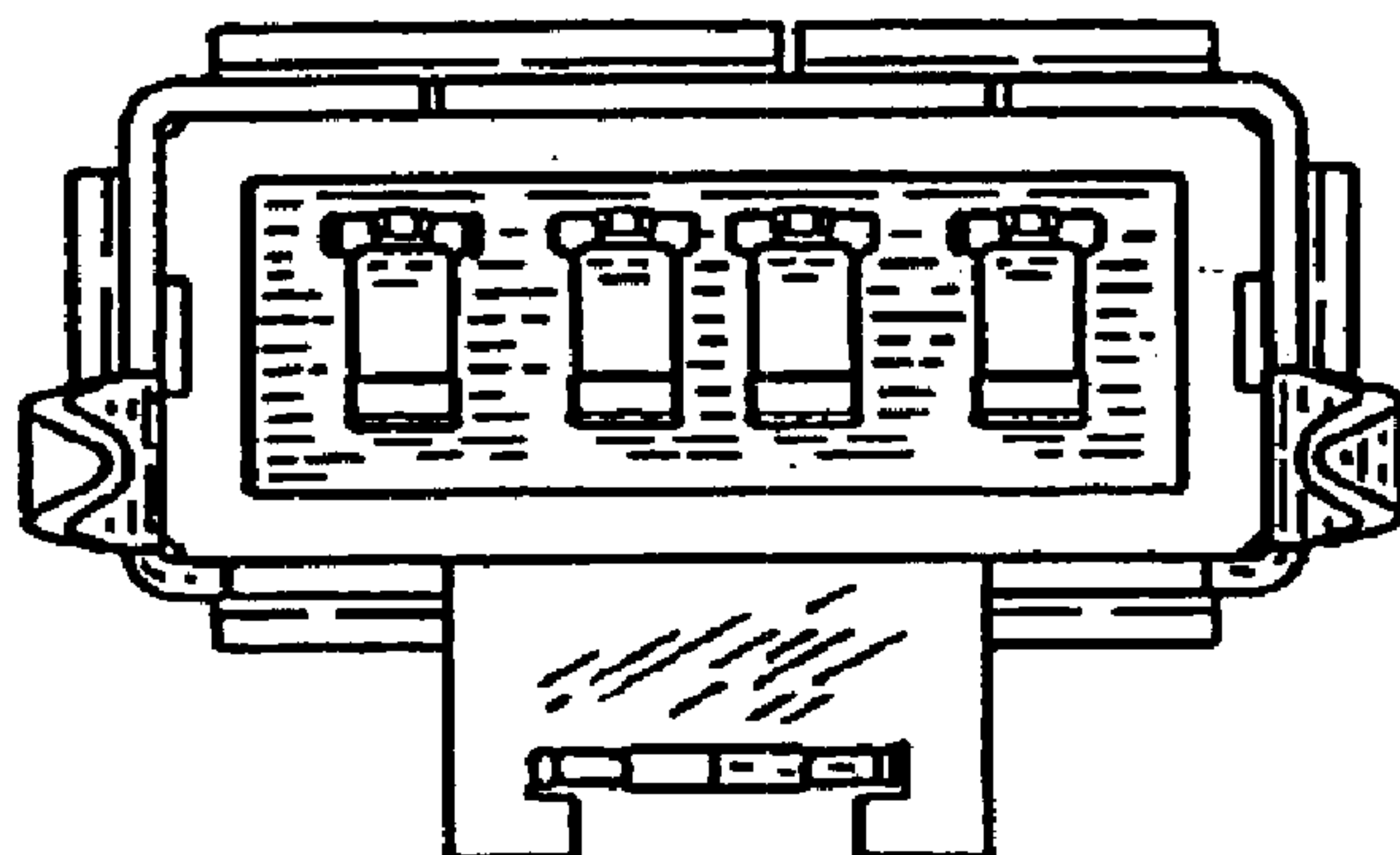
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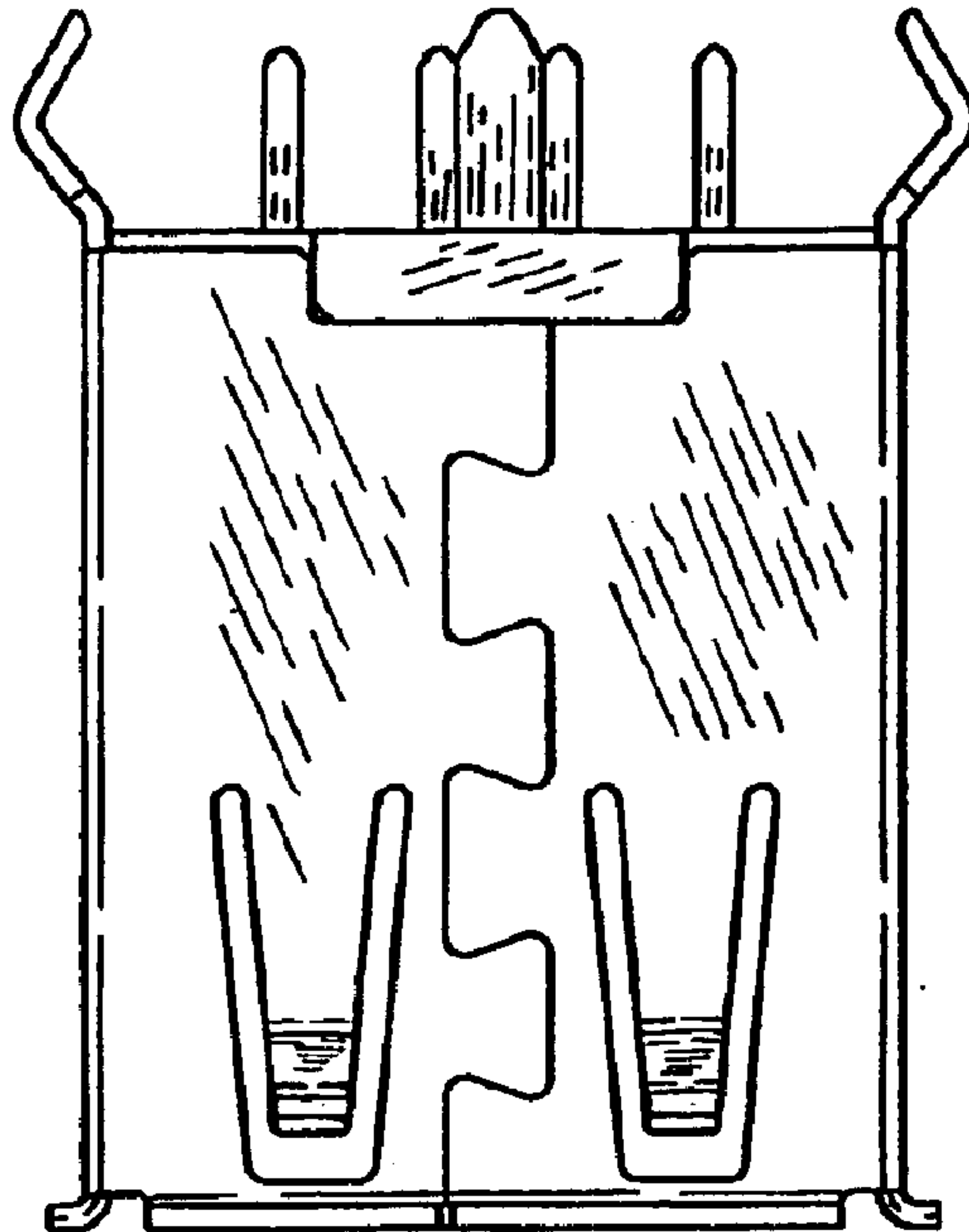
**FIG. 1**



**FIG. 2**



**FIG. 3**



**FIG. 4**

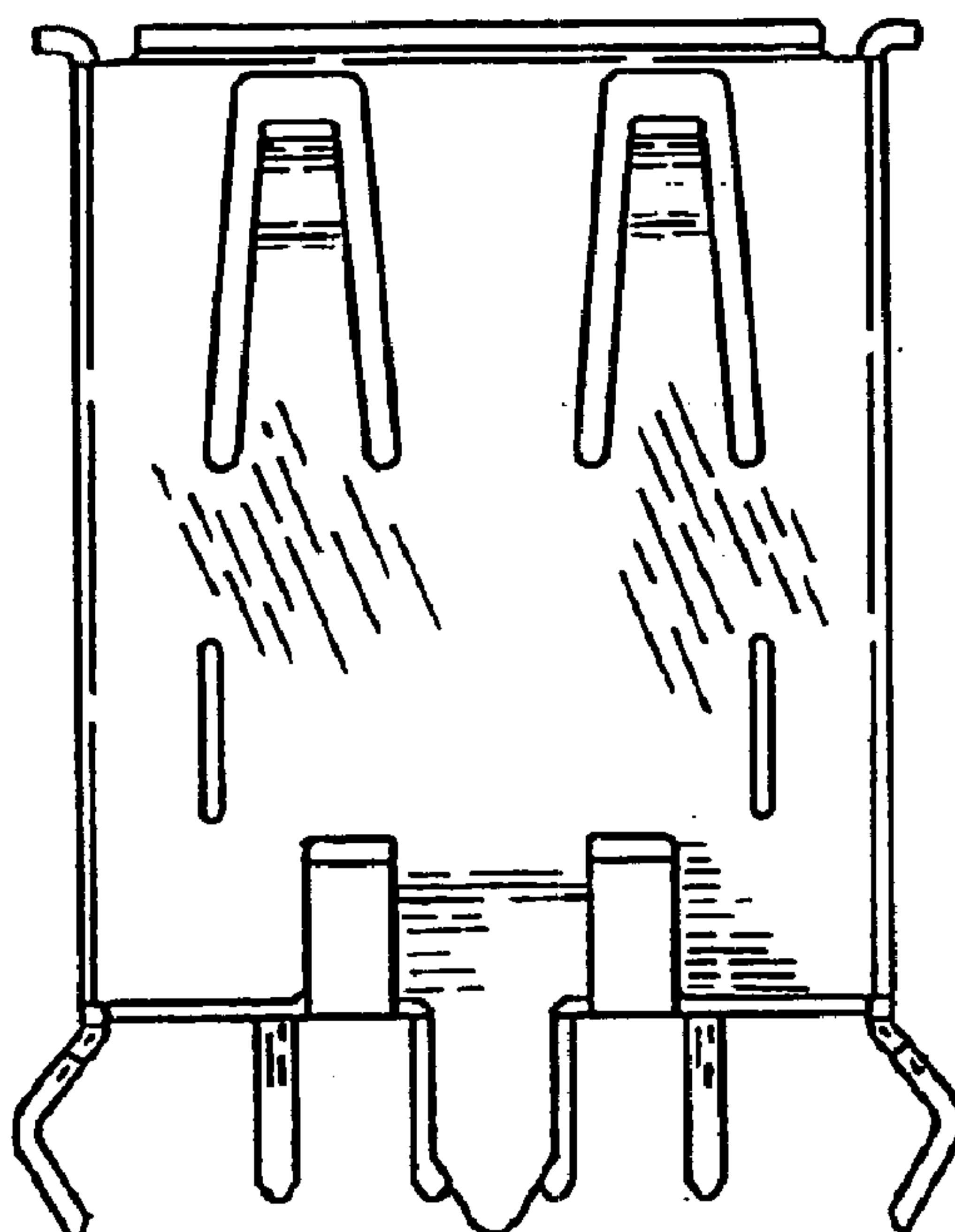


FIG. 5

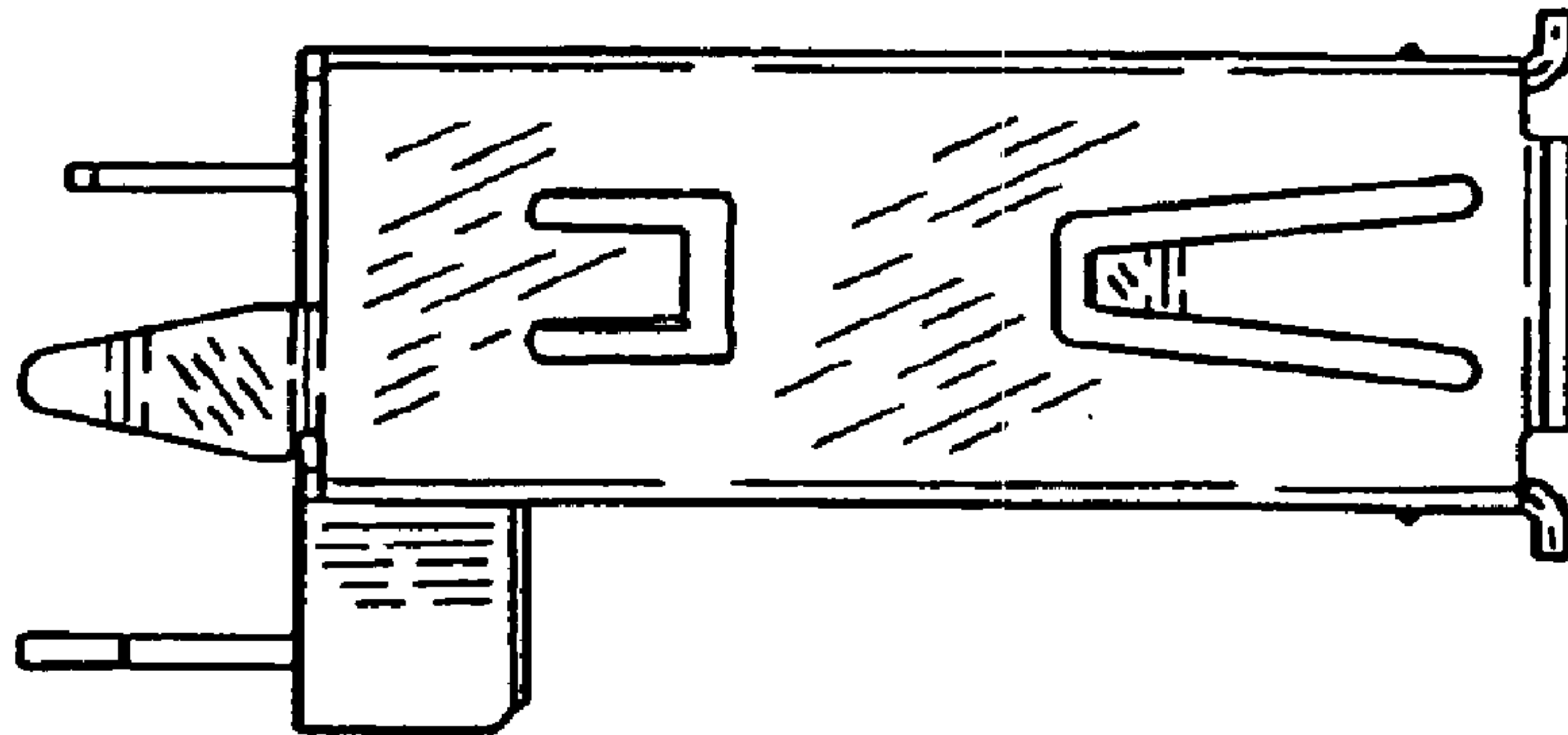


FIG. 6

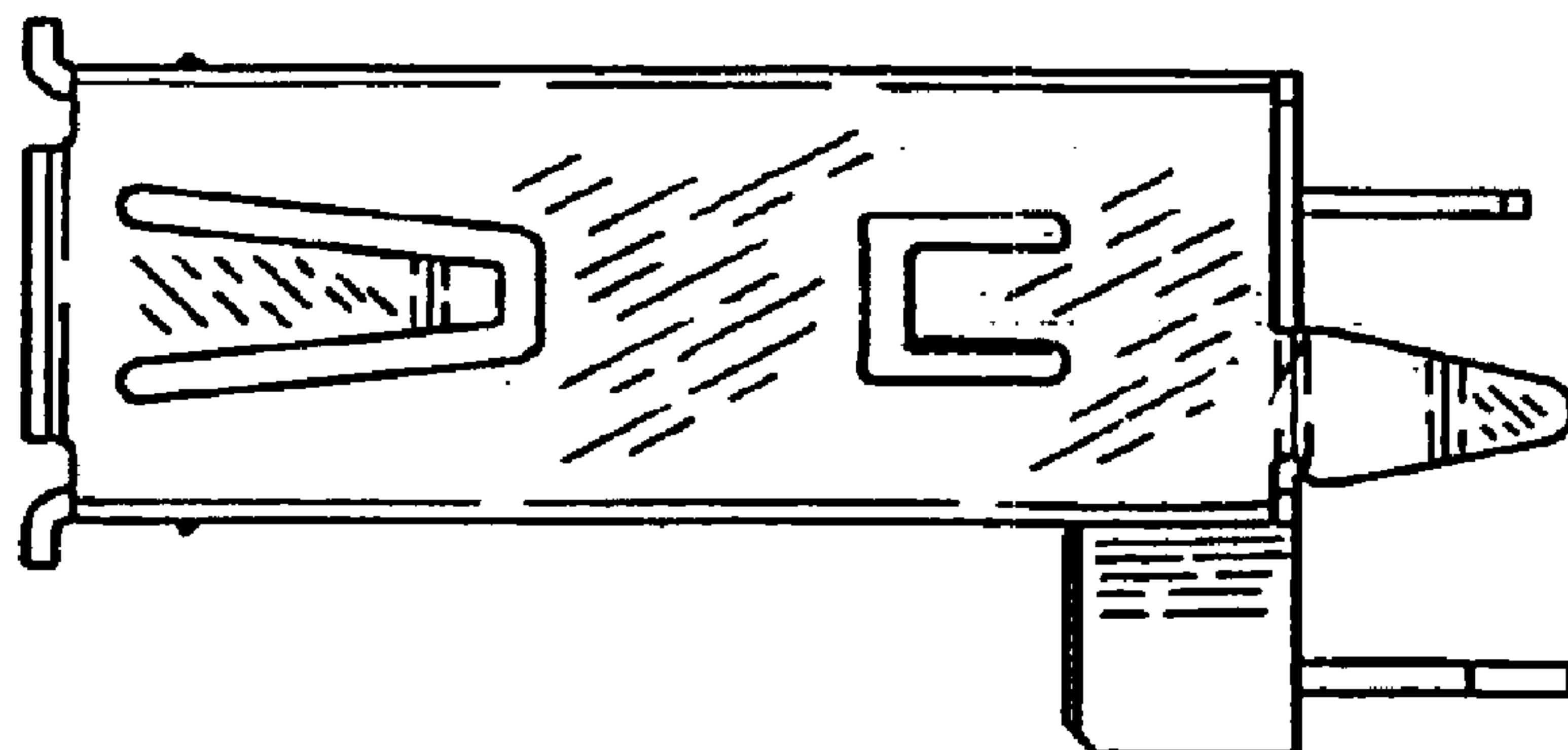


FIG. 7

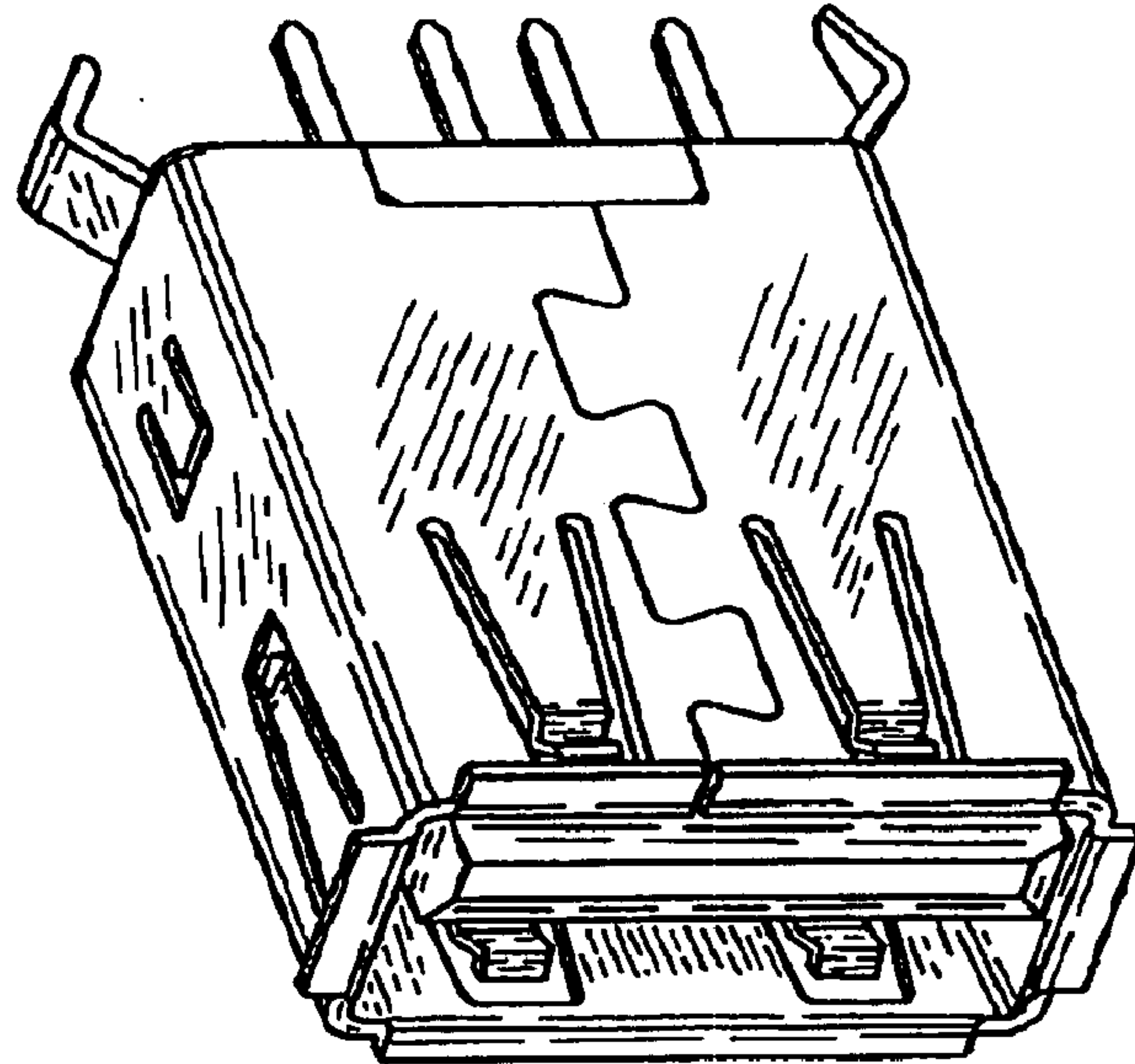
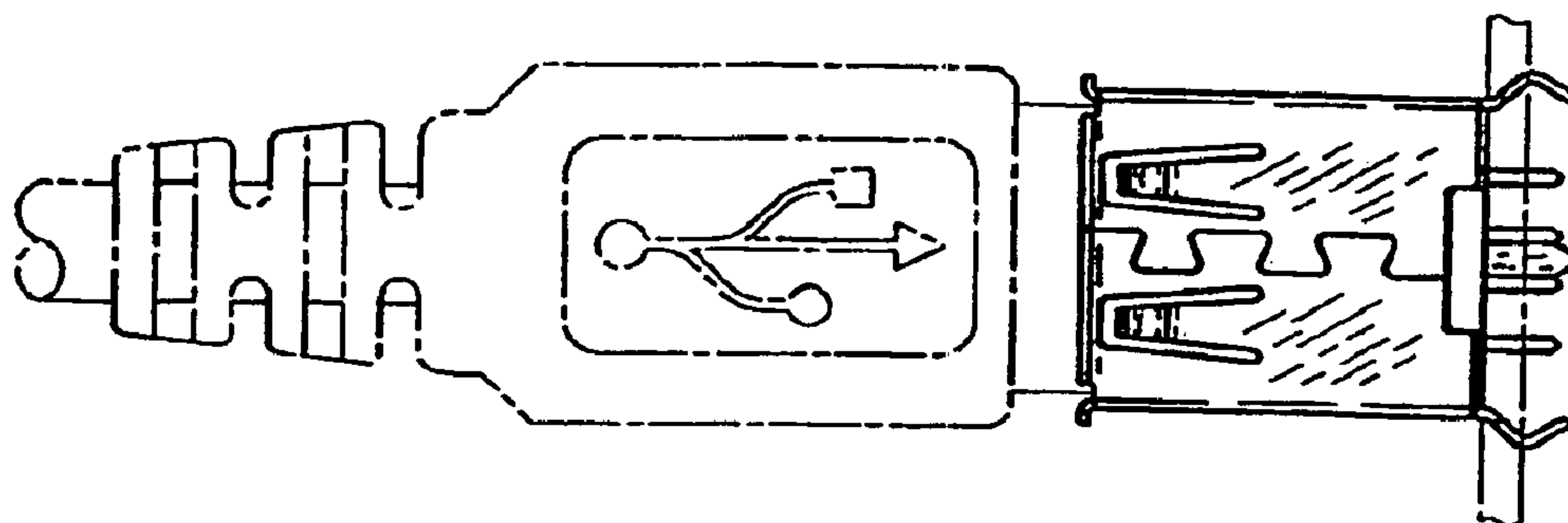


FIG. 8





UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : Des. 520,954 S  
APPLICATION NO. : 29/205628  
DATED : May 16, 2006  
INVENTOR(S) : Moriwake et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page.

Item [75], Inventors,

Line 1, replace "Izumioticsu" by --Izumioticsu-shi--;

Line 2, replace "Nissin" by --Nissin-shi--;

Under "OTHER PUBLICATIONS",

Line 2, after "Electronic Co.", replace ":" by --;--;

Under "CLAIM",

Line 1, after "connector" delete --;--;

Page 2.

Under "OTHER PUBLICATIONS",

Line 2, after "2.0", replace ":" by --;--;

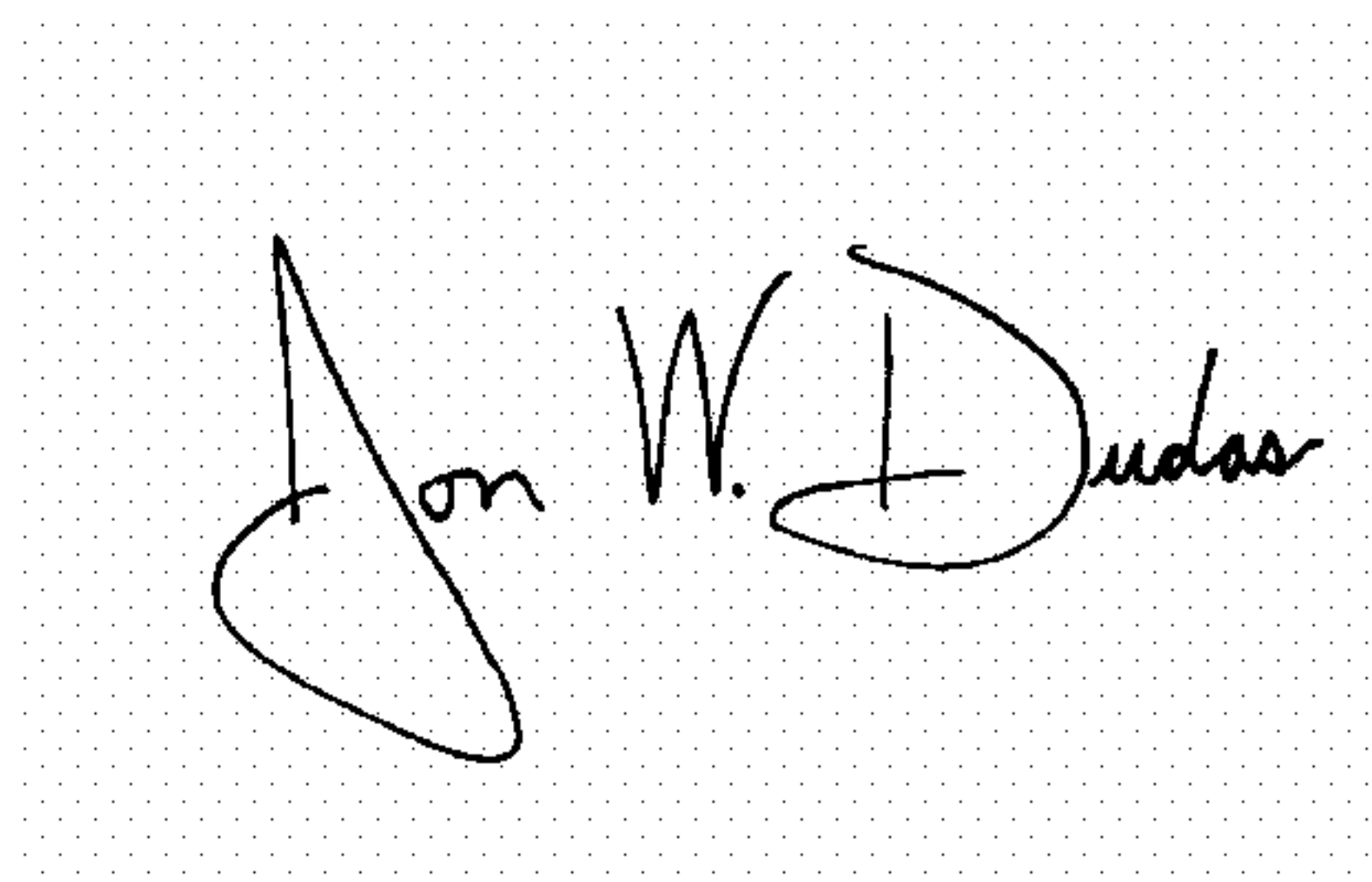
after "pp.", replace "82" by --85--;

Line 9, after "Series", replace ":" by --;--;

Line 11, after "Public", replace "known" by --Known--.

Signed and Sealed this

Twelfth Day of December, 2006

A handwritten signature in black ink, reading "Jon W. Dudas", is written over a rectangular area with a light gray dot grid background.

JON W. DUDAS

*Director of the United States Patent and Trademark Office*