

US00D517428S

(12) **United States Design Patent**
Chang

(10) **Patent No.:** **US D517,428 S**
(45) **Date of Patent:** **** Mar. 21, 2006**

(54) **DIGITAL CLOCK**

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(**) **Term:** **14 Years**

(21) **Appl. No.:** **29/200,331**

(22) **Filed:** **Feb. 26, 2004**

(30) **Foreign Application Priority Data**

Oct. 17, 2003 (HK) 0311766

(51) **LOC (8) Cl.** **10-01**

(52) **U.S. Cl.** **D10/3; D10/15**

(58) **Field of Classification Search** D10/1-29,
D10/122-132; 368/276-278, 280, 284, 285,
368/286, 316, 317, 294, 10, 82-84, 28-30,
368/239-242, 72, 73, 228, 232, 223
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a digital clock, as shown and
described.

DESCRIPTION

FIG. 1 is a top perspective view of a digital clock showing
my new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

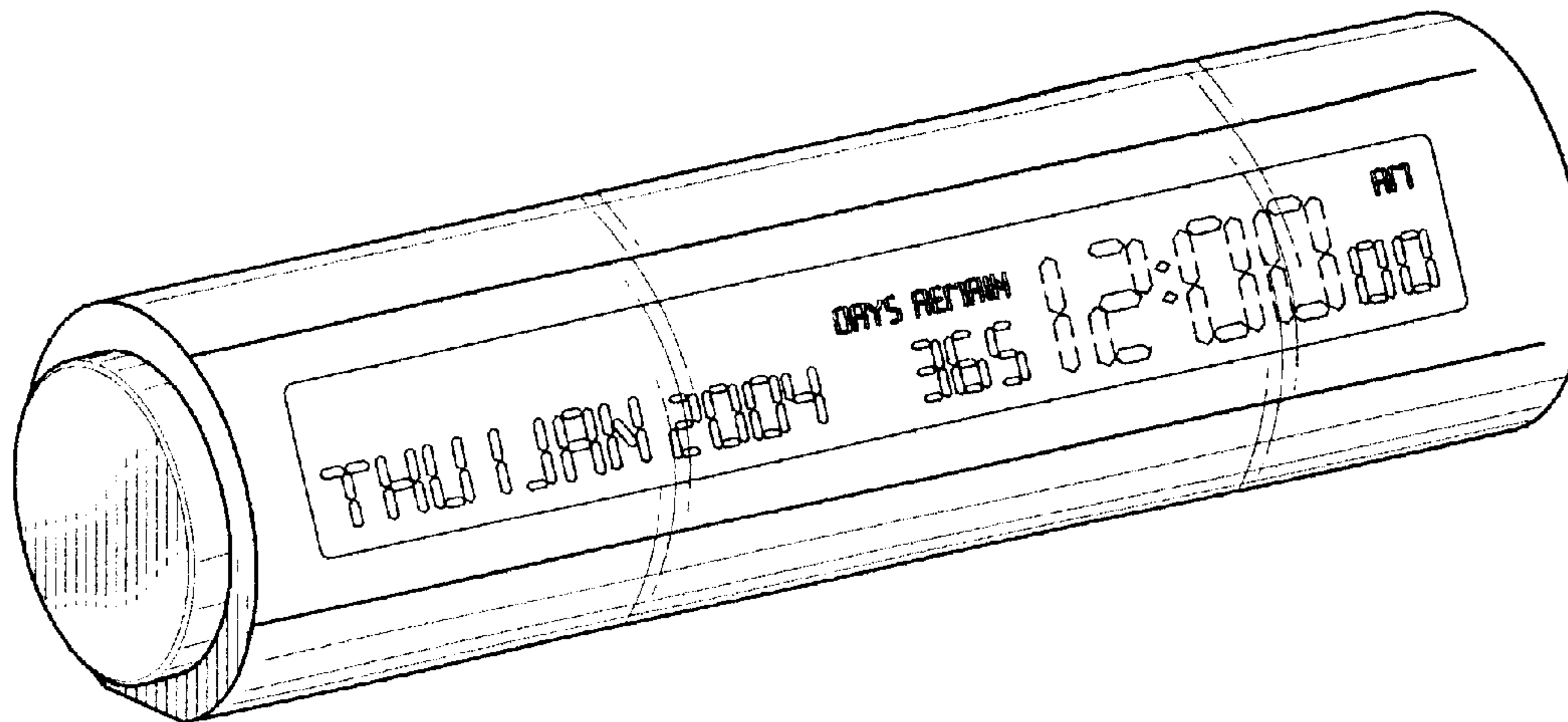
FIG. 4 is a top plan view thereof;

FIG. 5 is a bottom plan view thereof;

FIG. 6 is a left end elevational view thereof; and,

FIG. 7 is a right end elevational view thereof.

1 Claim, 2 Drawing Sheets



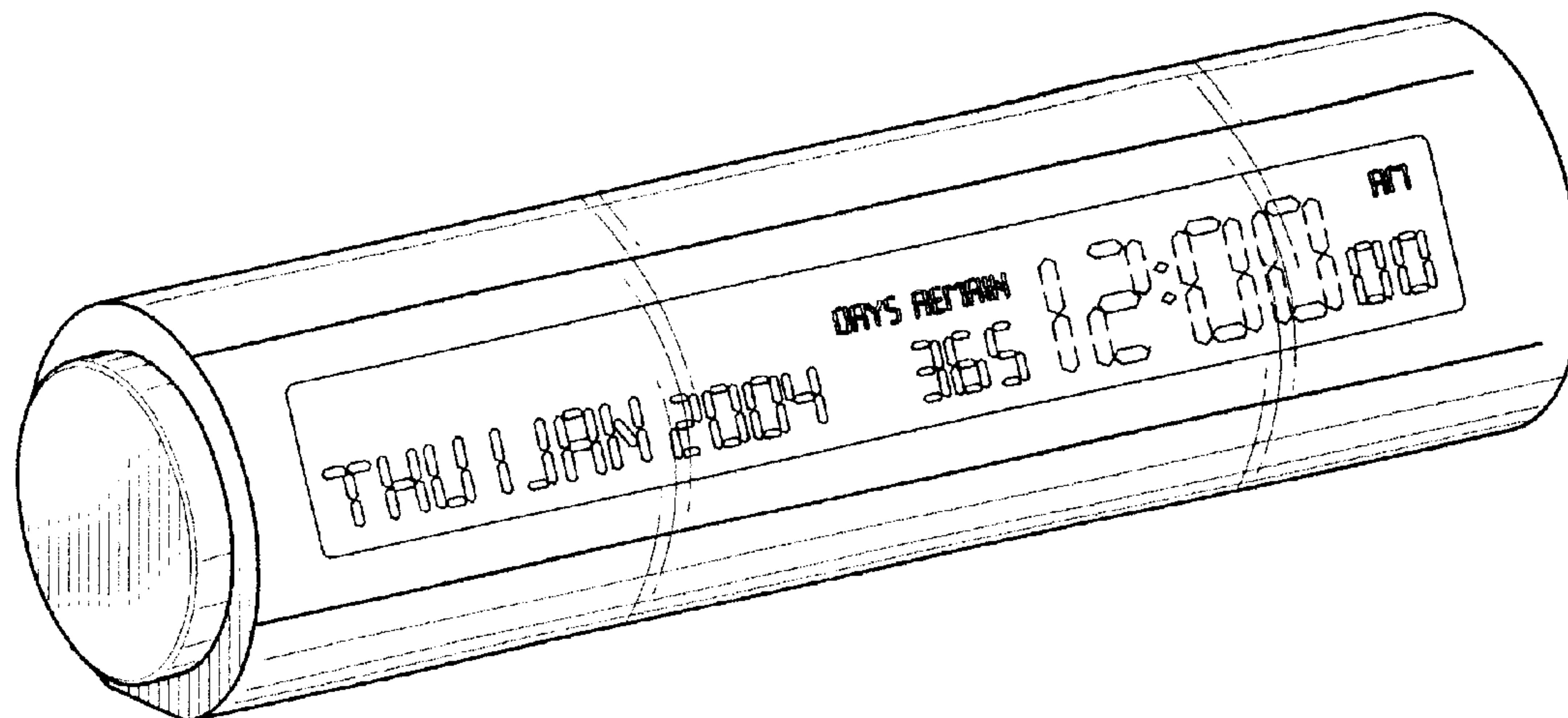


FIG. 1

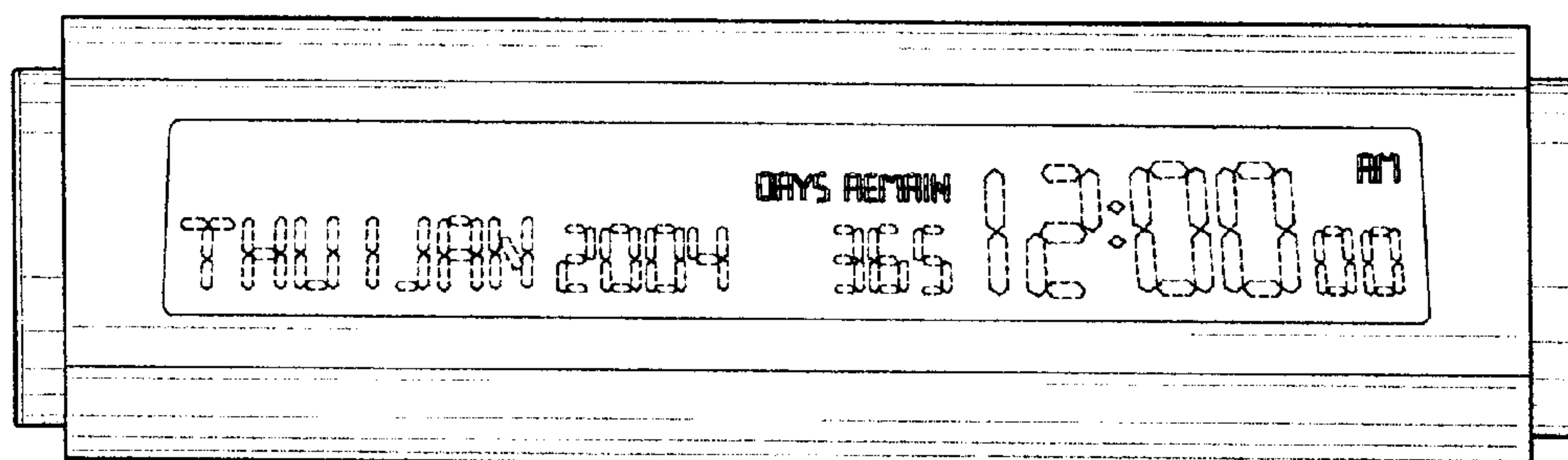


FIG. 2

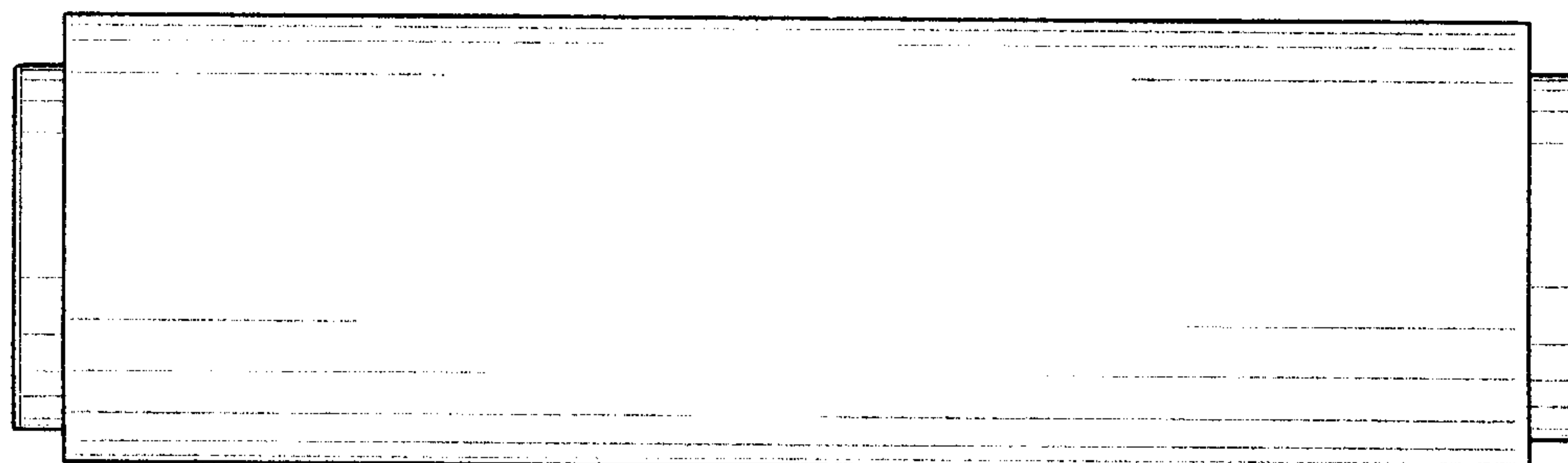


FIG. 3

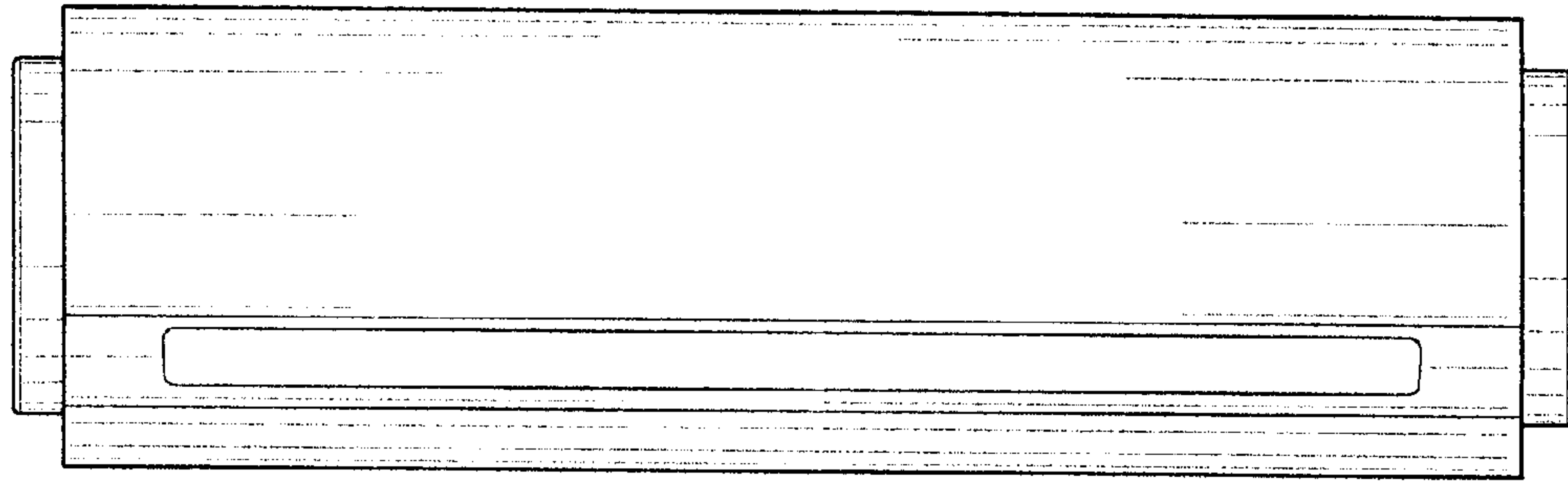


FIG. 4

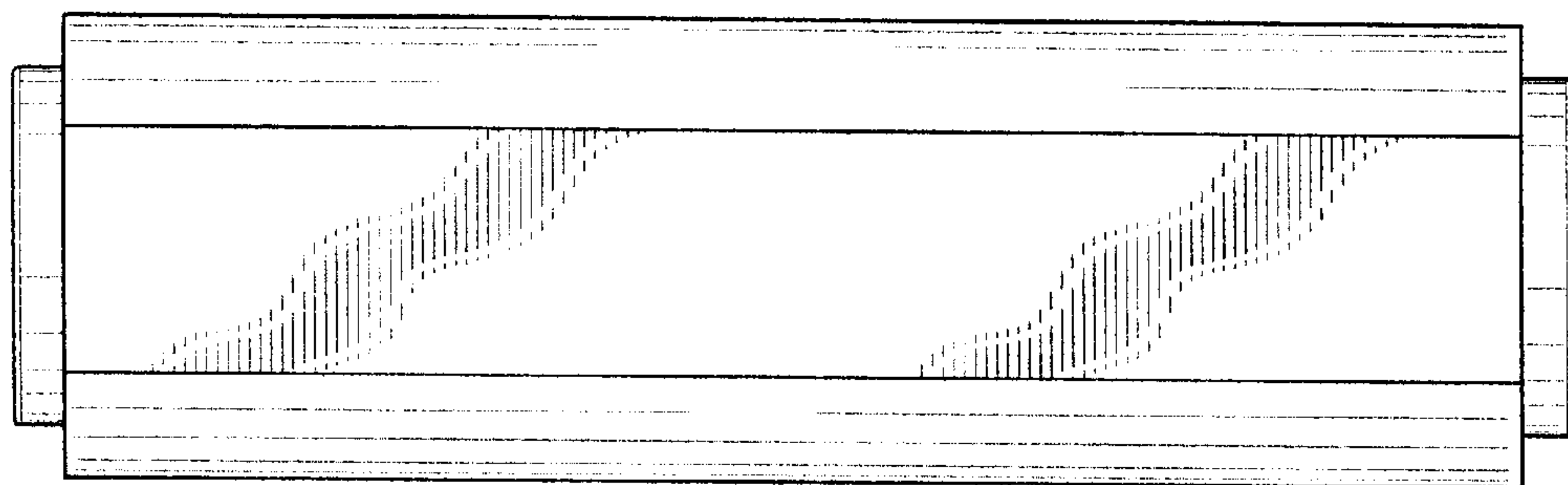


FIG. 5

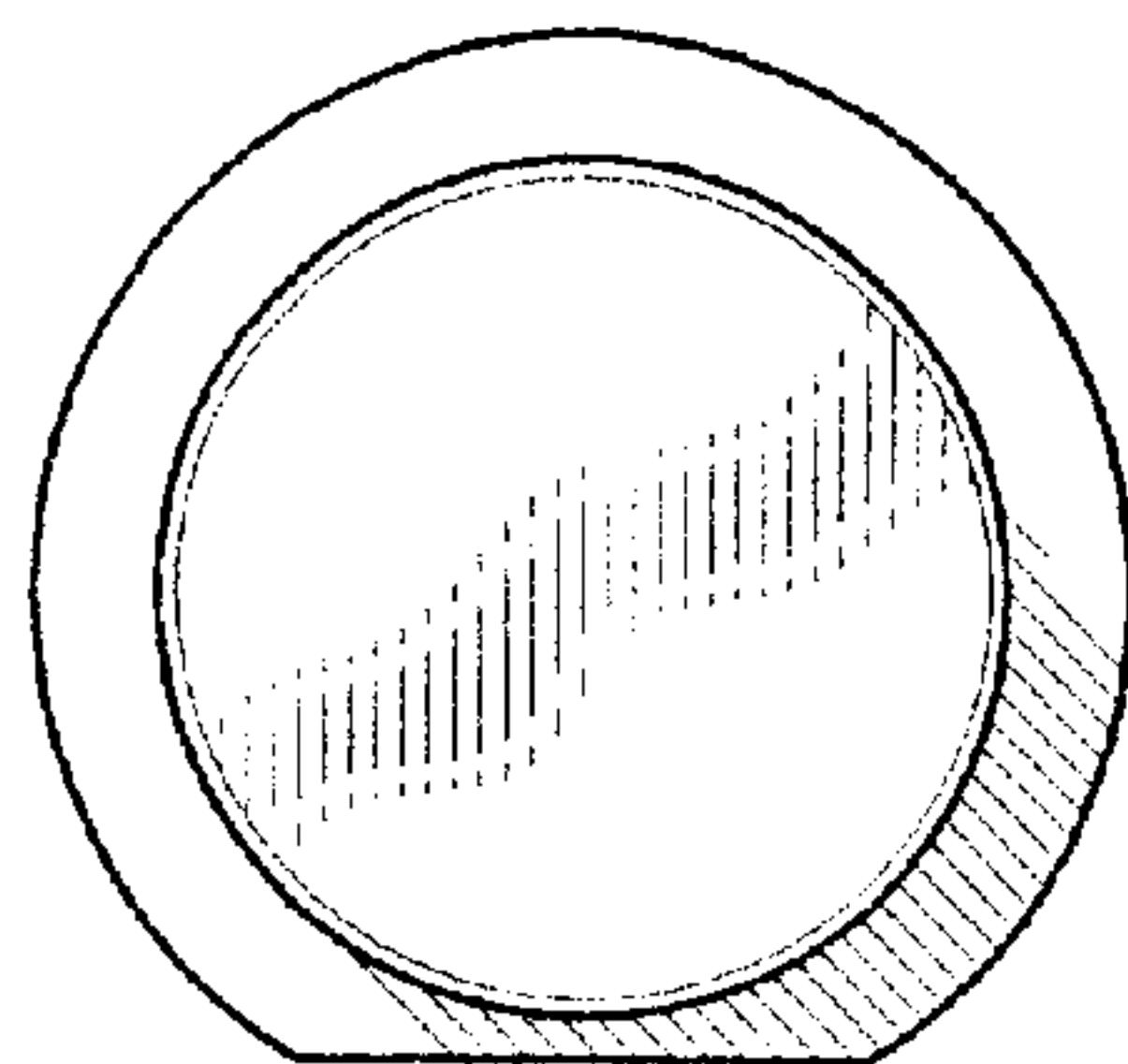


FIG. 6

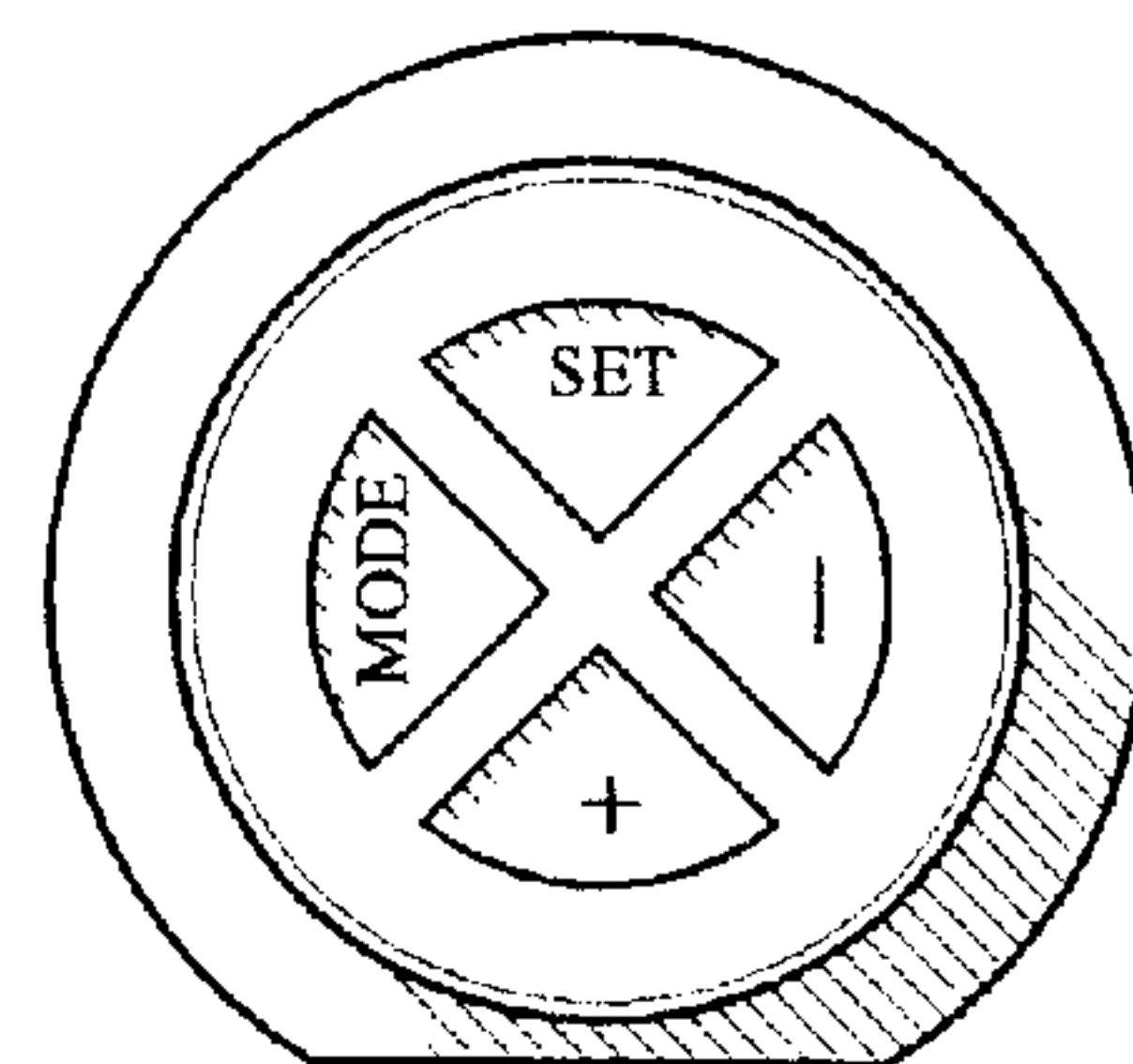


FIG. 7