



US00D516552S

(12) **United States Design Patent**
Iseki

(10) **Patent No.:** **US D516,552 S**
(45) **Date of Patent:** **** Mar. 7, 2006**

(54) **COMPUTER**

D494,580 S * 8/2004 Kondo D14/318
D495,691 S * 9/2004 Takada et al. D14/318

(75) Inventor: **Daisuke Iseki**, Tokyo (JP)

OTHER PUBLICATIONS

(73) Assignee: **Sony Corporation**, Tokyo (JP)

Web page "Sony eCatalog, VGN-S71PB" http://www.ecat.sony.co.jp/computer/vaio_pc/products/index.cfm?PD=19001&KM=VNG-S71PB, 1 sheet, Sony (Oct. 2004).

(**) Term: **14 Years**

* cited by examiner

(21) Appl. No.: **29/218,363**

(22) Filed: **Dec. 2, 2004**

Primary Examiner—Freda S. Nunn

(51) **LOC (8) Cl.** **14-02**

(52) **U.S. Cl.** **D14/318**

(58) **Field of Classification Search** D14/315-323;
D18/1, 7; 235/145 A, 145 R; 345/104, 156,
345/168; 361/683

(74) *Attorney, Agent, or Firm*—Rader, Fishman & Grauer PLLC

See application file for complete search history.

(57) **CLAIM**

The ornamental design for a computer, as shown.

(56) **References Cited**

DESCRIPTION

U.S. PATENT DOCUMENTS

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FIG. 1 is a perspective view of a computer showing my new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a left side elevational view thereof;

FIG. 5 is a right side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

1 Claim, 4 Drawing Sheets

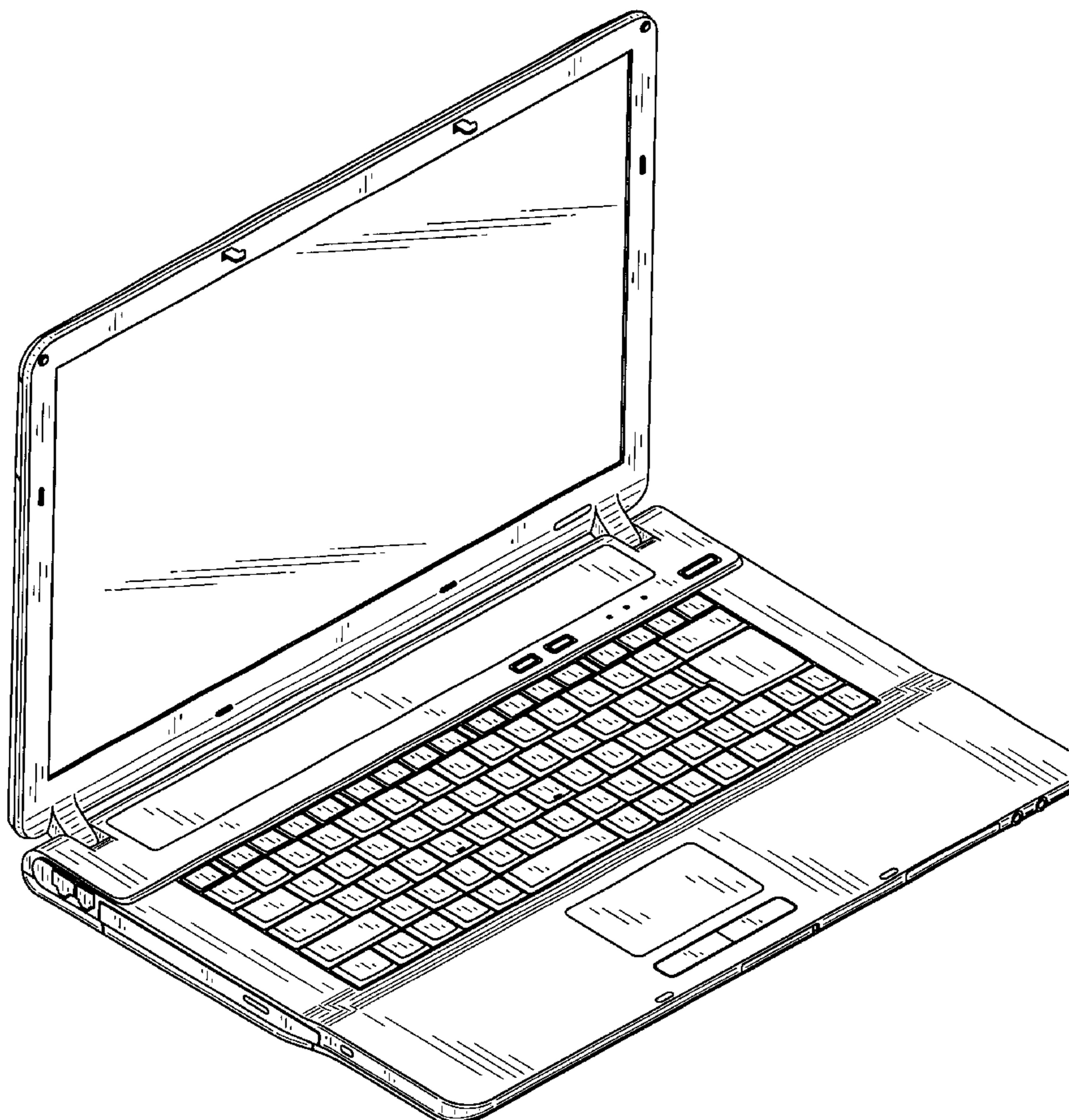


FIG. 1

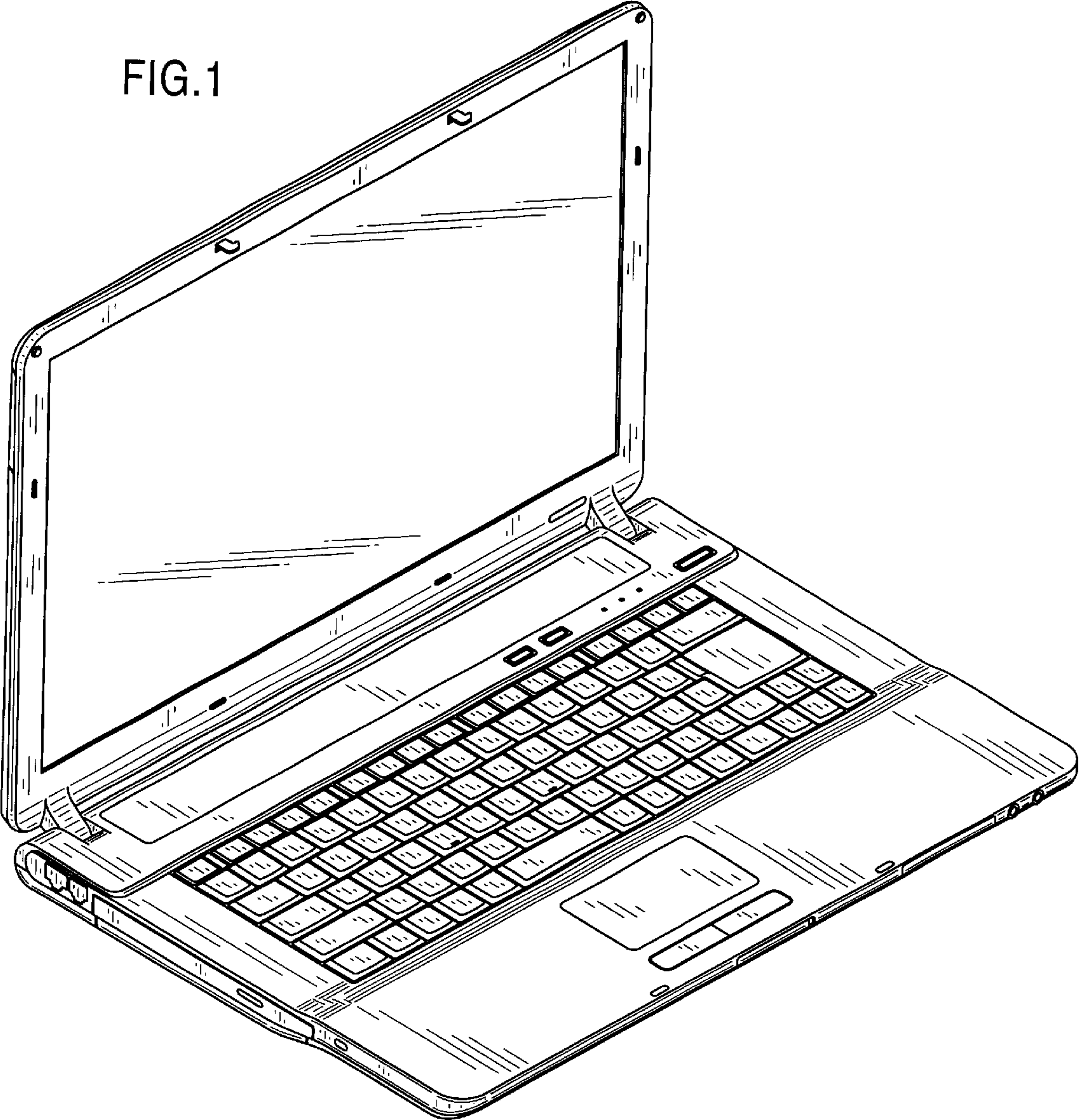


FIG.2

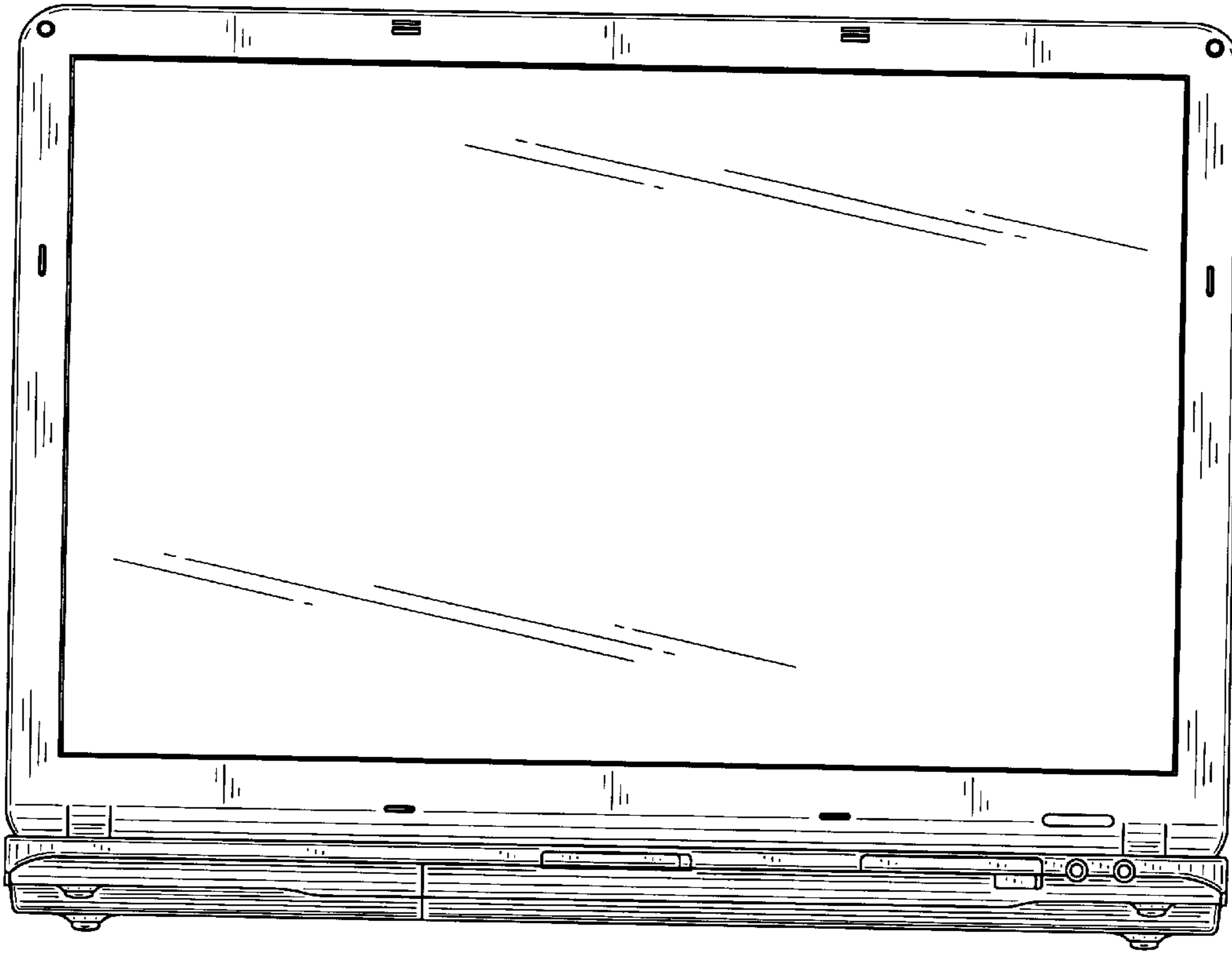


FIG.3

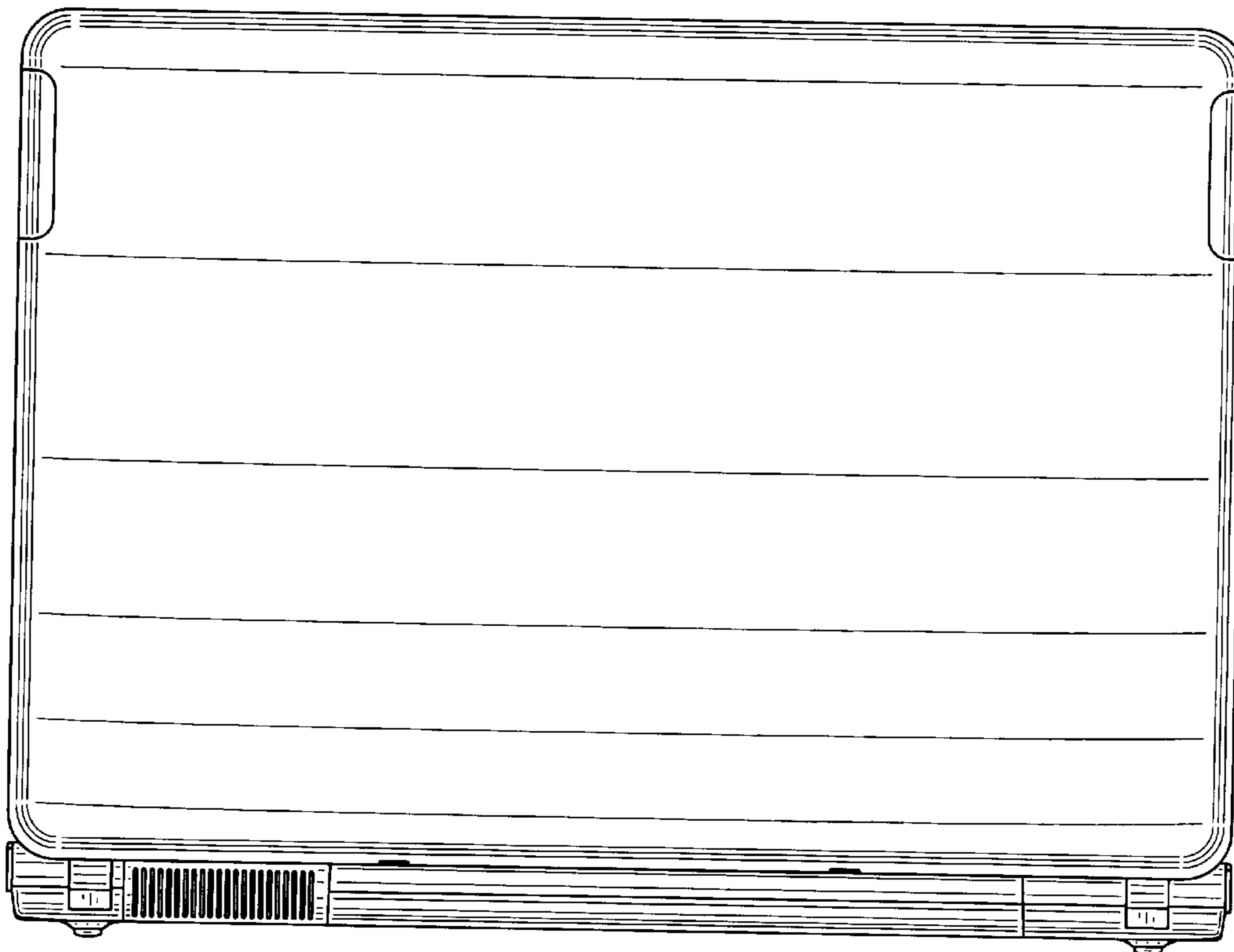


FIG.4

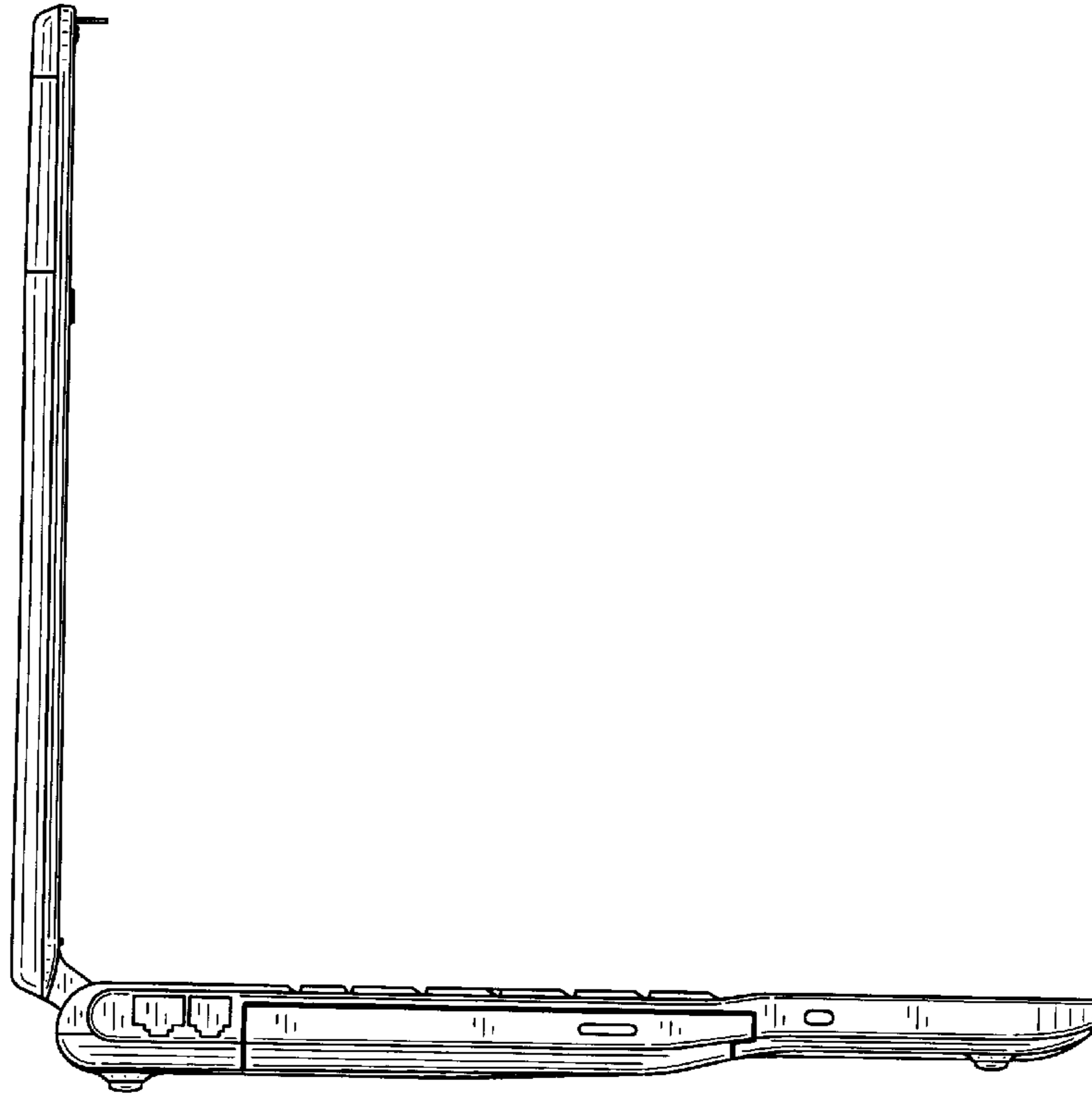


FIG.5

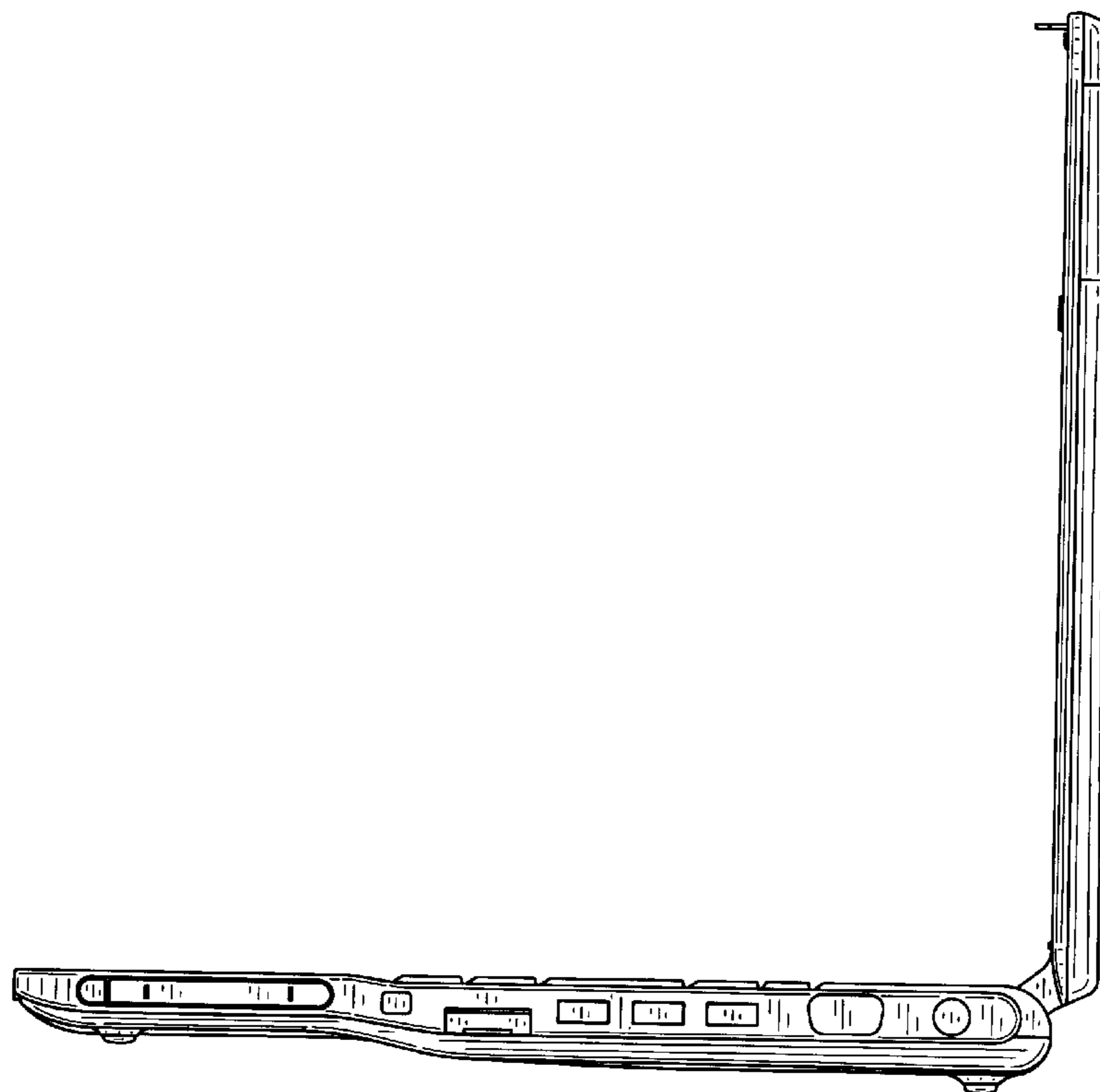


FIG.6

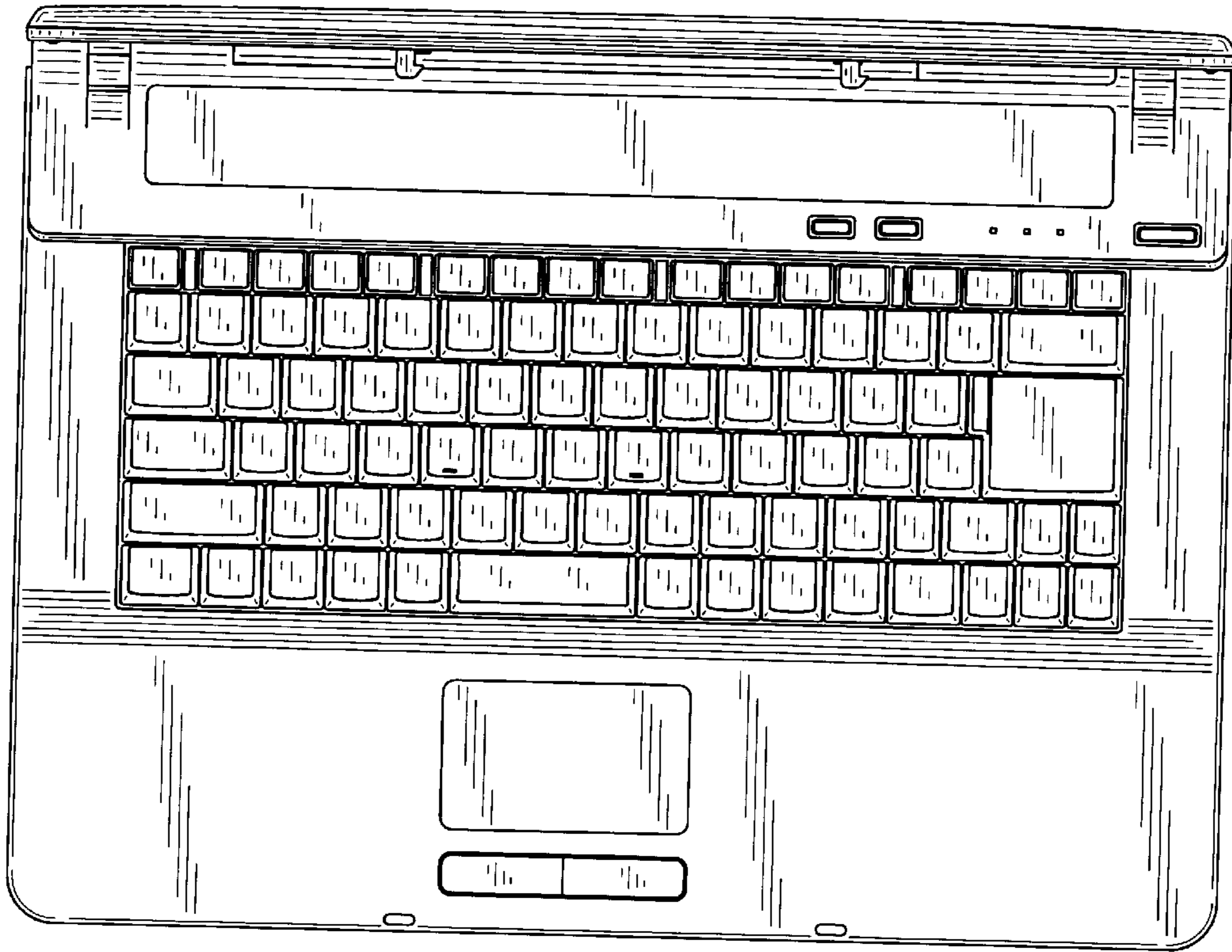


FIG.7

