



US00D514963S

(12) **United States Design Patent**
Shionoiri et al.

(10) **Patent No.:** **US D514,963 S**
(45) **Date of Patent:** **** Feb. 14, 2006**

(54) **VOLTAGE DETECTOR**

(75) Inventors: **Kenichi Shionoiri, Ueda (JP); Koichi Tanaka, Ueda (JP)**

(73) Assignee: **Hioki Denki Kabushiki Kaisha, Ueda (JP)**

(**) Term: **14 Years**

(21) Appl. No.: **29/215,342**

(22) Filed: **Oct. 19, 2004**

(30) **Foreign Application Priority Data**

Jun. 25, 2004 (JP) 2004-19037

(51) **LOC (8) Cl.** **10-04**

(52) **U.S. Cl.** **D10/78**

(58) **Field of Classification Search** D10/78;
324/72.5, 133, 156

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

5,363,045 A * 11/1994 Martin et al. 324/72.5
D410,203 S * 5/1999 Beha D10/78

5,952,820 A * 9/1999 Thrasher et al. 324/133
6,222,378 B1 * 4/2001 Campbell et al. 324/72.5
6,512,361 B1 * 1/2003 Becker 324/72.5
6,756,799 B1 * 6/2004 Seltzer 324/72.5
6,828,767 B2 * 12/2004 Douglas 324/72.5
D501,415 S * 2/2005 Seymour D10/78

* cited by examiner

Primary Examiner—Antoine D. Davis

(74) *Attorney, Agent, or Firm*—Manabu Kanesaka

(57) **CLAIM**

The ornamental design for a voltage detector, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a voltage detector showing our new design;
FIG. 2 is a front view thereof;
FIG. 3 is a rear view thereof;
FIG. 4 is a top plan view thereof;
FIG. 5 is a bottom view thereof;
FIG. 6 is a left side view thereof; and,
FIG. 7 is a right side view thereof.

1 Claim, 3 Drawing Sheets

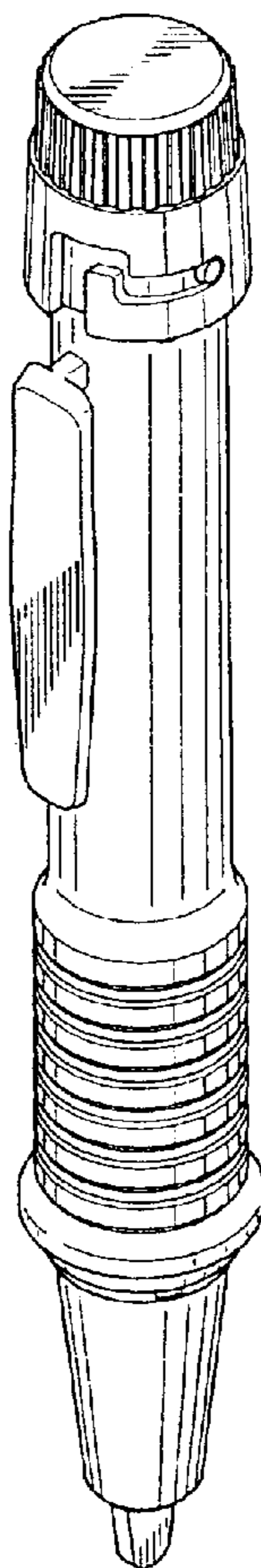


FIG.1

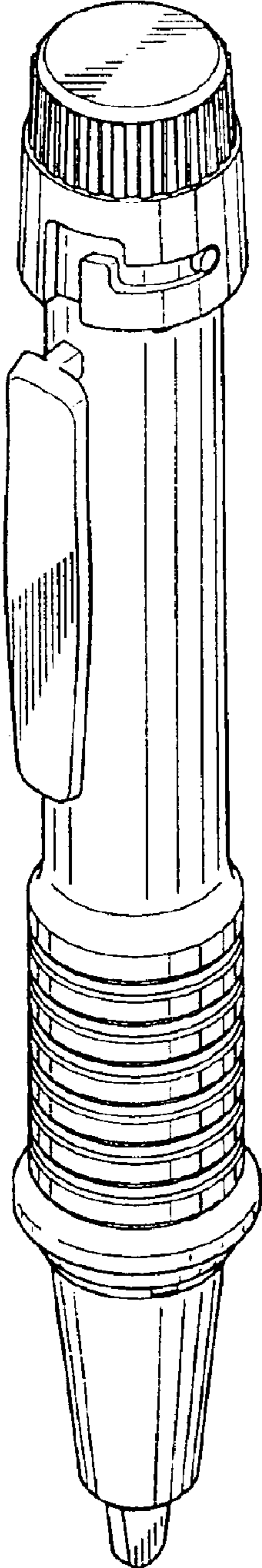


FIG.2

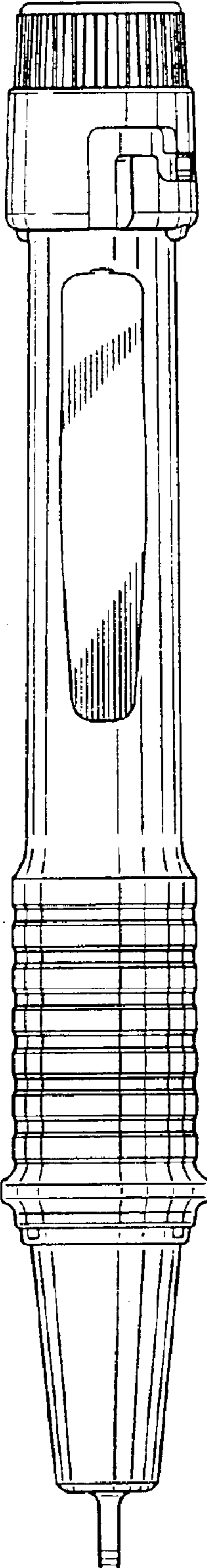


FIG.3

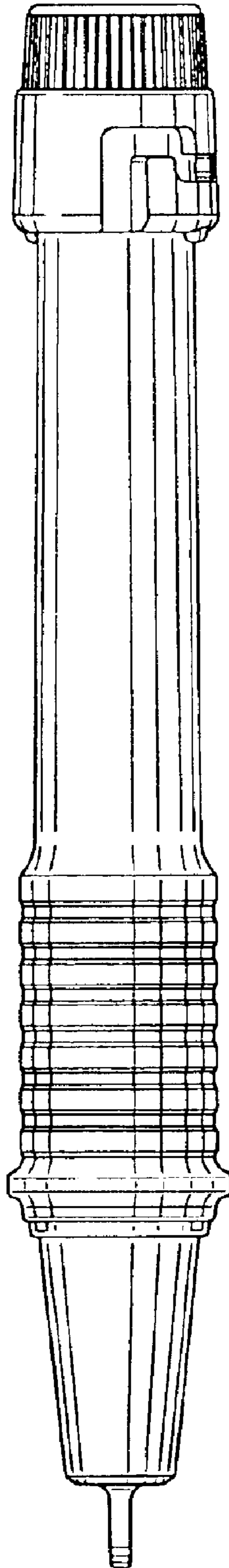


FIG.4

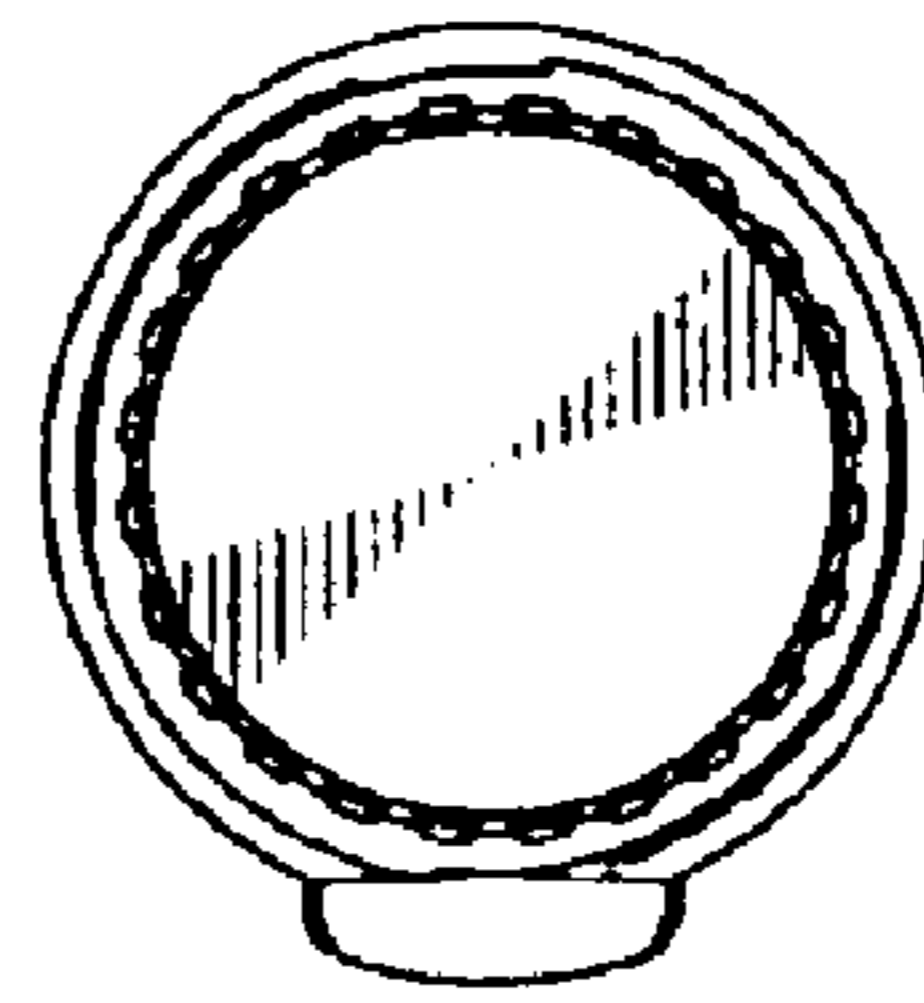


FIG.5

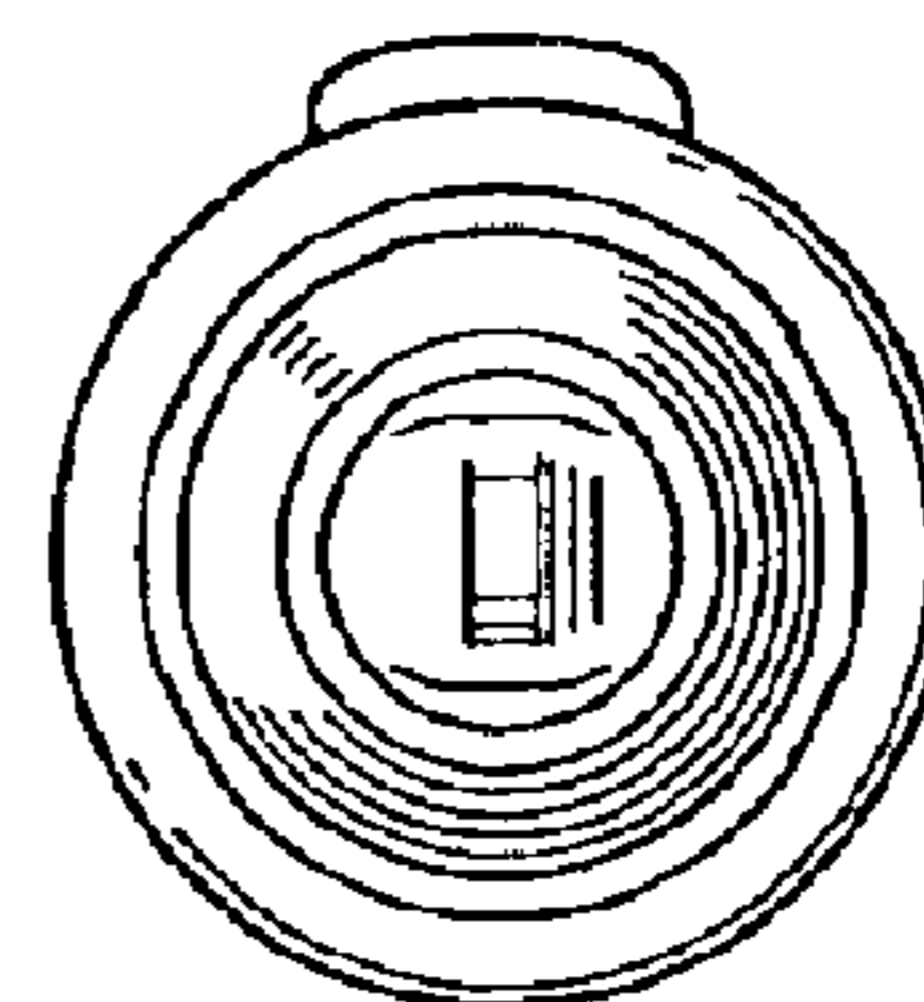


FIG. 6

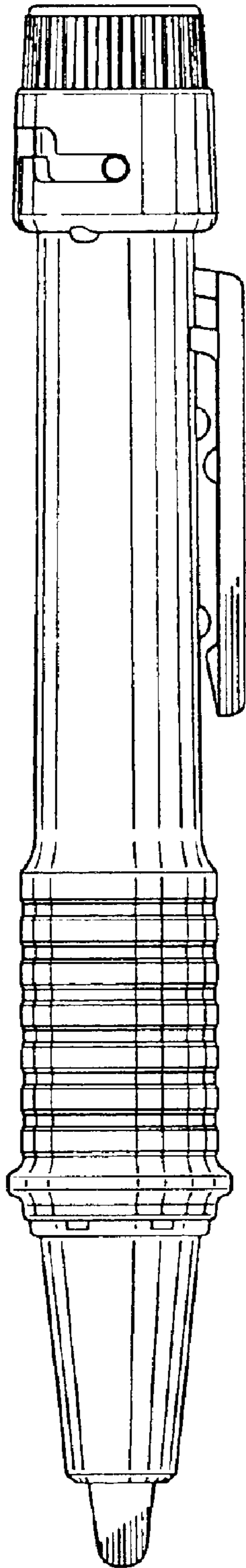


FIG. 7

