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(12) **United States Design Patent** (10) **Patent No.:** **US D514,074 S**
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(54) **SEMICONDUCTOR DEVICE**

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(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (8) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Classification Search** D13/182;
D9/432; 174/52.1, 52.2, 52.4; 257/666, 669,
257/675, 676, 678, 690, 692, 696, 697, 724,
257/728, 730, 787; 361/760, 773, 820; 438/64,
438/65

See application file for complete search history.

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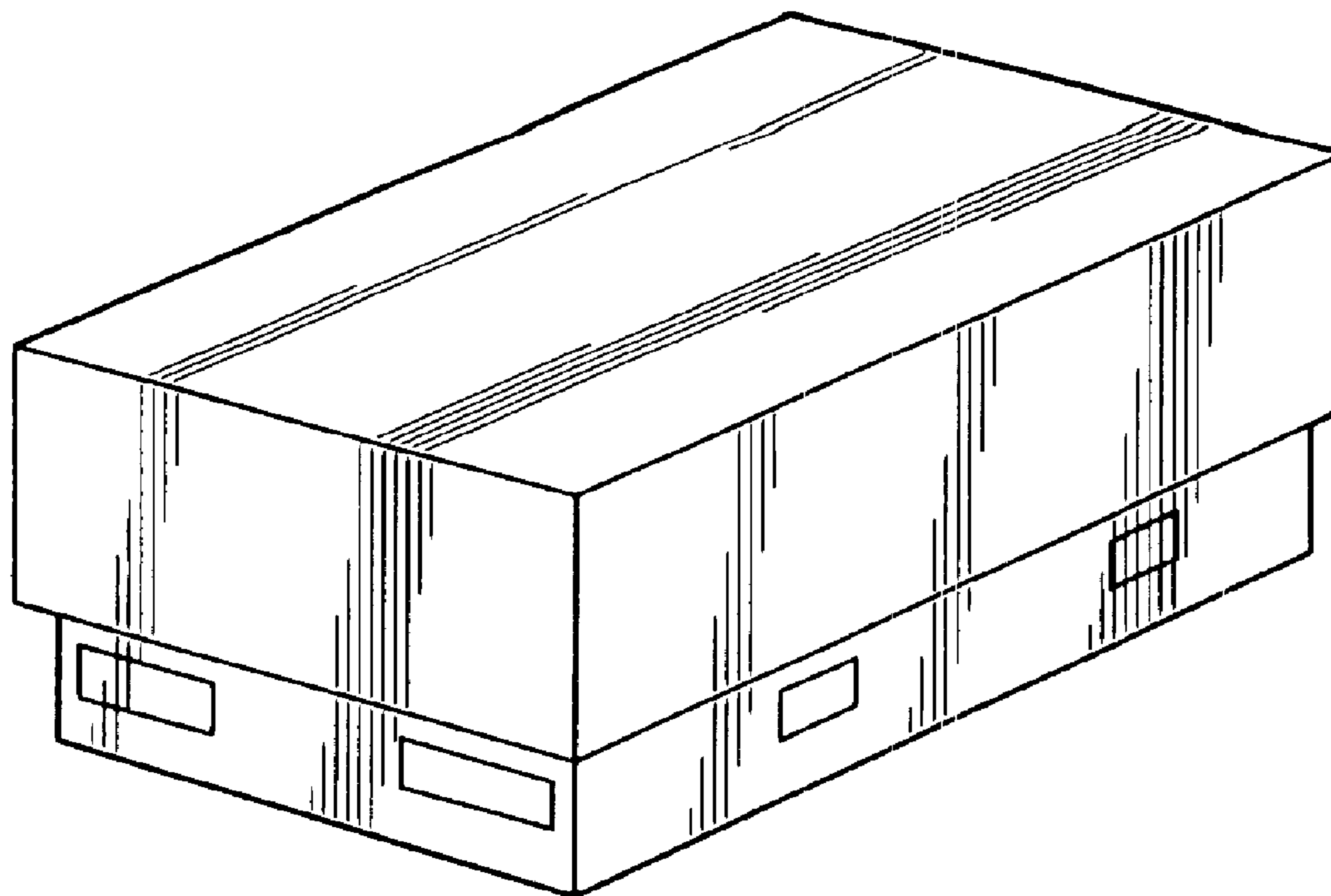
(57) **CLAIM**

The ornamental design for a semiconductor device, as shown and described.

DESCRIPTION

FIG. 1 is a front, top and right side perspective view of a semiconductor device, showing our new design;
 FIG. 2 is a front elevational view thereof; the opposite side being a mirror image thereof;
 FIG. 3 is a right side elevational view thereof; the opposite side being a mirror image thereof;
 FIG. 4 is a top plan view thereof; and,
 FIG. 5 is a bottom plan view thereof.

1 Claim, 2 Drawing Sheets



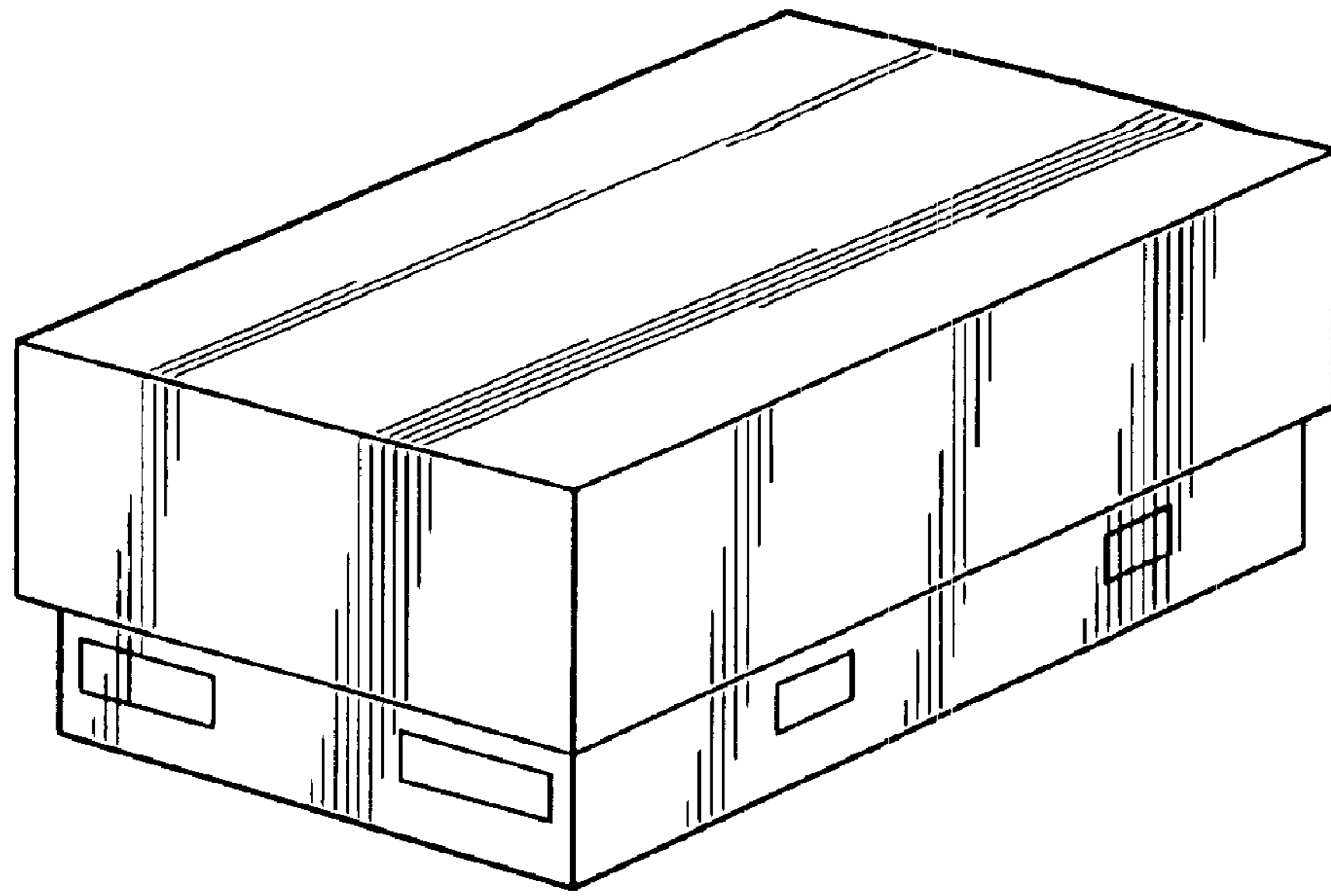


FIG.1

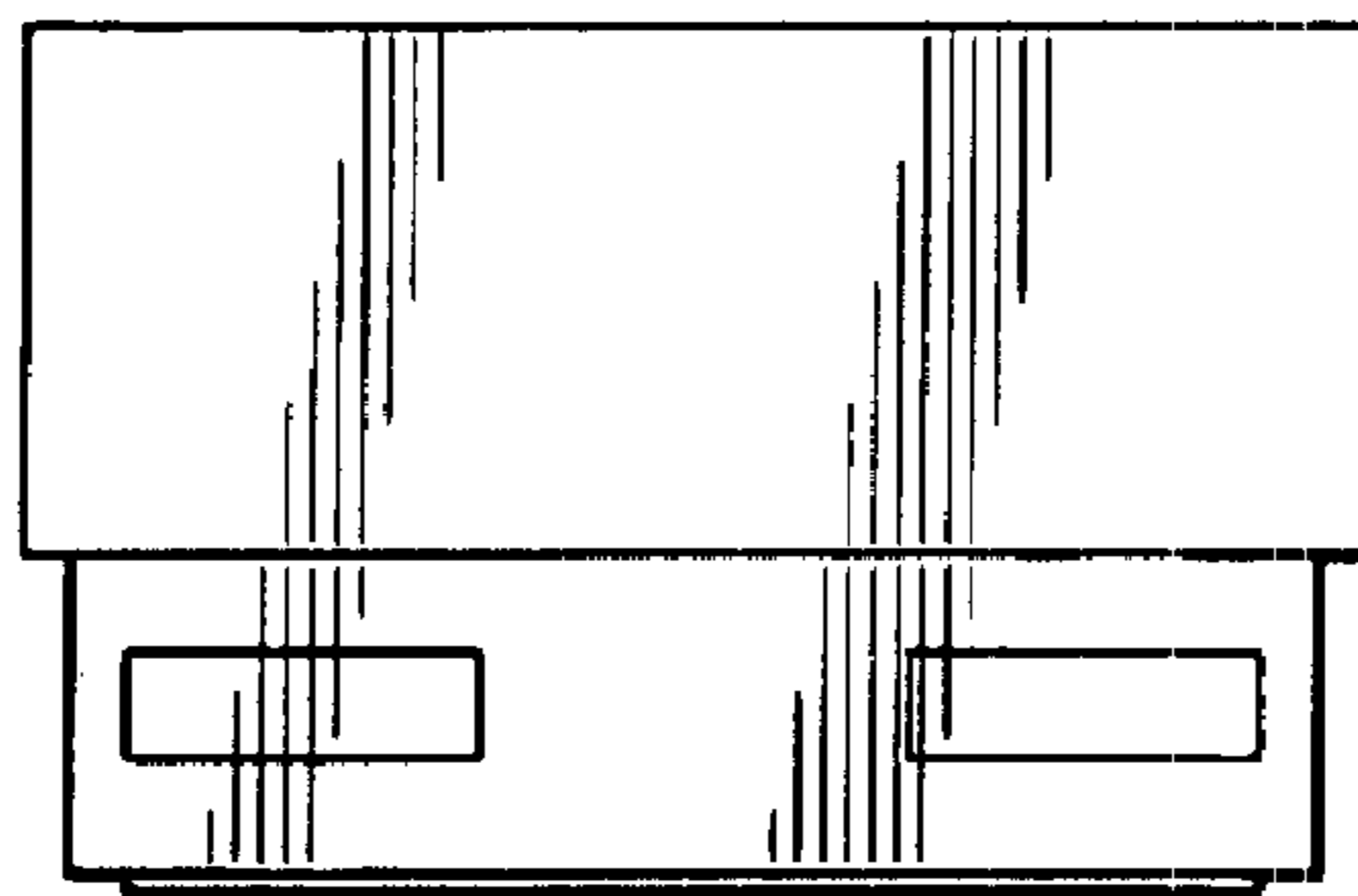


FIG.2

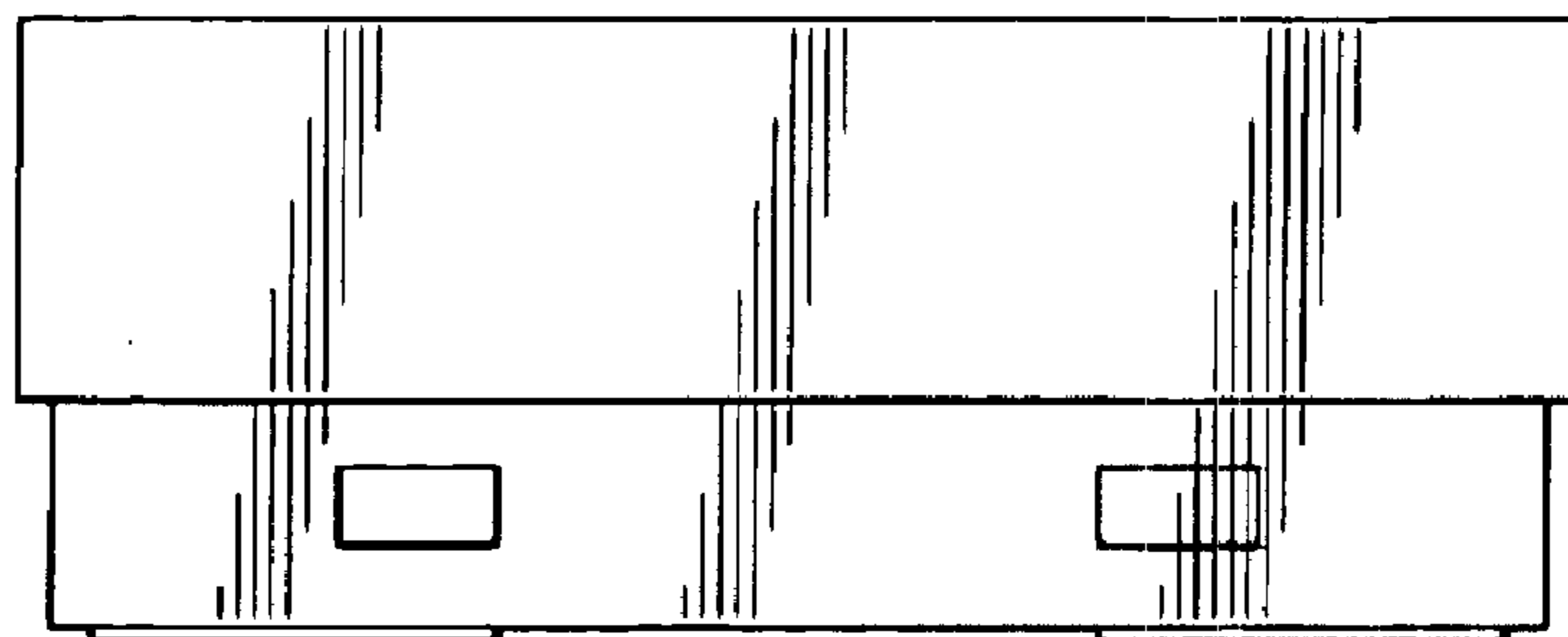


FIG.3

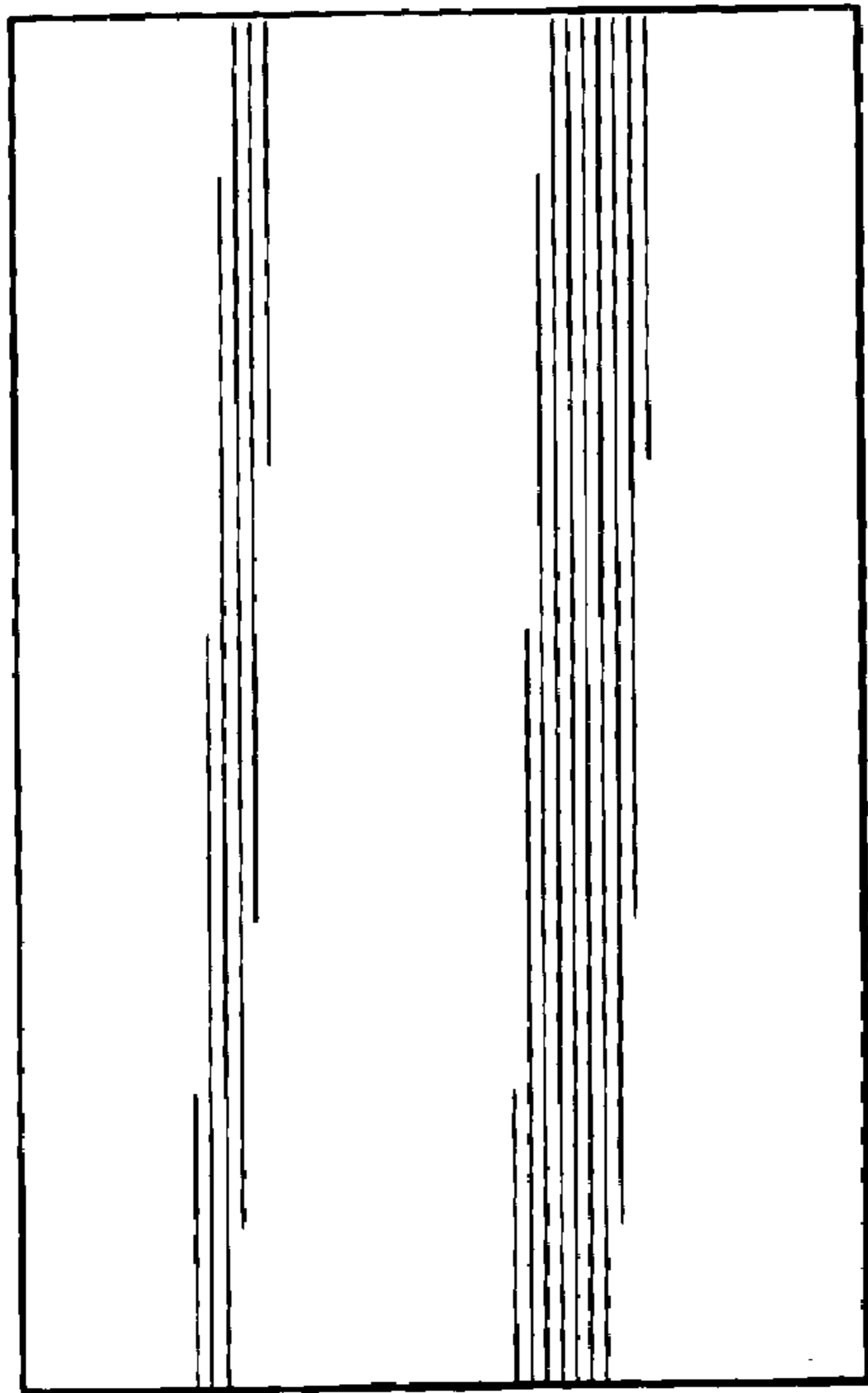


FIG.4

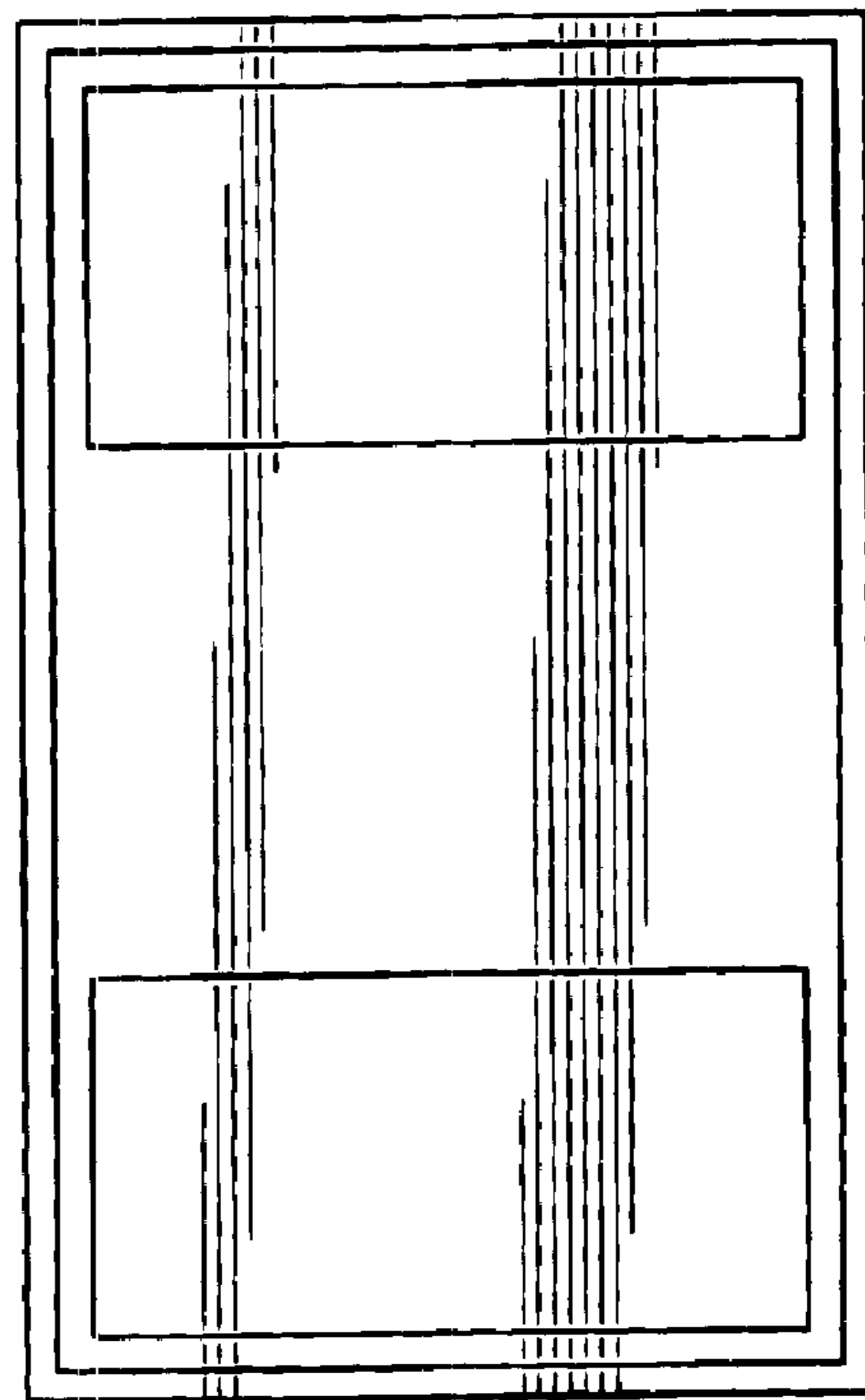


FIG.5