

US00D513251S

(12) **United States Design Patent** (10) **Patent No.:** **US D513,251 S**
Conyers et al. (45) **Date of Patent:** **** Dec. 27, 2005**

(54) **COMPUTING DEVICE**

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(73) Assignee: **Wyse Technology Inc.**, San Jose, CA (US)

(**) Term: **14 Years**

(21) Appl. No.: **29/211,236**

(22) Filed: **Aug. 13, 2004**

(51) **LOC (6) Cl.** **14-02**

(52) **U.S. Cl.** **D14/352**

(58) **Field of Search** D14/349-356;
D13/162, 184, 199; 312/223.1-223.3; 360/99.01-99.12;
361/690-696; 369/34.01, 36.01

(56) **References Cited**

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Primary Examiner—Freda S. Nunn

(74) *Attorney, Agent, or Firm*—McDermott Will & Emery LLP

(57) **CLAIM**

The ornamental design for a computing device, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of the computing device showing the new design;

FIG. 2 is a rear perspective view of the computing device shown in FIG. 1;

FIG. 3 is a side view of the computing device shown in FIG. 1;

FIG. 4 is a front view of the computing device shown in FIG. 1;

FIG. 5 is another side view of the computing device shown in FIG. 1;

FIG. 6 is a rear view of the computing device shown in FIG. 1;

FIG. 7 is a top view of the computing device shown in FIG. 1;

FIG. 7 is a top view of the computing device shown in FIG. 1;

FIG. 9 is a front perspective view of a second embodiment of the computing device;

FIG. 10 is a rear perspective view of the computing device shown in FIG. 9;

FIG. 11 is a side view of the computing device shown in FIG. 9;

FIG. 12 is a front view of the computing device shown in FIG. 9;

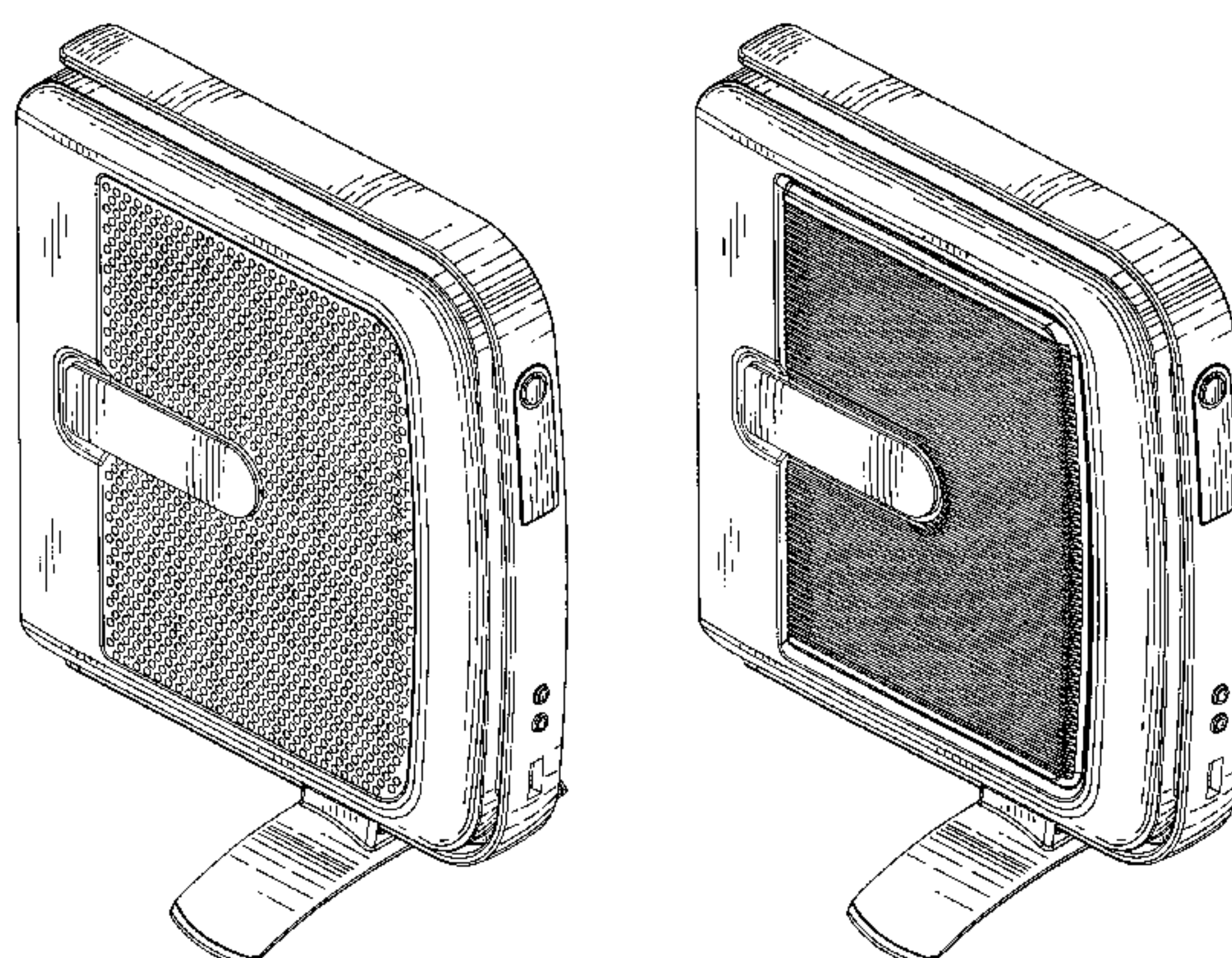
FIG. 13 is another side view of the computing device shown in FIG. 9;

FIG. 14 is a rear view of the computing device shown in FIG. 9;

FIG. 15 is a top view of the computing device shown in FIG. 9; and,

FIG. 16 is a bottom view of the computing device shown in FIG. 9.

1 Claim, 10 Drawing Sheets



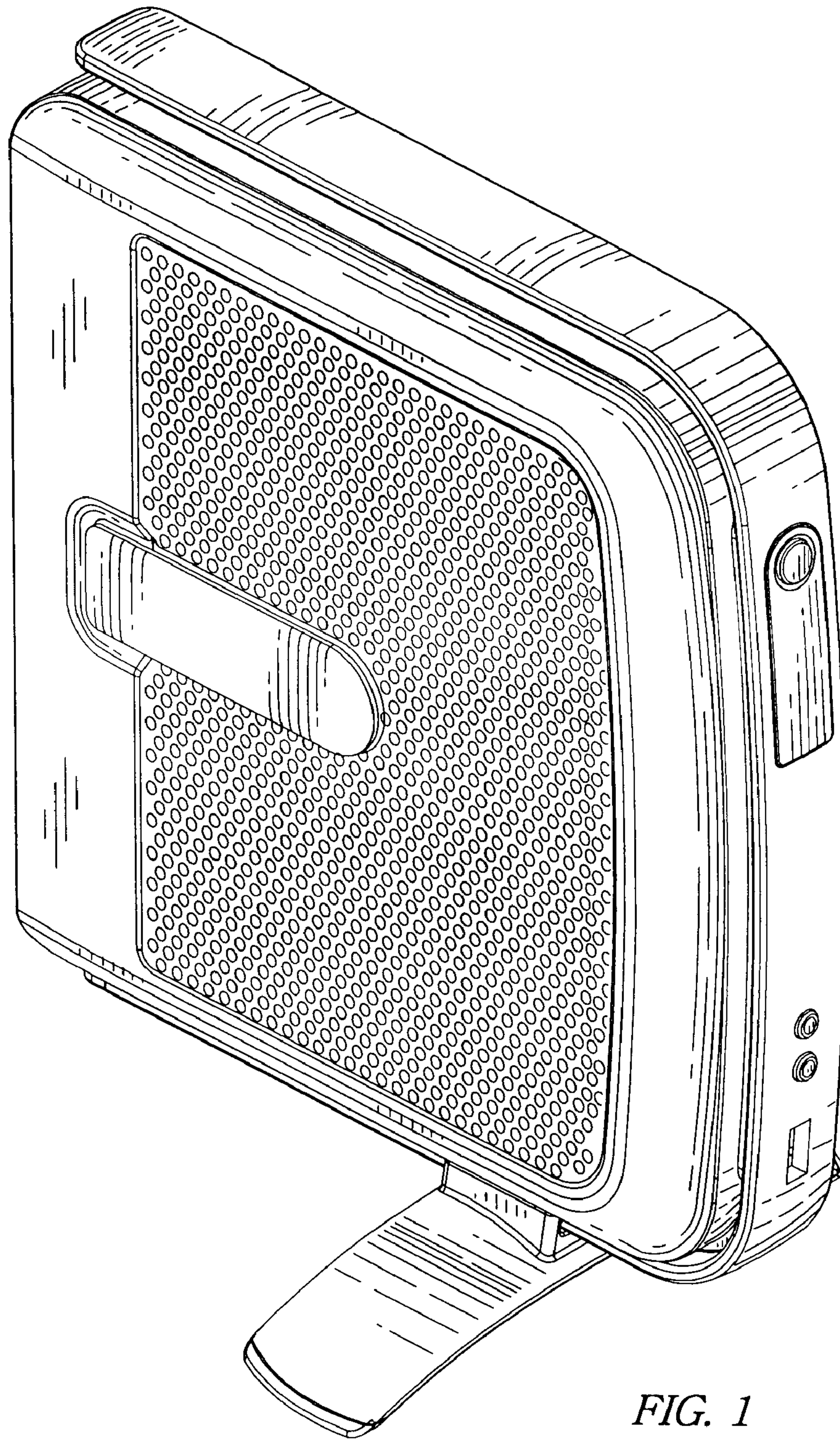


FIG. 1

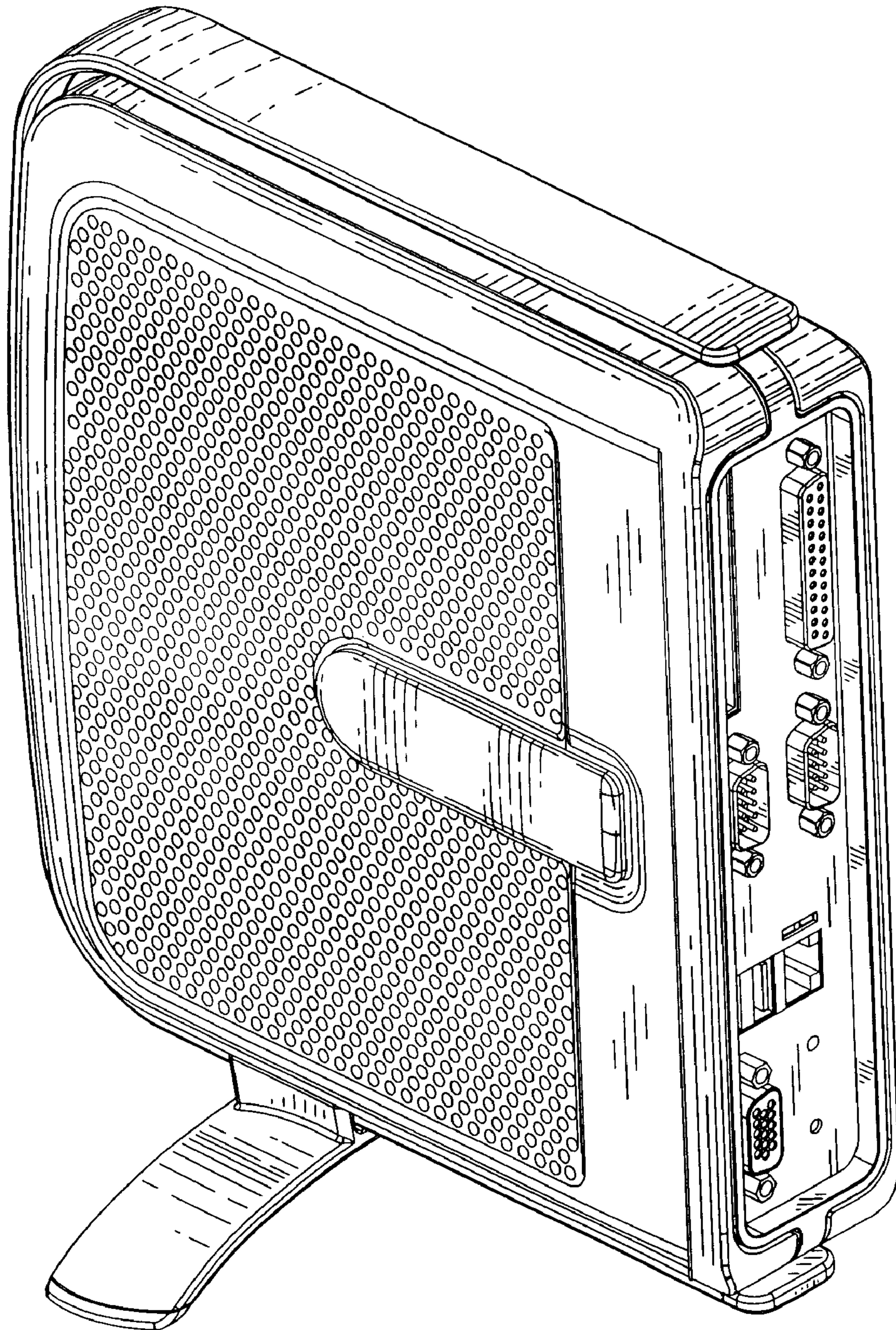


FIG. 2

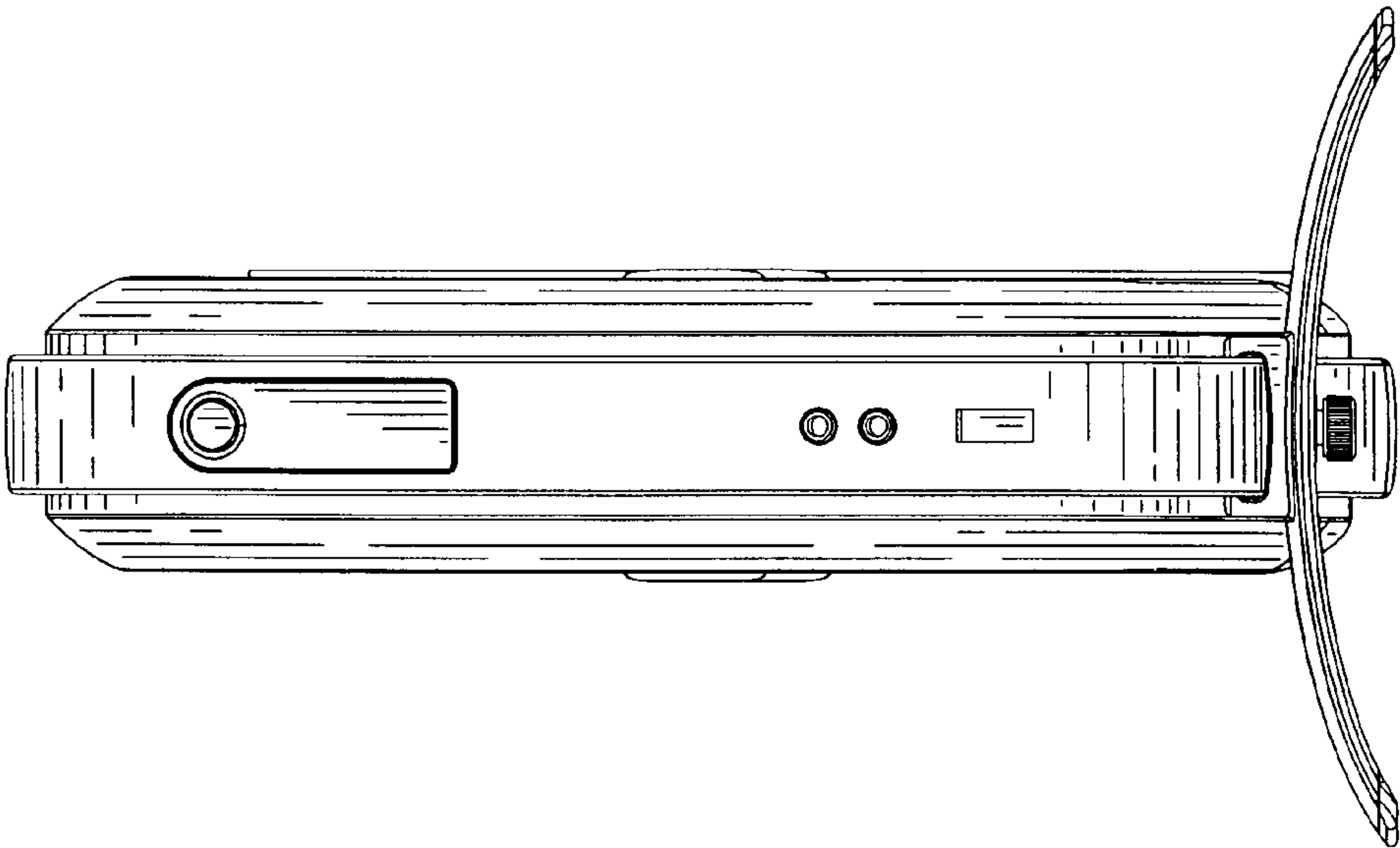


FIG. 4

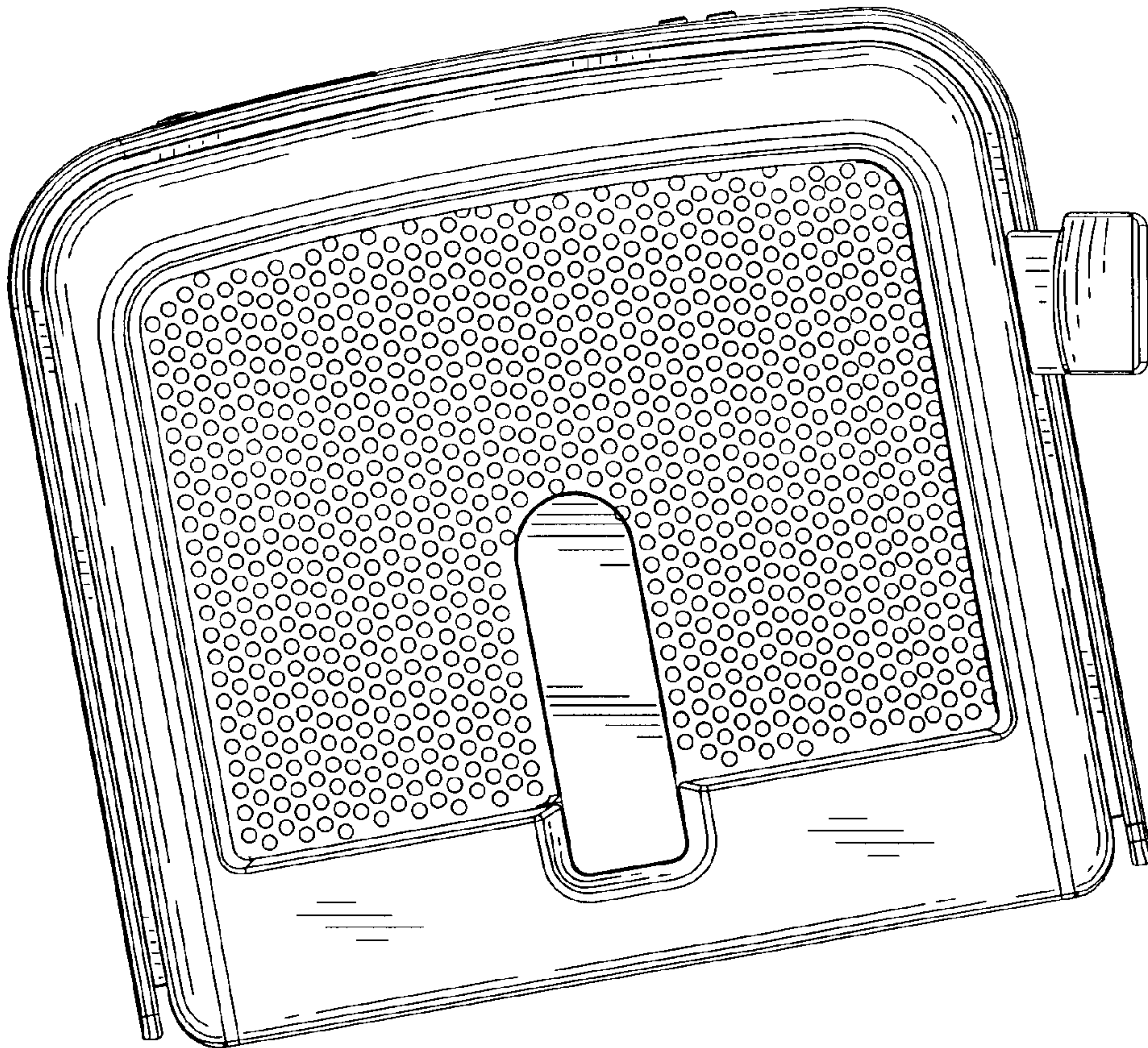


FIG. 3

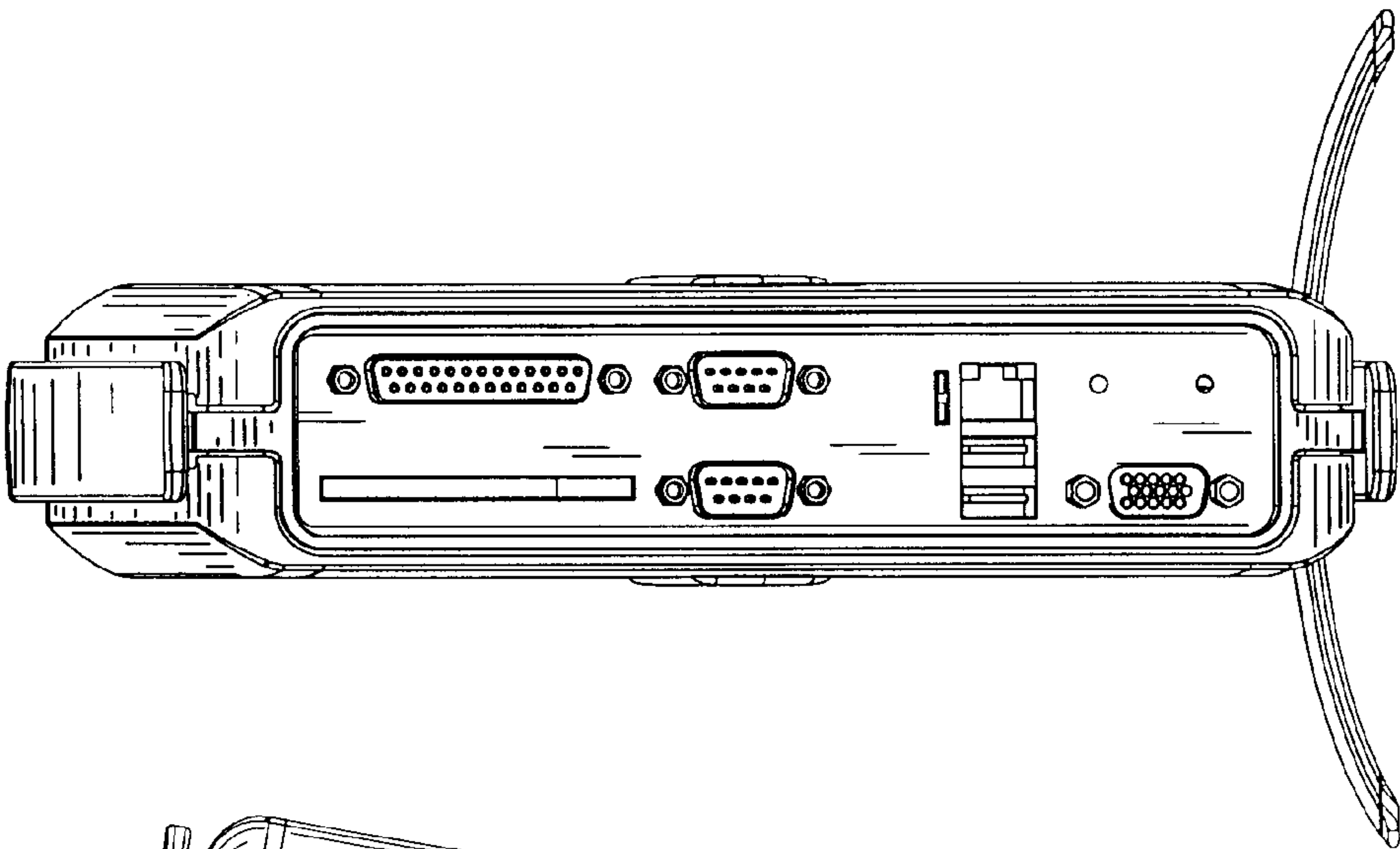


FIG. 6

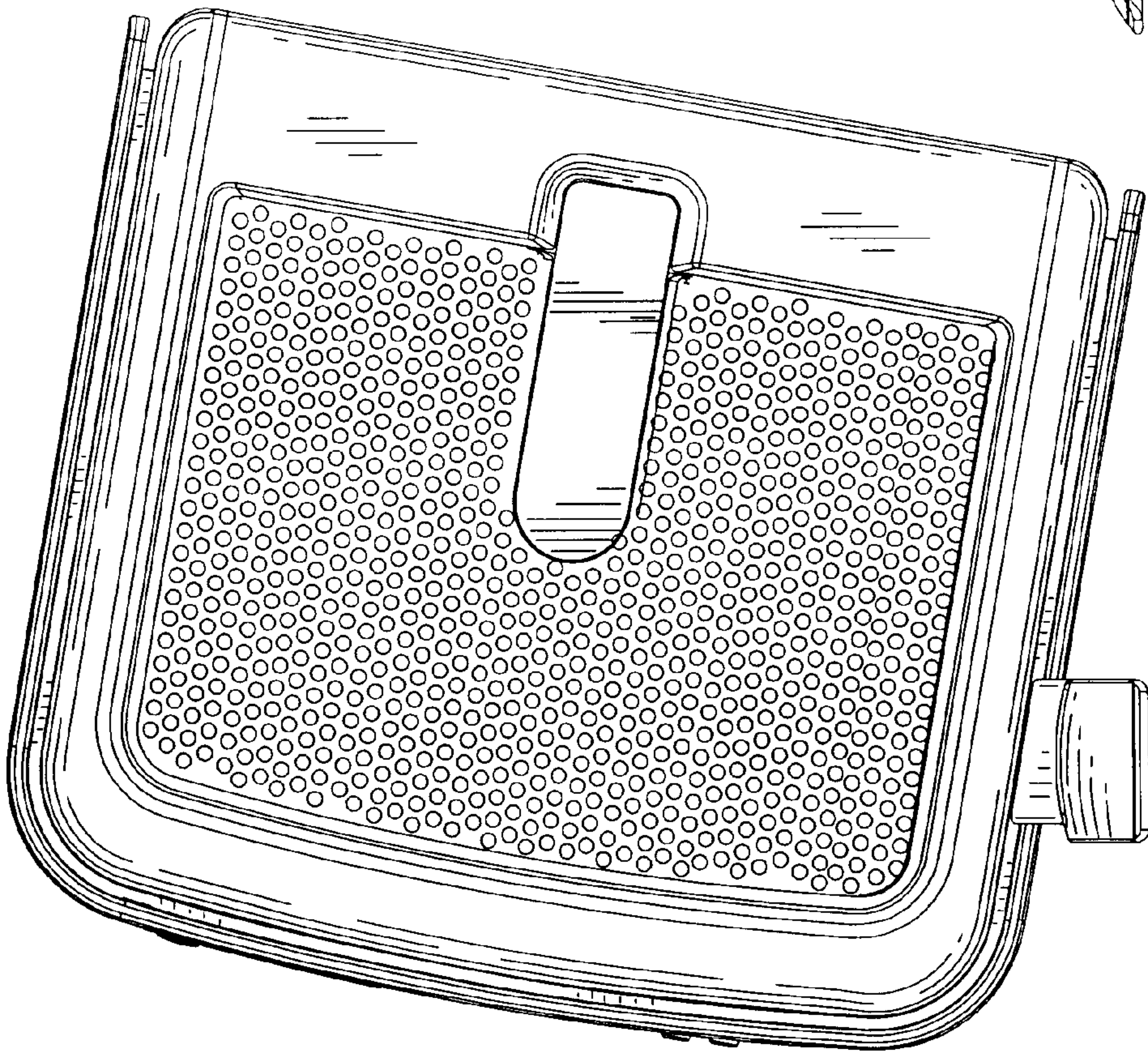


FIG. 5

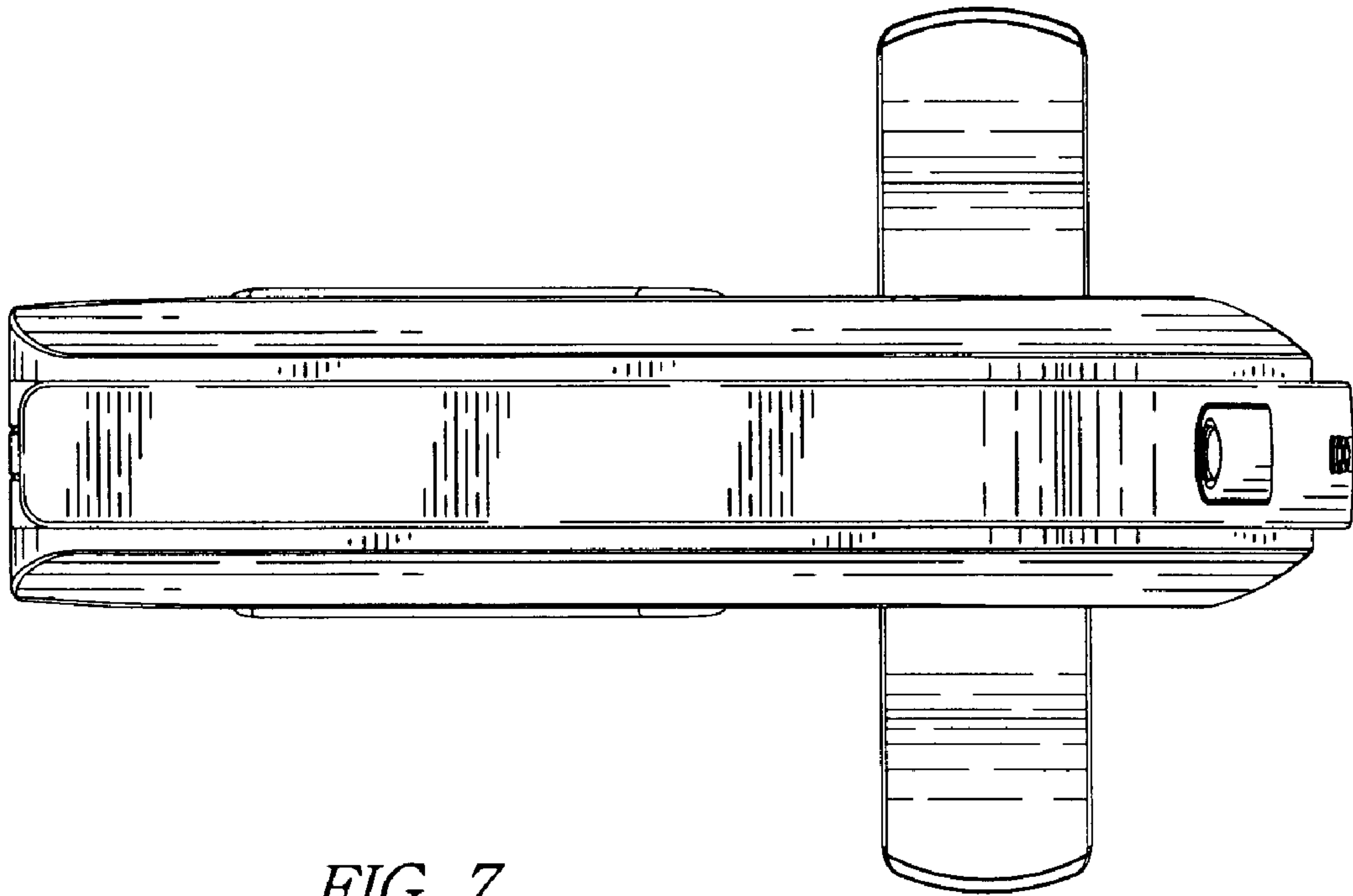


FIG. 7

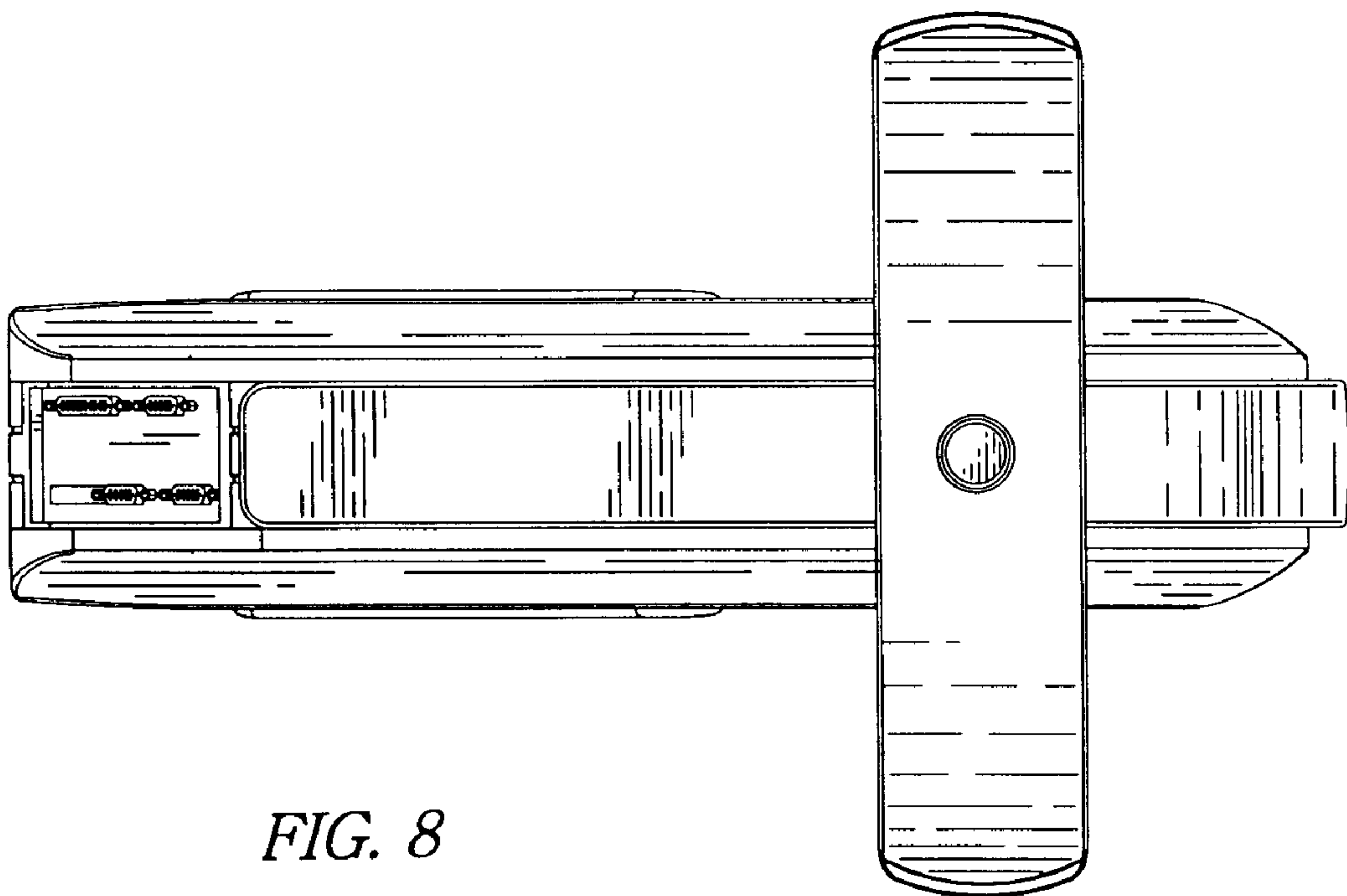


FIG. 8

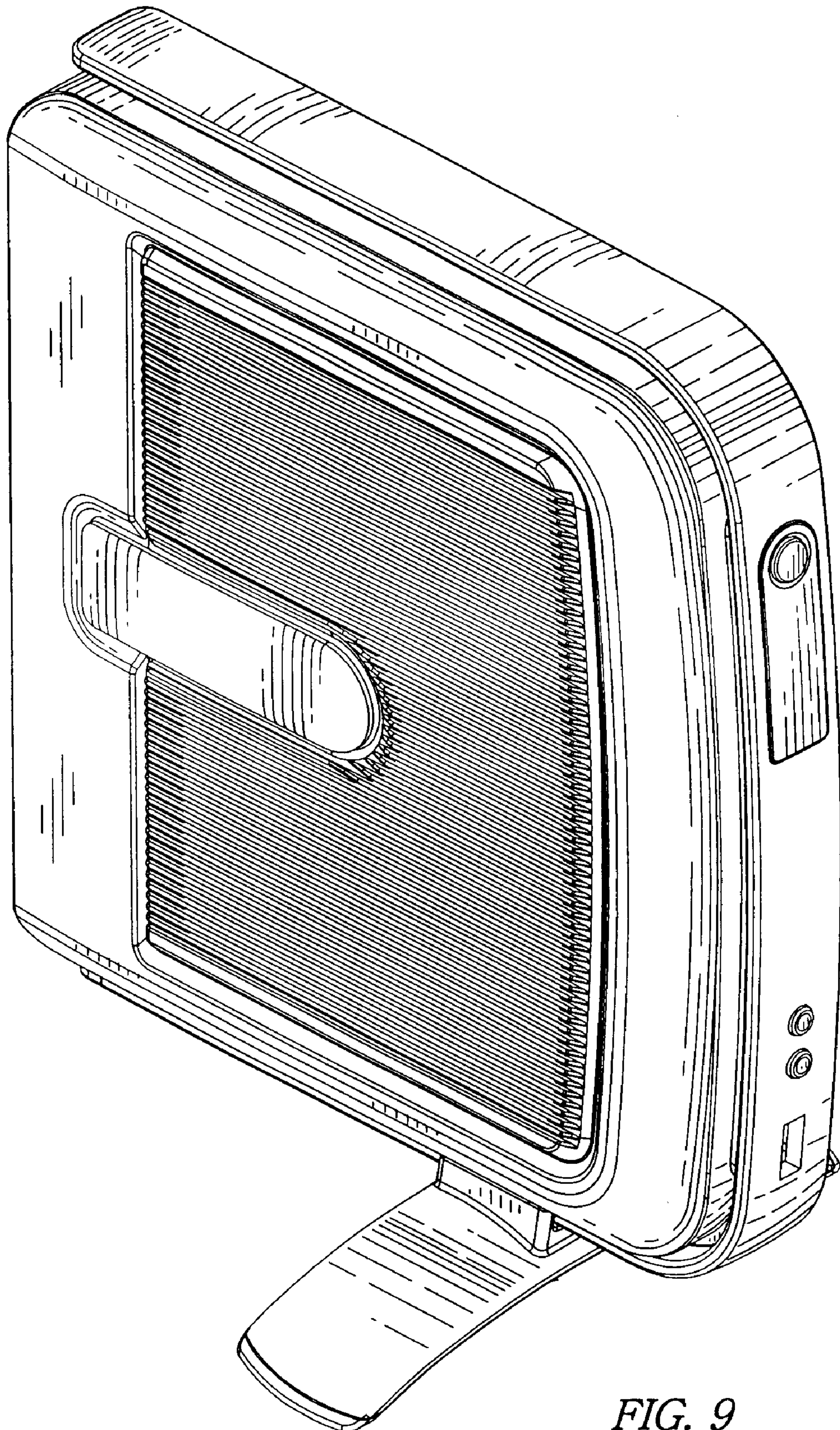


FIG. 9

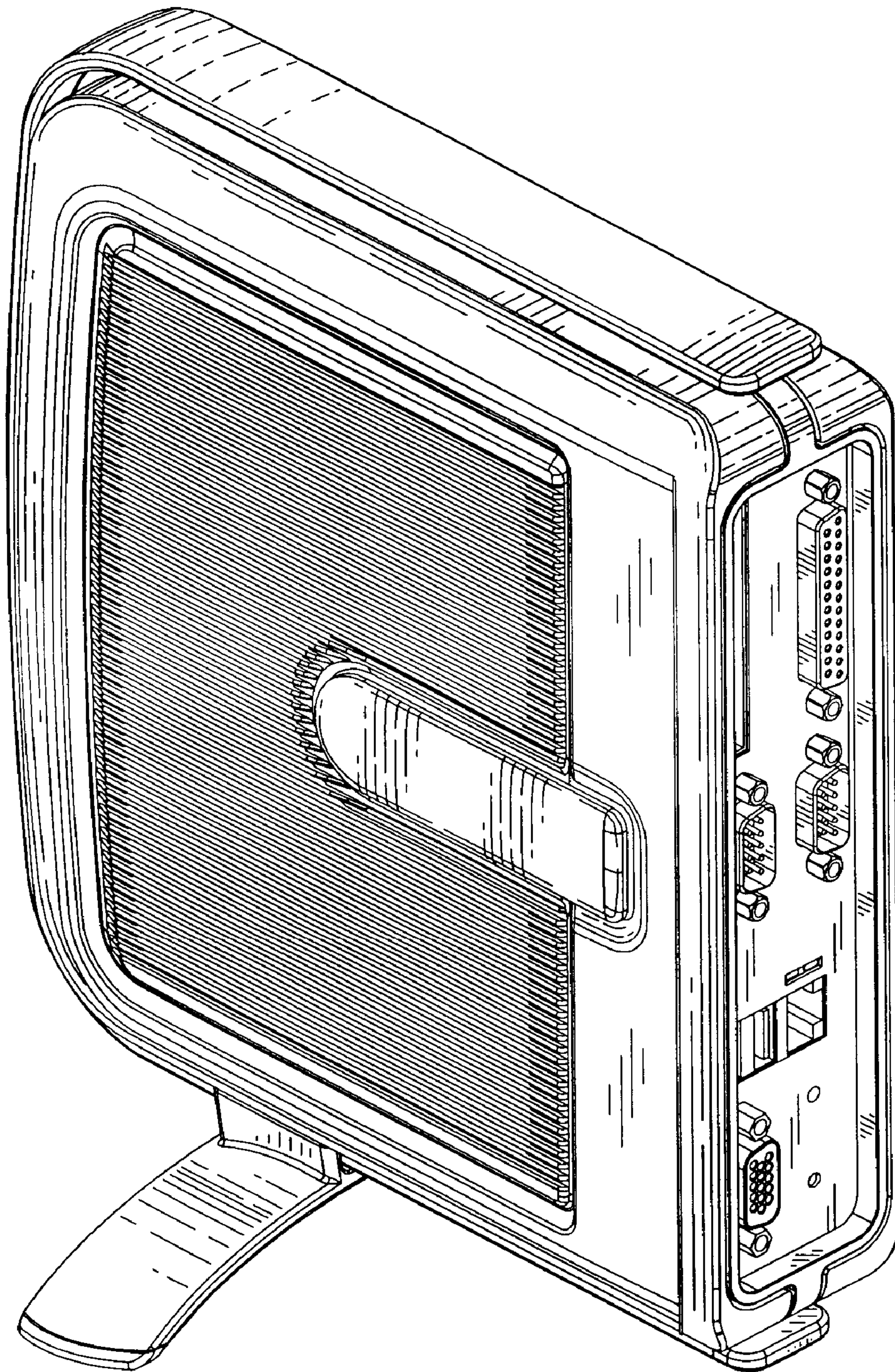


FIG. 10

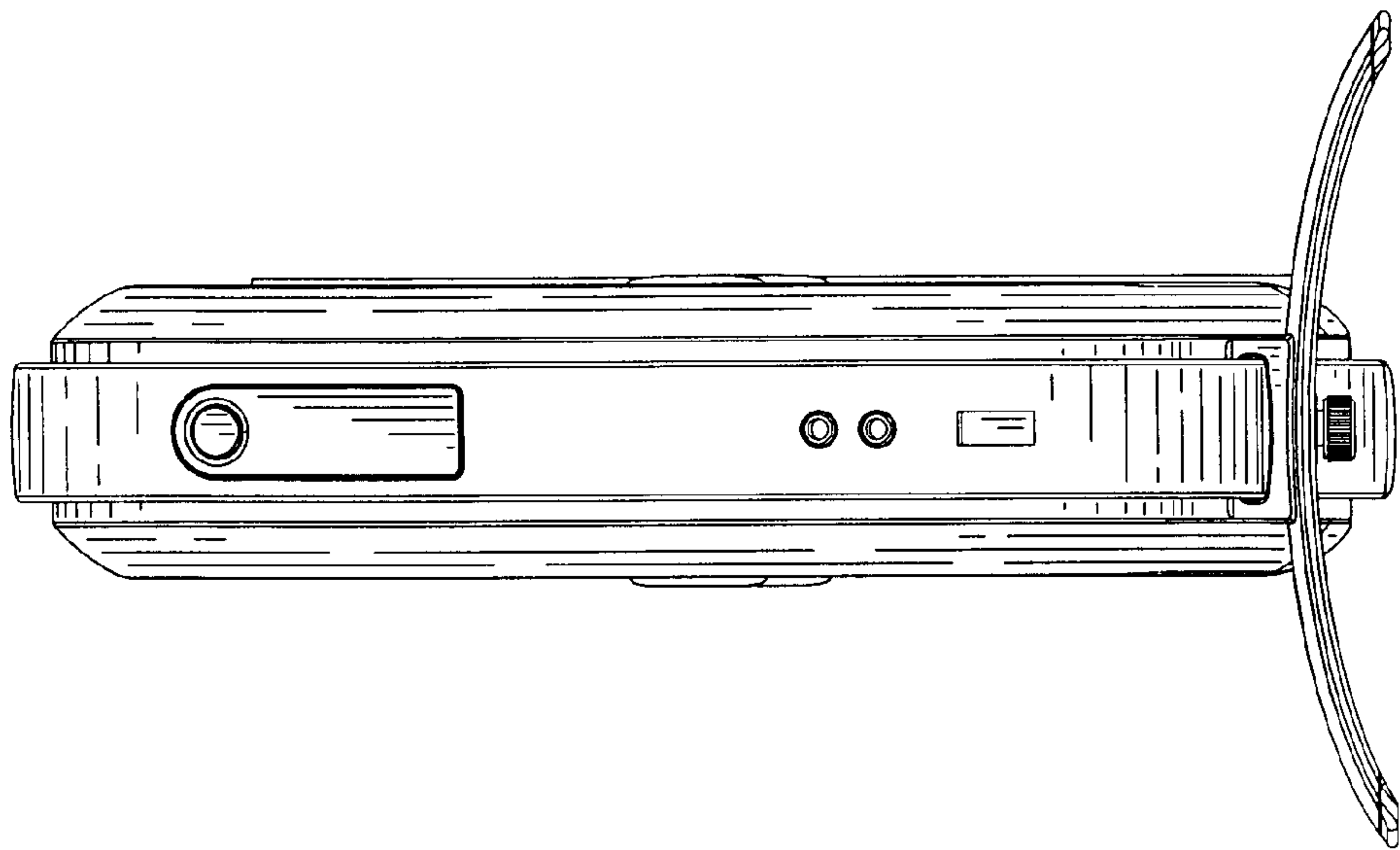


FIG. 12

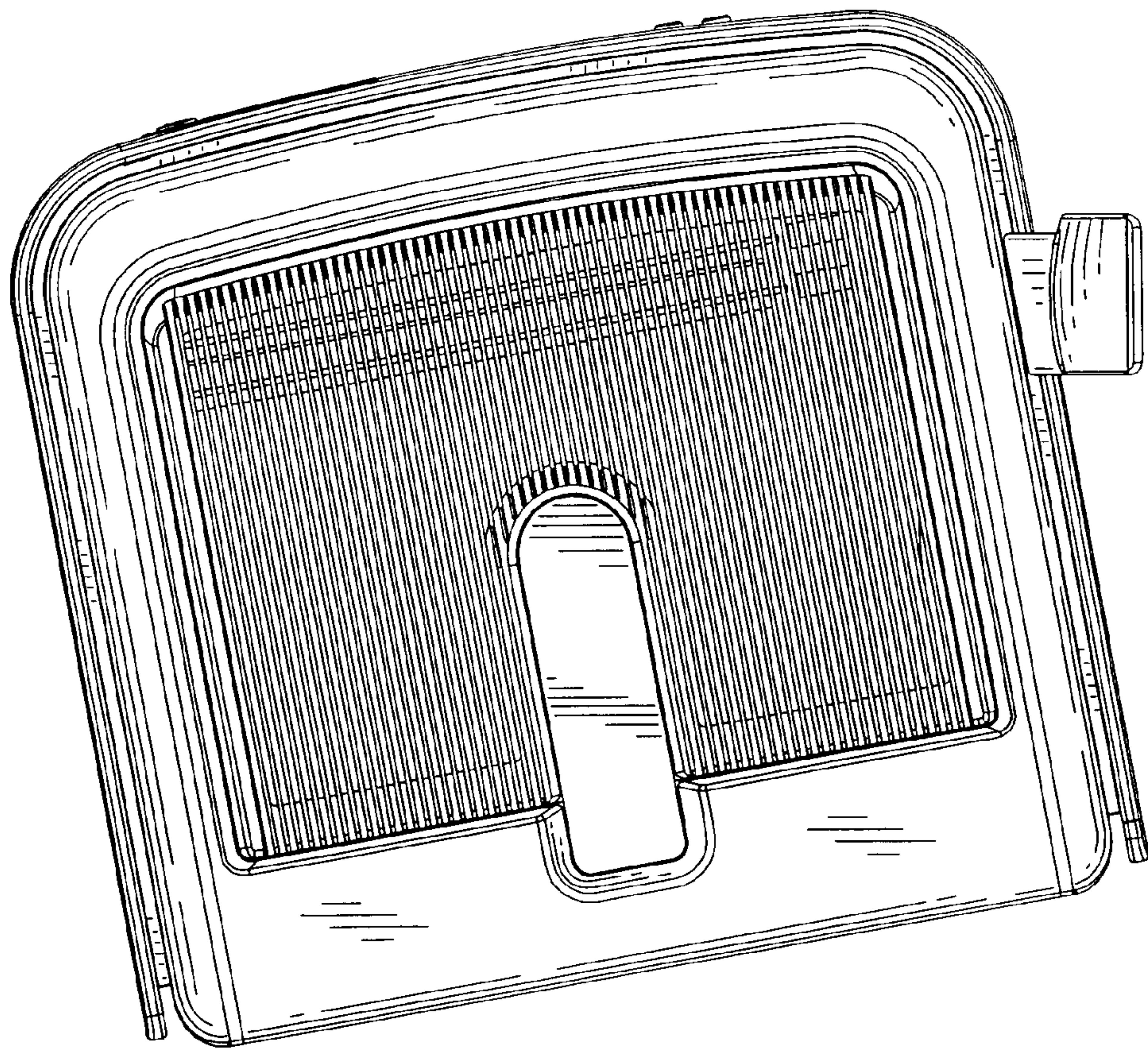


FIG. 11

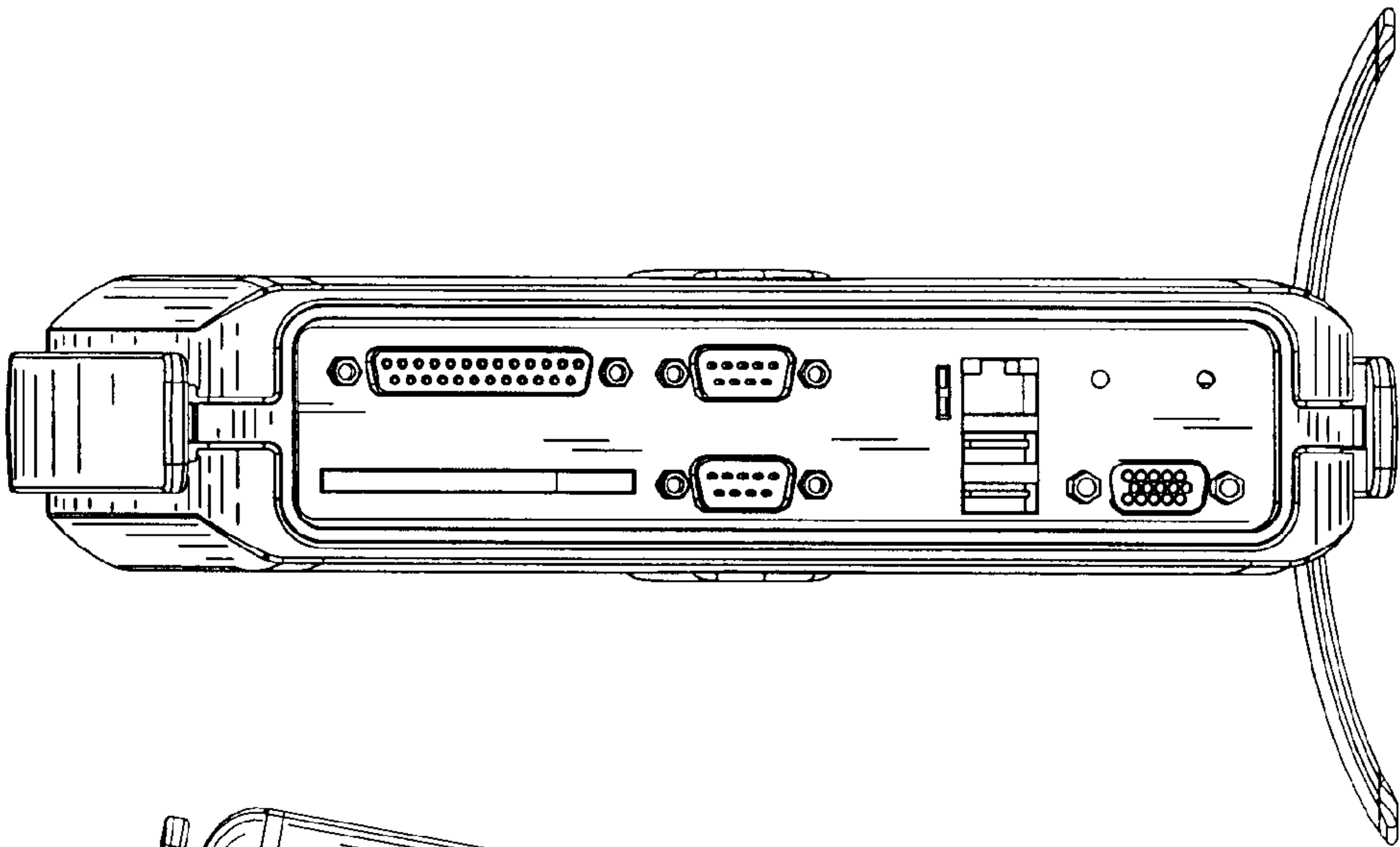


FIG. 14

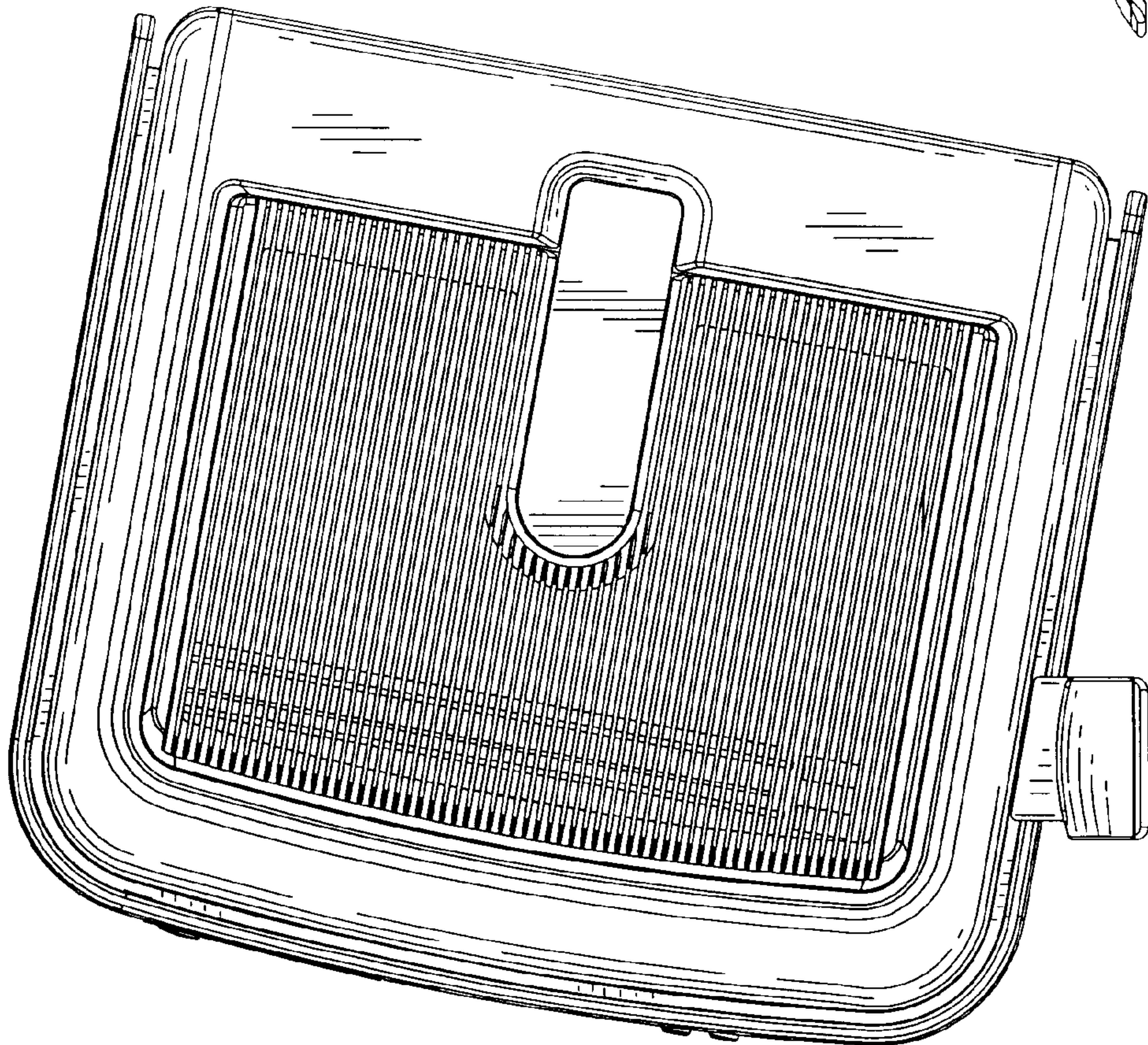


FIG. 13

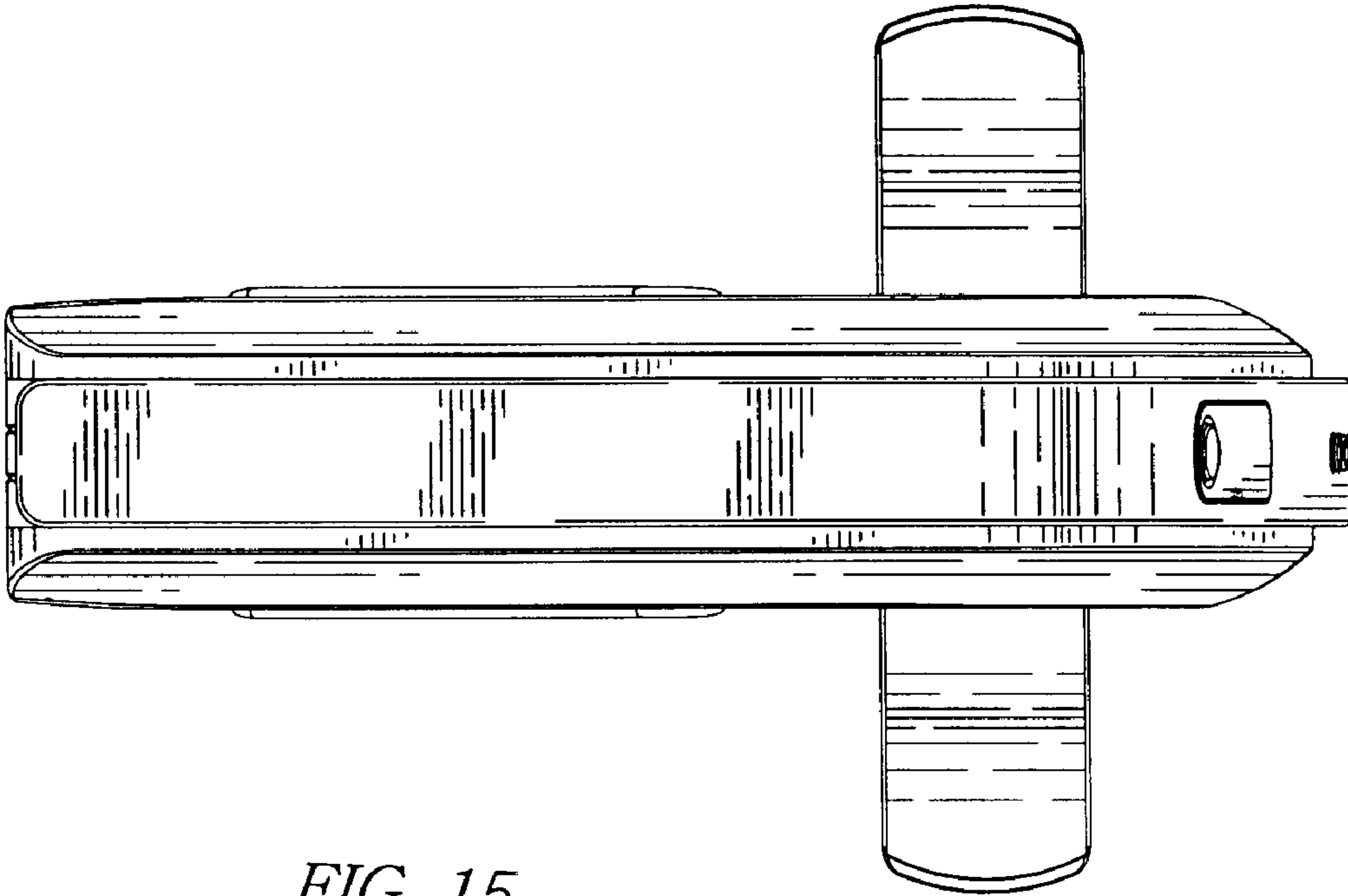


FIG. 15

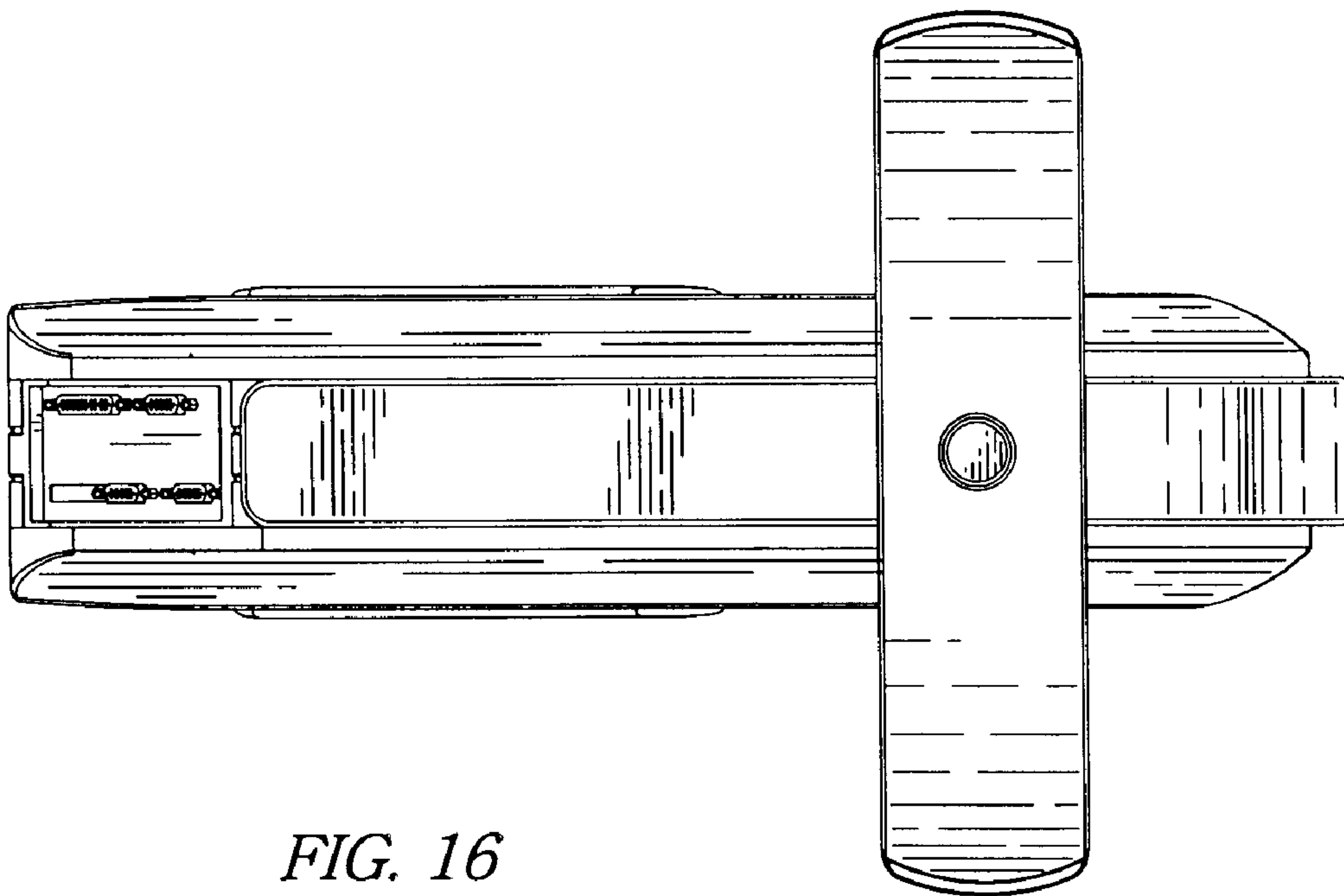


FIG. 16

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : Des. 513,251 S
APPLICATION NO. : 29/211236
DATED : December 27, 2005
INVENTOR(S) : Mike DeNeffe et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On The Title Page

IN THE DESCRIPTION

“FIG. 7 is a top...FIG. 7” should read --FIG. 7 is a top view of the computing device shown in FIG. 1--; and the second occurrence of “FIG. 7 is a top...FIG. 1” should be deleted and replaced with --FIG. 8 is a bottom view of the computing device shown in FIG. 1--.

Signed and Sealed this

Twenty-second Day of August, 2006

A handwritten signature in black ink, reading "Jon W. Dudas", is written over a rectangular area with a light gray dotted background.

JON W. DUDAS

Director of the United States Patent and Trademark Office