



US00D511501S

(12) **United States Design Patent** (10) **Patent No.:** **US D511,501 S**  
**Lee et al.** (45) **Date of Patent:** **\*\* Nov. 15, 2005**

(54) **DVI CONNECTOR**

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(\*\*) Term: **14 Years**

(21) Appl. No.: **29/212,606**

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(51) **LOC (8) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/156; 13/147**

(58) **Field of Search** ..... D13/133, 146,  
D13/147, 154, 156; 439/353, 362, 372,  
588, 606-610; D14/256, 433

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(57) **CLAIM**

The ornamental design for a DVI connector, as herein shown and described.

**DESCRIPTION**

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FIG. 1 is a perspective view of a DVI connector, in accordance with the present invention.

FIG. 2 is a top view of a DVI connector, in accordance with the present invention.

FIG. 3 is a bottom view of a DVI connector, in accordance with the present invention.

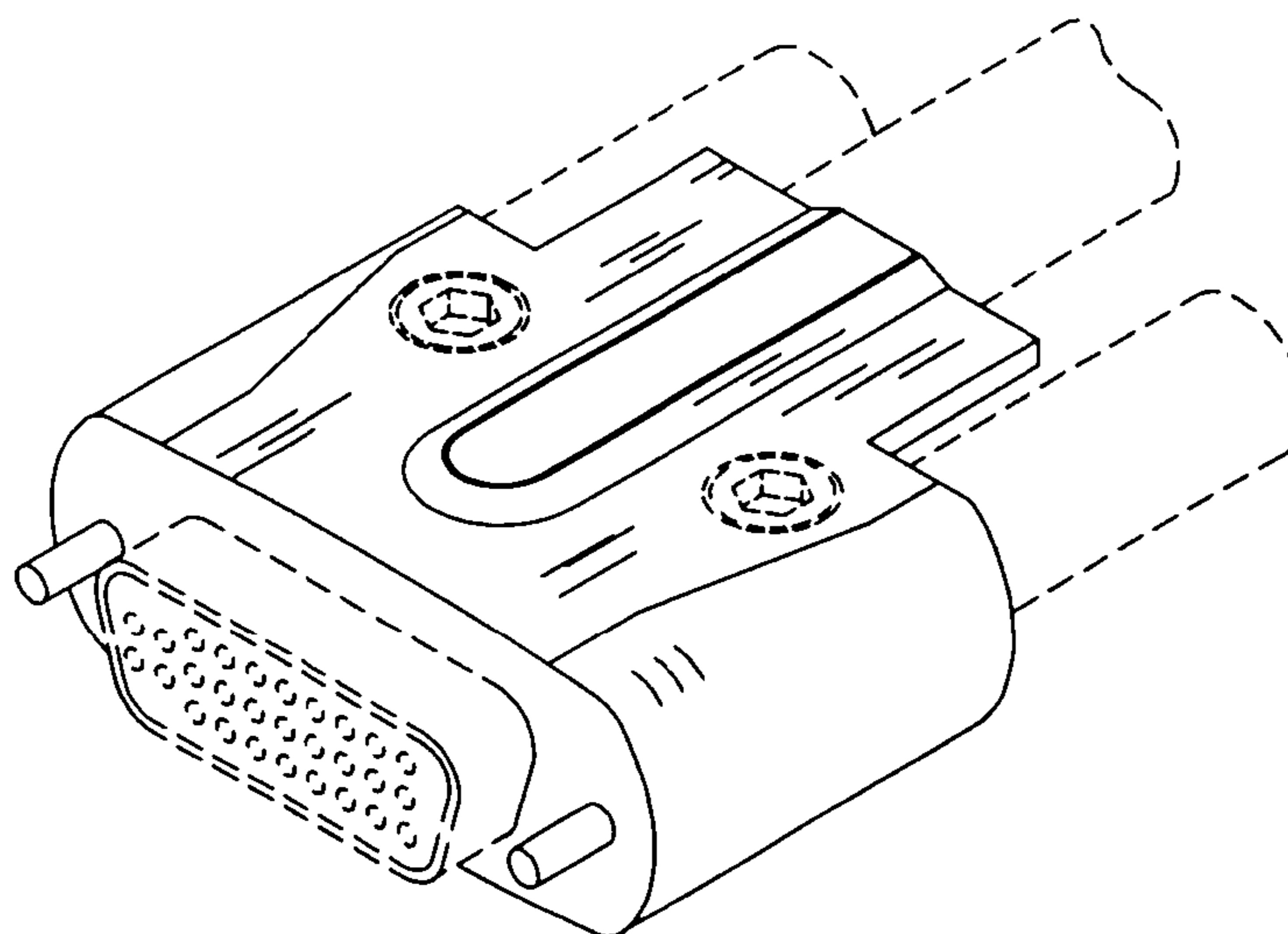
FIG. 4 is a side view, of a DVI connector, in accordance with the present invention.

FIG. 5 is an opposing side view of a DVI connector, in accordance with the present invention.

FIG. 6 is a front view, i.e., distal end view, of a DVI connector, in accordance with the present invention; and, FIG. 7 is a rear view, i.e., proximal end view, of a DVI connector, in accordance with the present invention.

Broken lines shown in any of the foregoing figures represent the environment only and do not constitute any part of the invention.

**1 Claim, 2 Drawing Sheets**



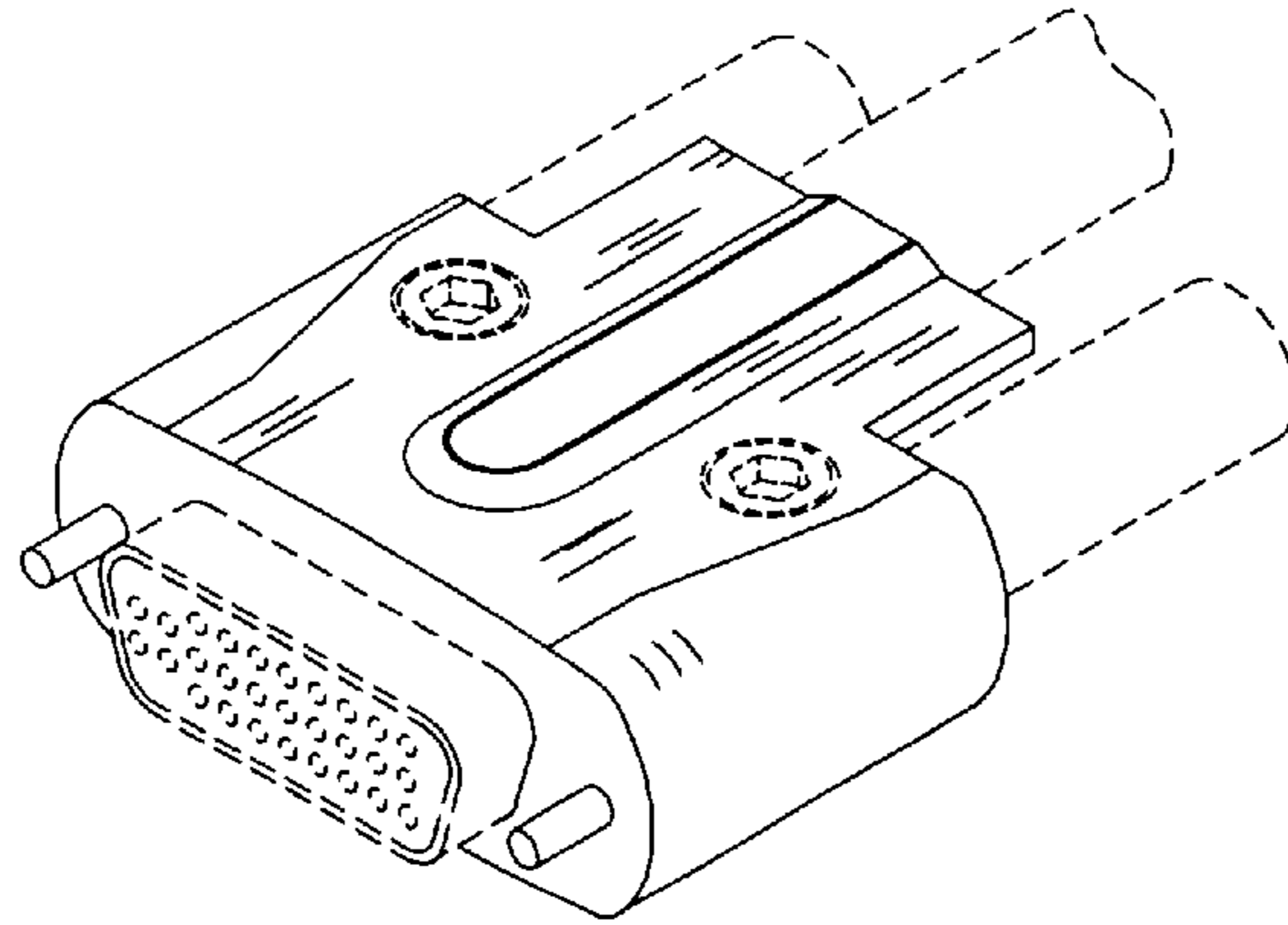


FIG. 1

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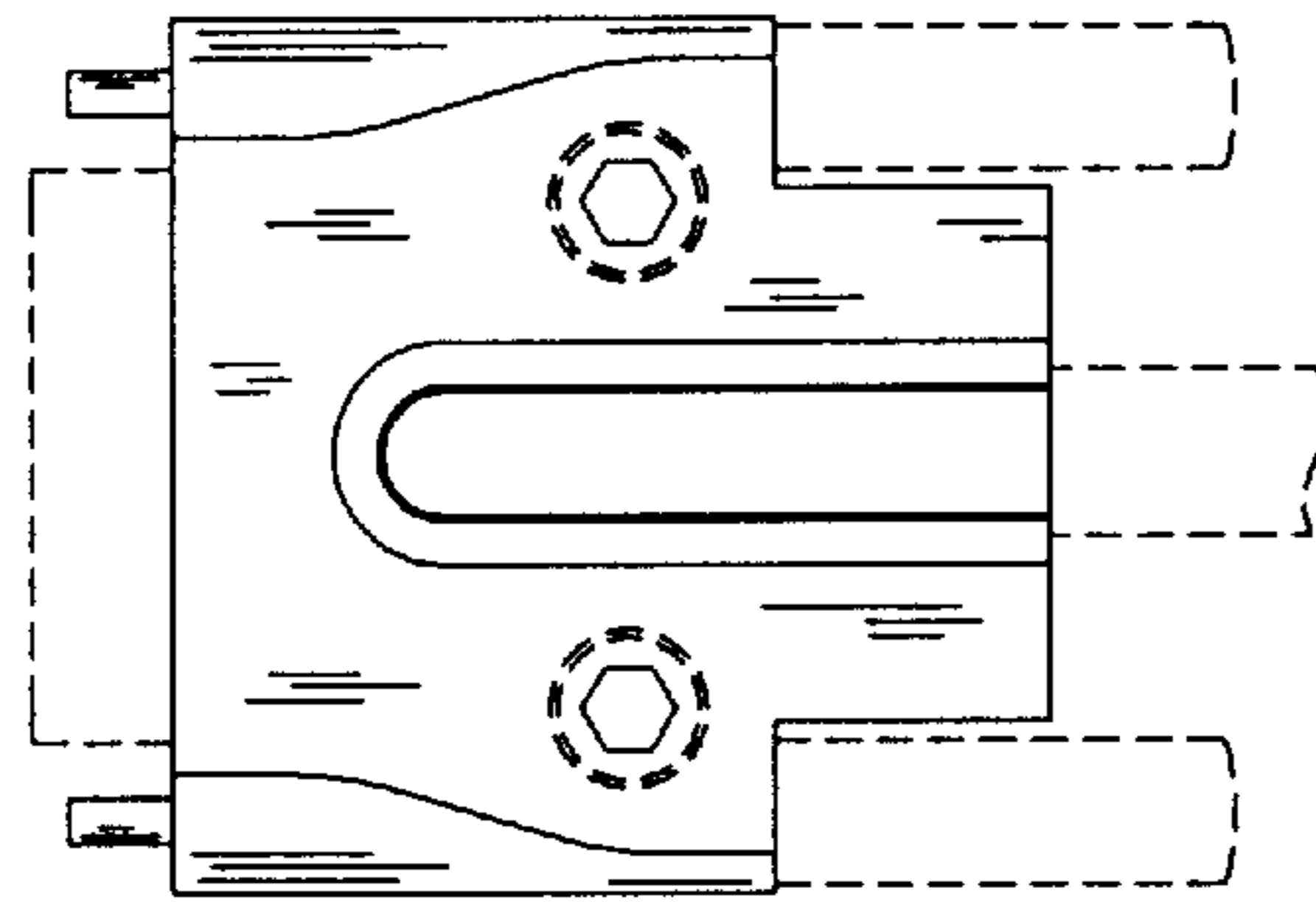


FIG. 2

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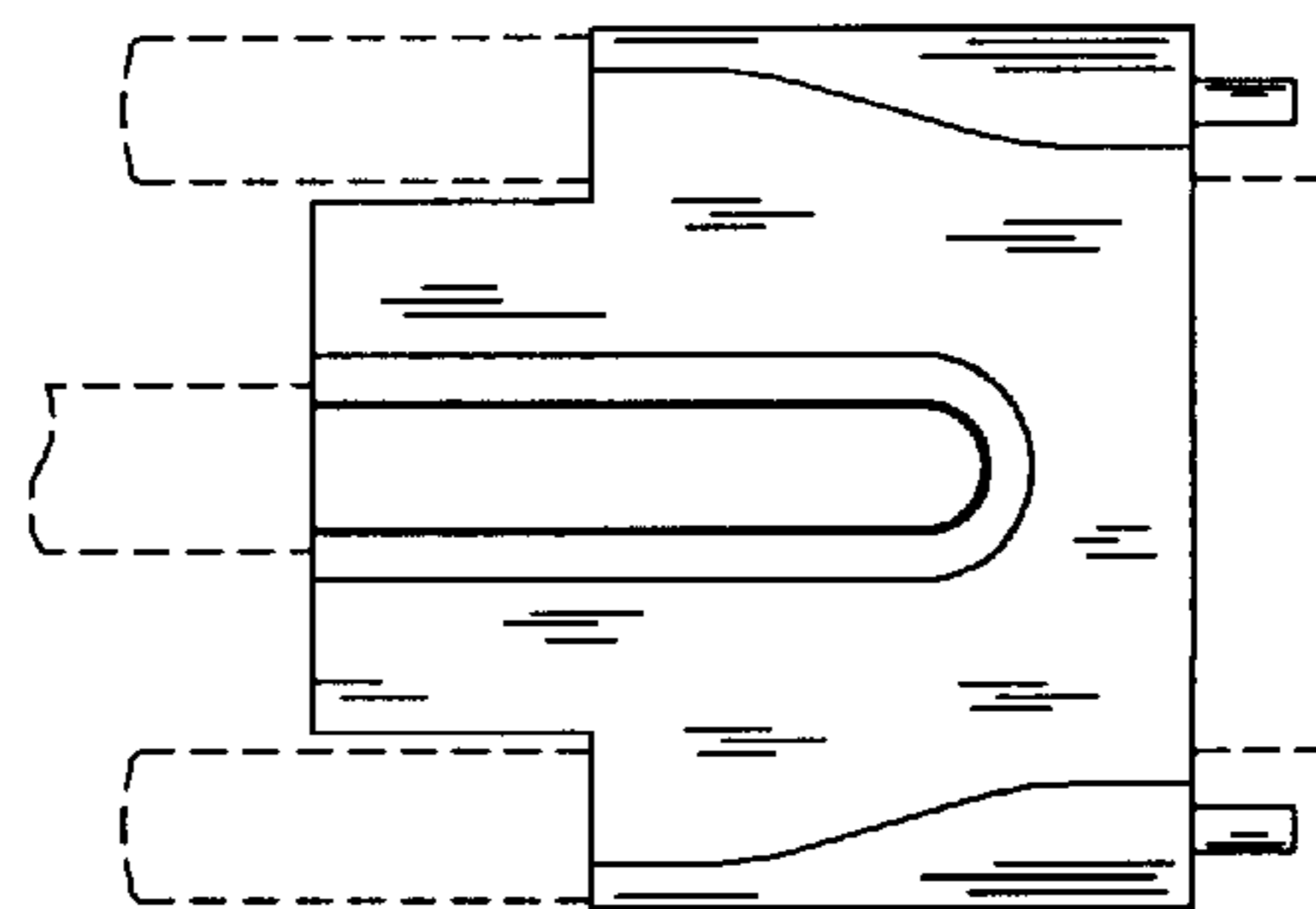


FIG. 3

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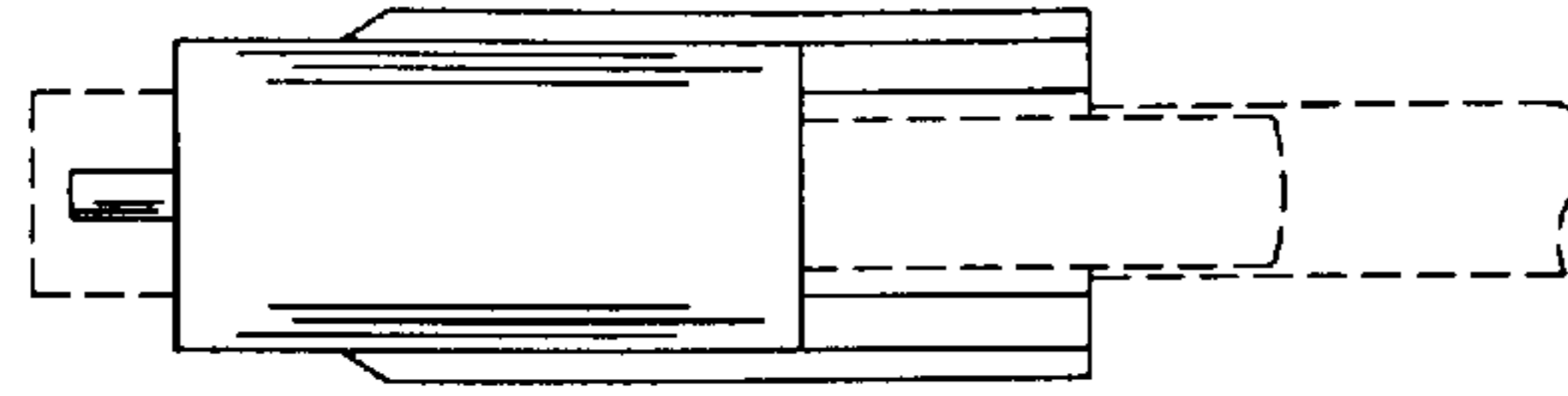


FIG. 4

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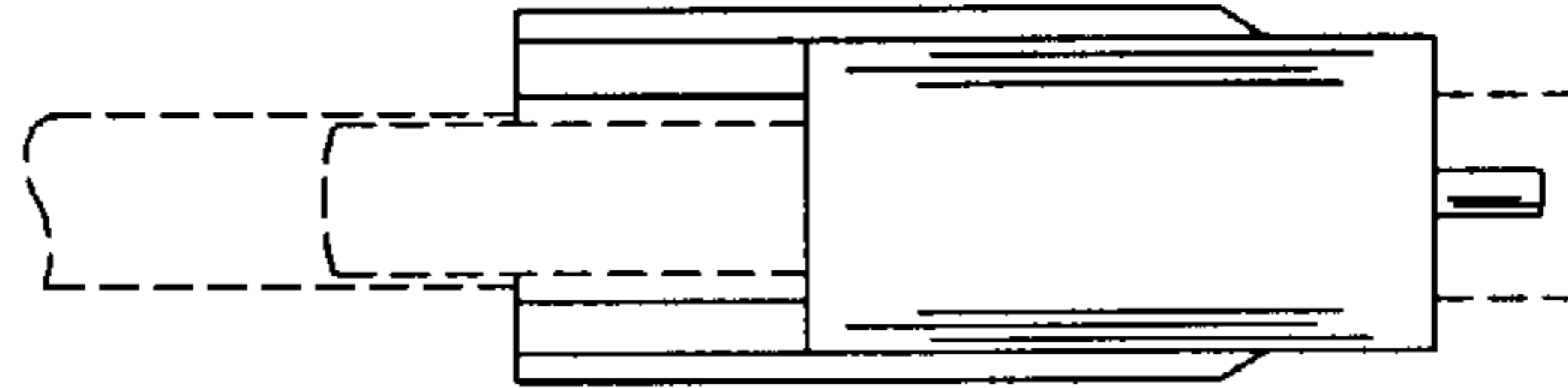


FIG. 5

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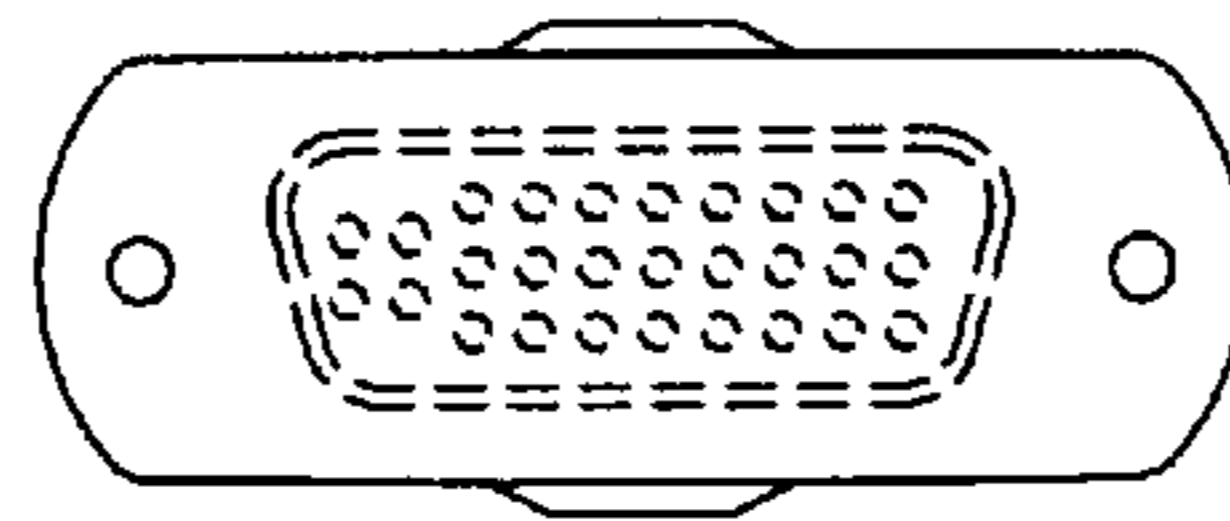


FIG. 6

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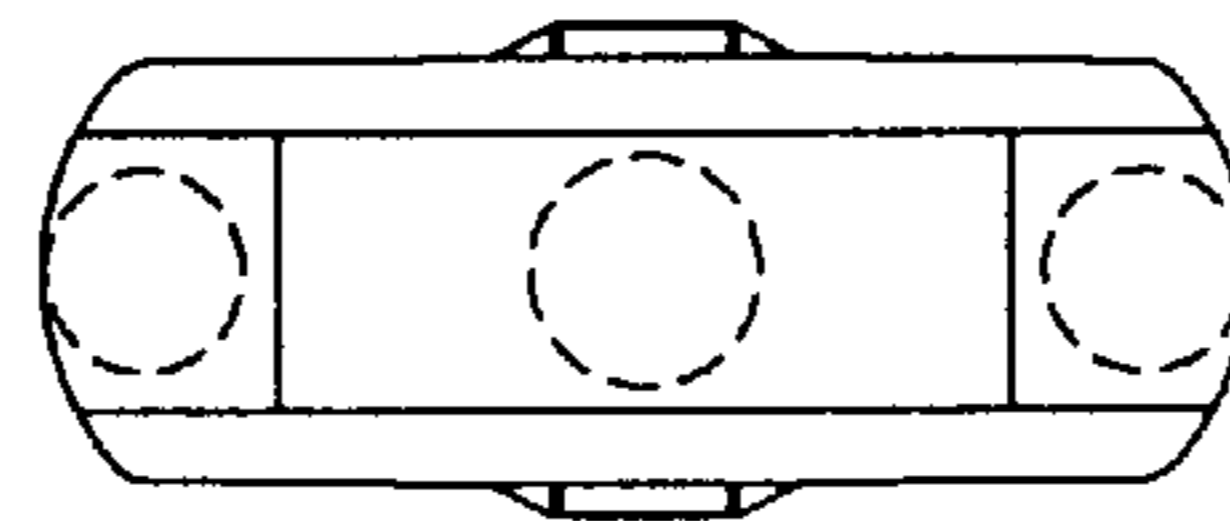


FIG. 7

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