



US00D510956S

(12) **United States Design Patent** (10) **Patent No.:** **US D510,956 S**
Yasuoka et al. (45) **Date of Patent:** **** Oct. 25, 2005**

(54) **MASK FOR A DISPLAY UNIT AND DISPLAY UNIT FOR AN ELECTRONIC DISPLAY BOARD**

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(73) Assignee: **Nichia Corporation (JP)**

(**) Term: **14 Years**

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Related U.S. Application Data

(62) Division of application No. 29/217,610, filed on Nov. 19, 2004, now Pat. No. Des. 506,510, which is a division of application No. 29/194,759, filed on Dec. 1, 2003, now Pat. No. Des. 503,745.

(30) **Foreign Application Priority Data**

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May 30, 2003	(JP)	2003-019266
May 30, 2003	(JP)	2003-019267

(51) **LOC (8) Cl.** **20-02**

(52) **U.S. Cl.** **D20/10; D20/12; D20/19**

(58) **Field of Search** **D20/10, 18-21, D20/39, 40-42, 99; D10/46.1; 40/448-454, 541-2, 553, 573, 584; 52/38, 73; 340/323; 712/217**

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(57) **CLAIM**

The ornamental design for a mask for a display unit and display unit for an electronic display board, as shown and described.

DESCRIPTION

FIG. 1 is a front elevational view of a mask for display unit and display unit for an electronic display board, showing our new design;

FIG. 2 is a top plan view thereof;

FIG. 3 is a bottom plan view thereof;

FIG. 4 is a left side elevational view thereof;

FIG. 5 is a right side elevational view thereof;

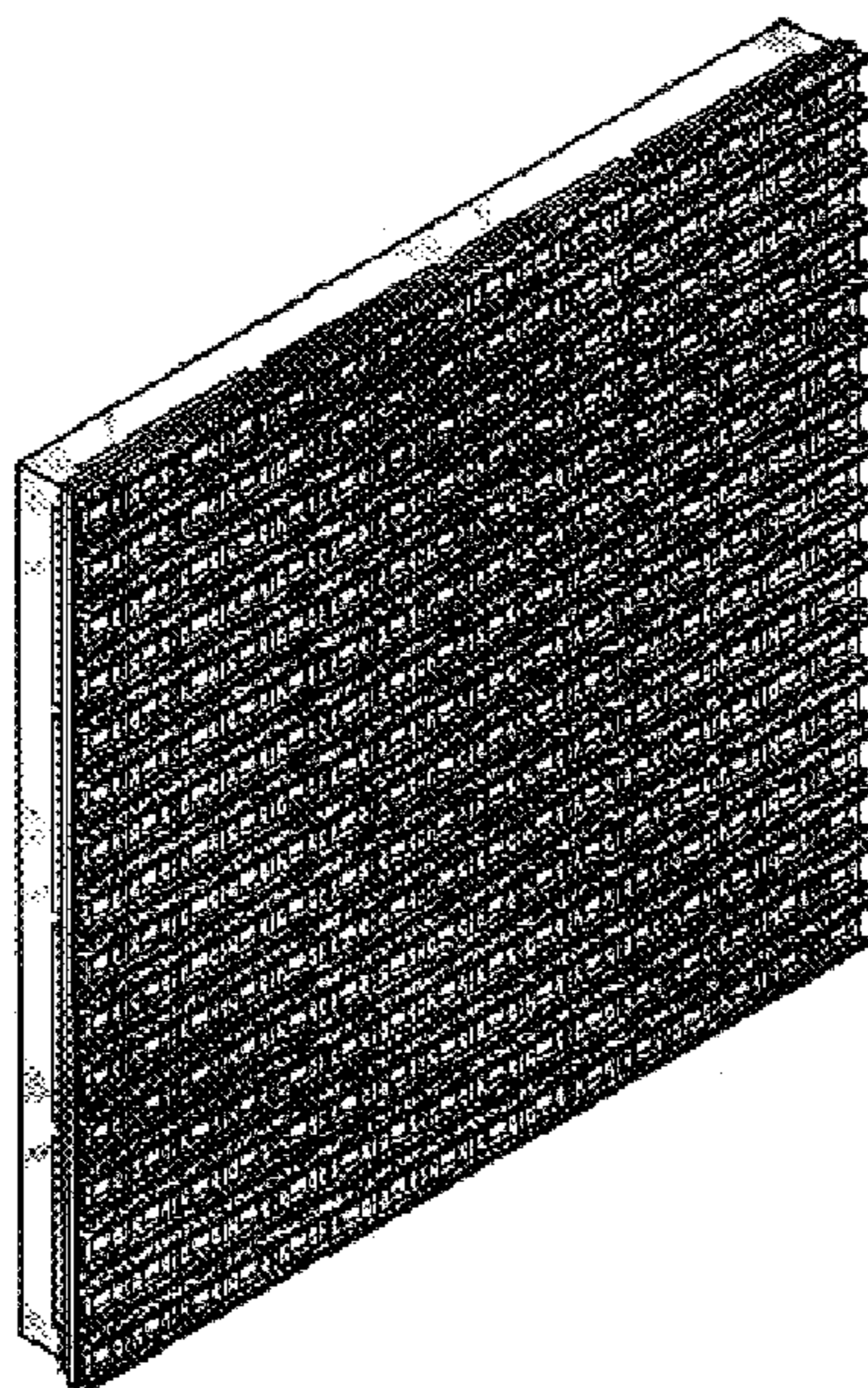
FIG. 6 is a front left perspective view thereof;

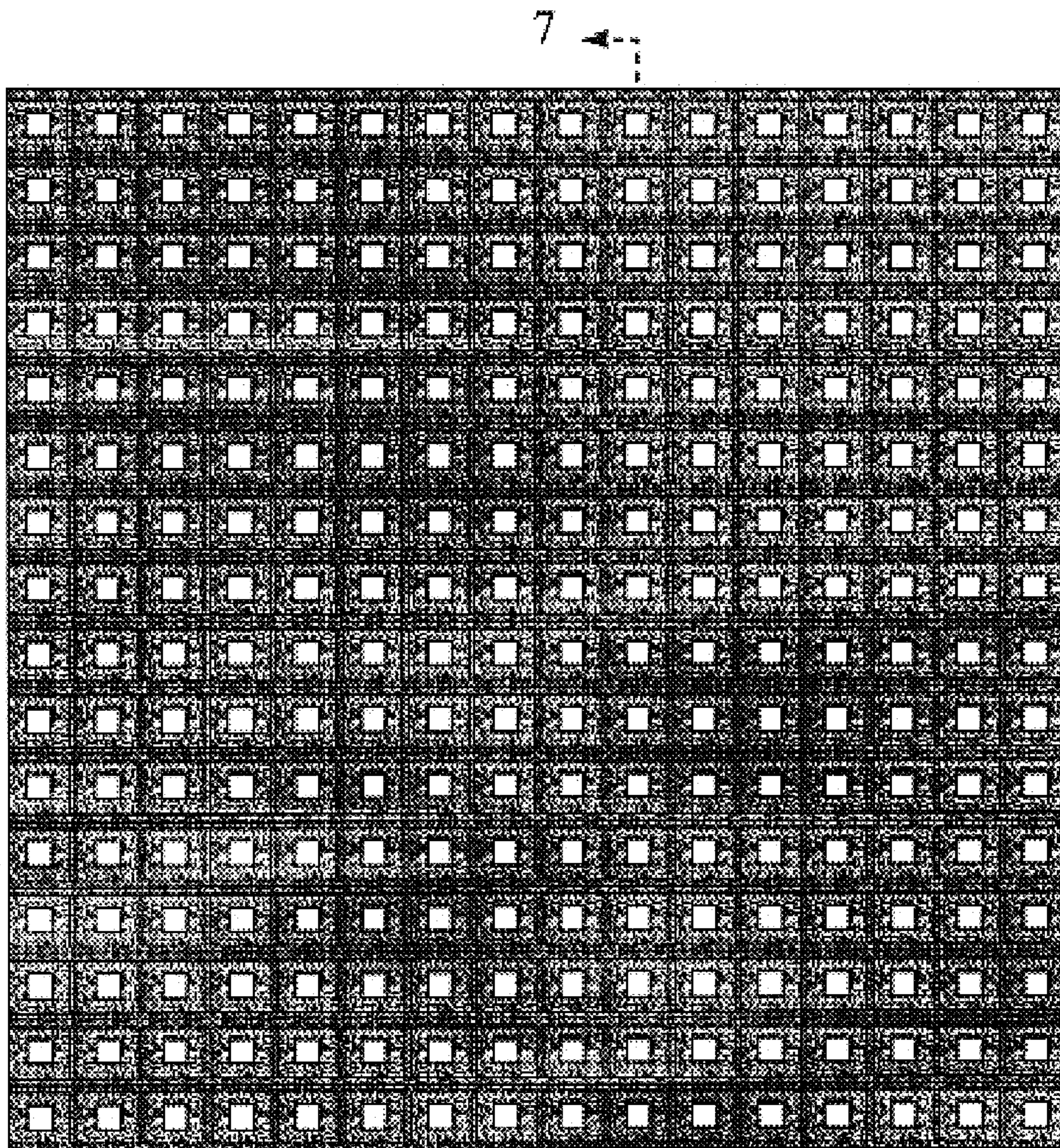
FIG. 7 is a sectional view thereof, taken along line 7—7 of FIG. 1;

FIG. 8 is a sectional view thereof, taken along line 8—8 of FIG. 7; and,

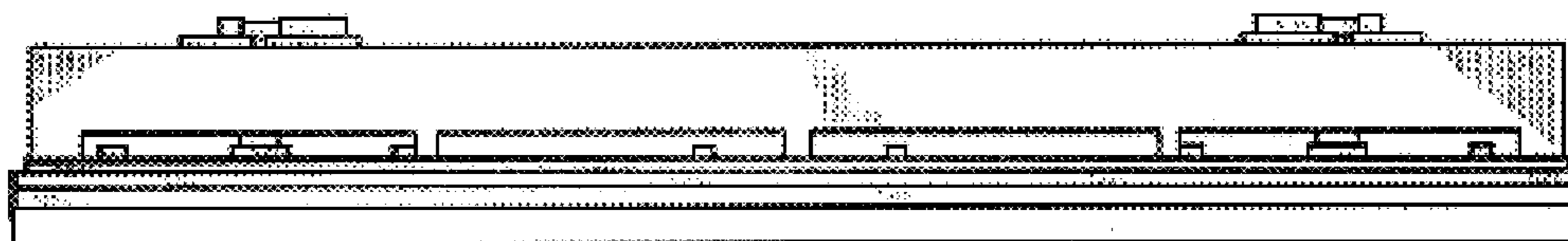
FIG. 9 is a sectional view thereof, taken along line 9—9 of FIG. 7.

1 Claim, 5 Drawing Sheets





7
Figure 1



7
Figure 2

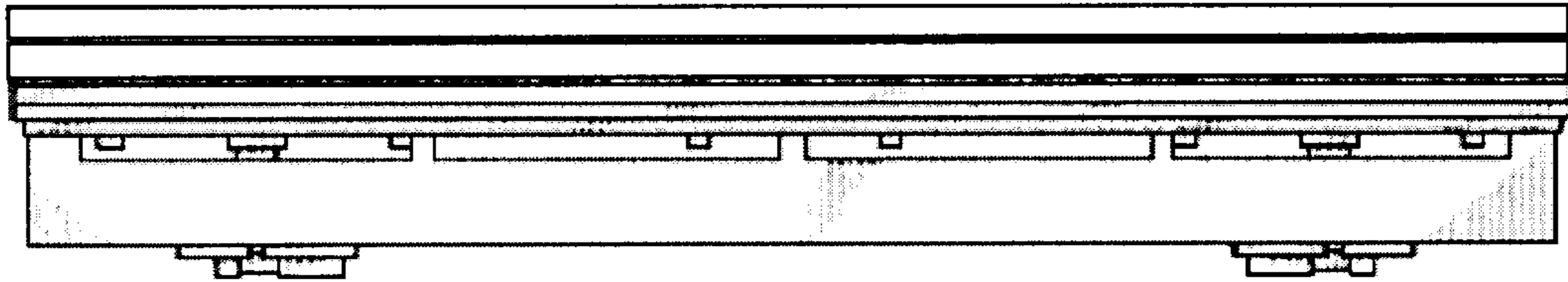


Figure 3

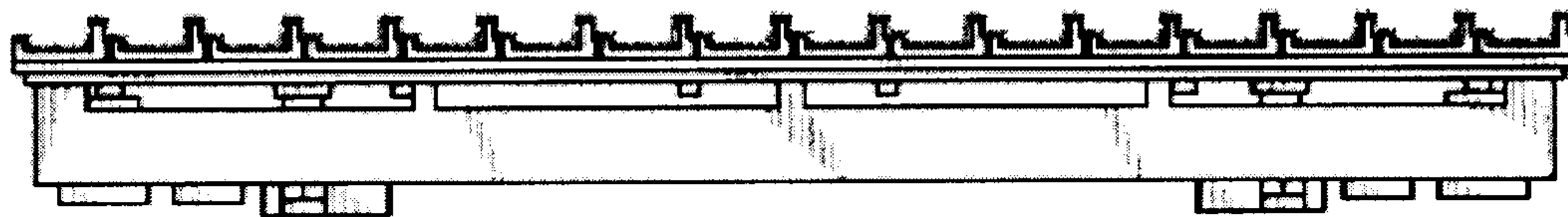


Figure 4

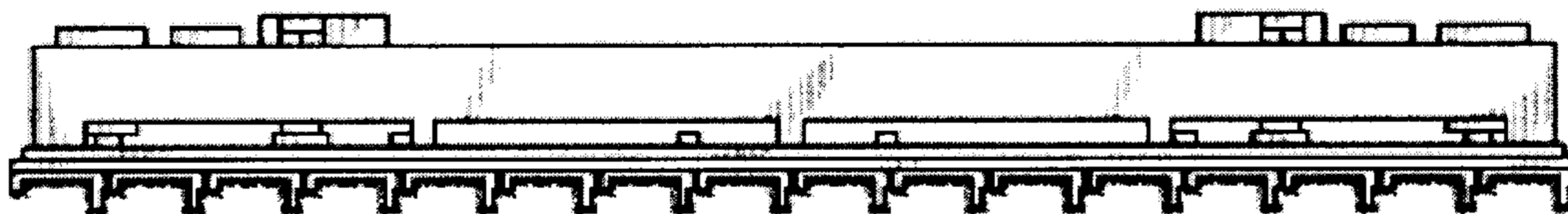


Figure 5

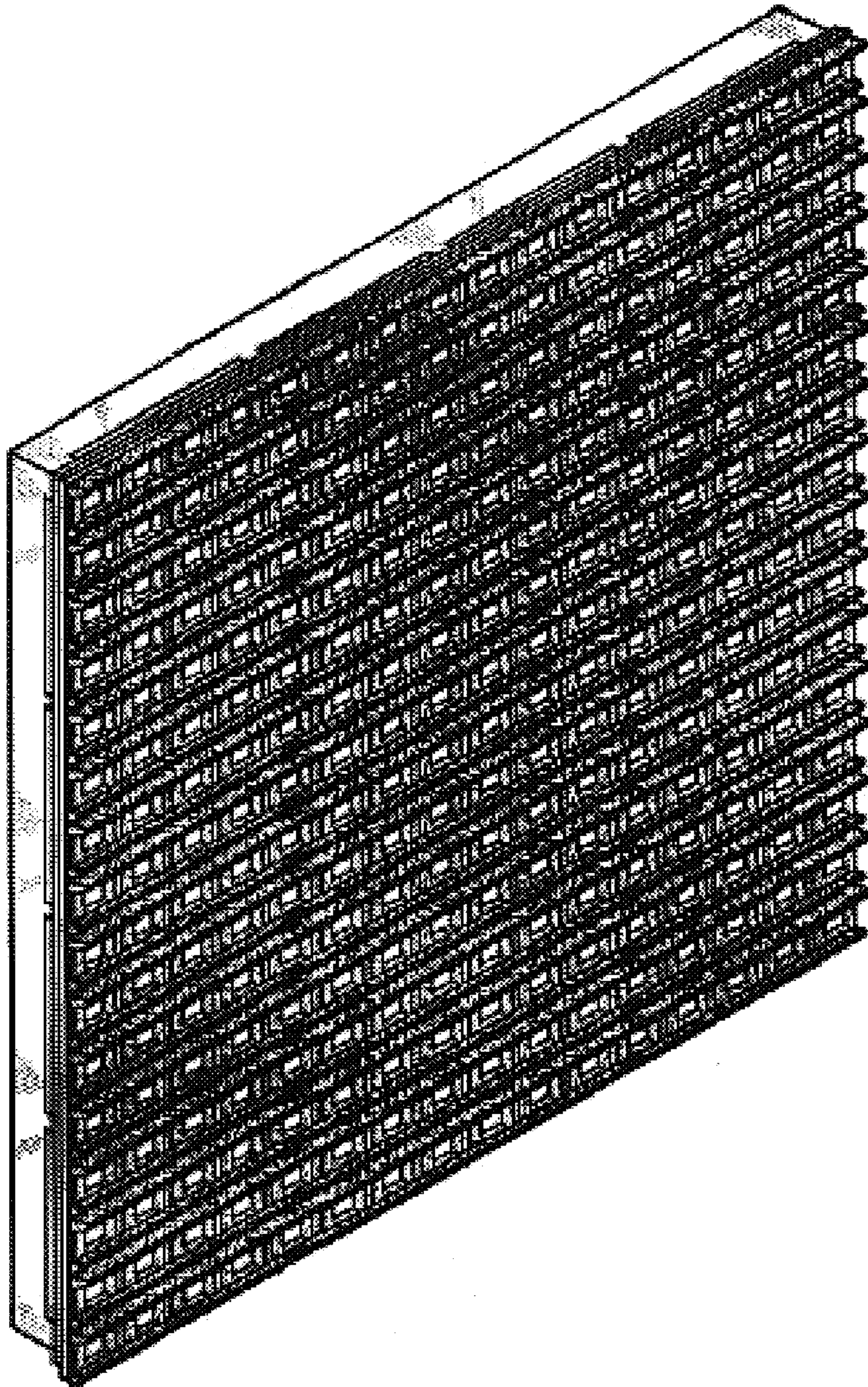


Figure 6

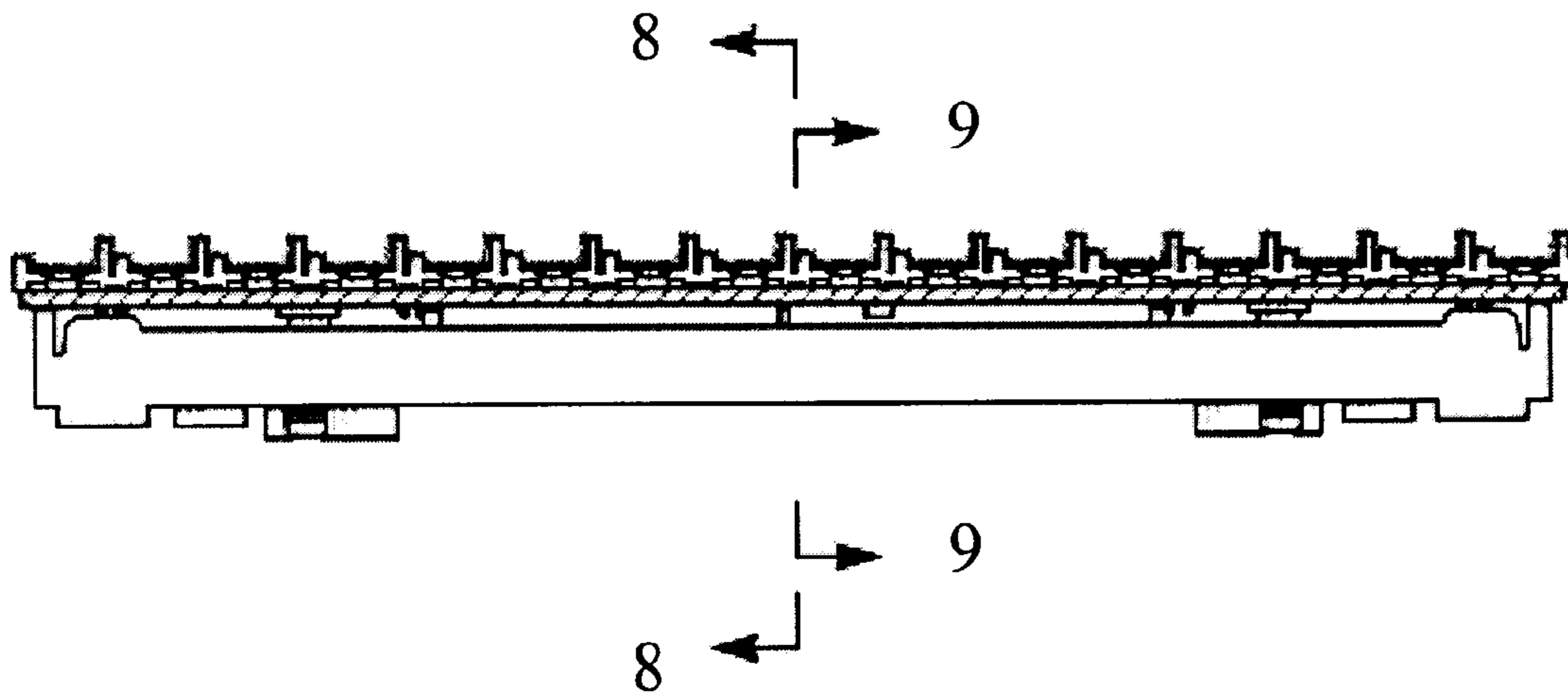


Figure 7

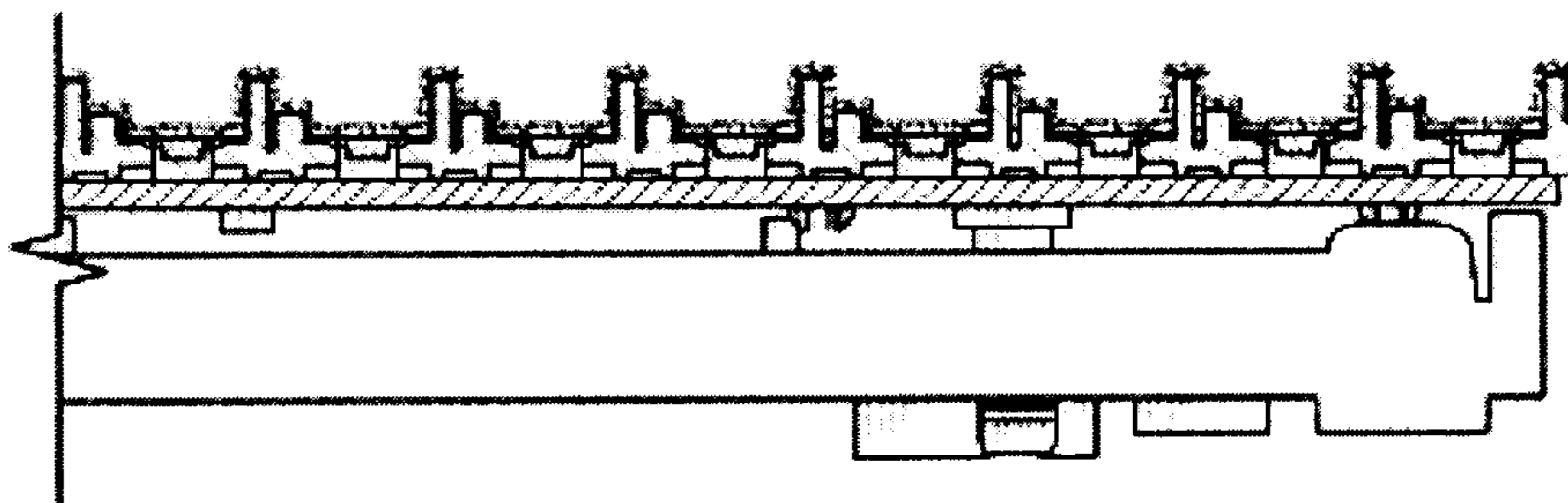


Figure 8

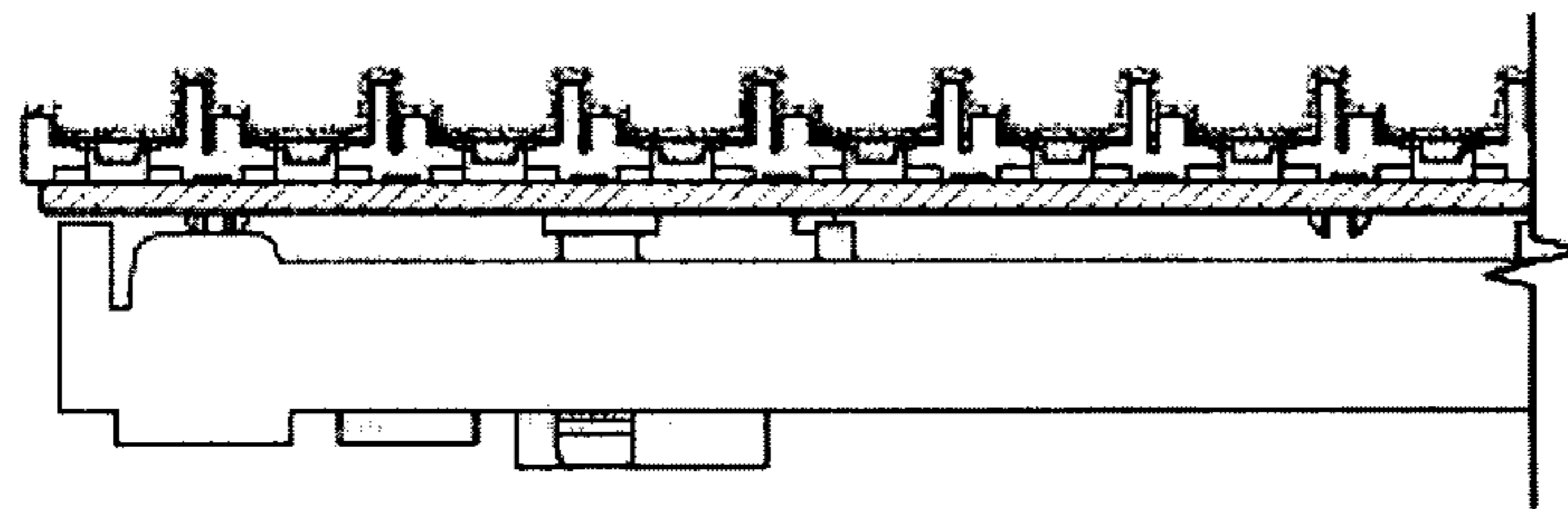


Figure 9