



US00D510579S

(12) **United States Design Patent** (10) **Patent No.:** **US D510,579 S**
Yamada et al. (45) **Date of Patent:** **** Oct. 11, 2005**

(54) **IC MEMORY CARD**

D467,586 S * 12/2002 Shimoda et al. D14/436
D475,056 S 5/2003 Taniguchi et al.
D476,011 S 6/2003 Taniguchi et al.

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FOREIGN PATENT DOCUMENTS

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* cited by examiner

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Lione

(**) Term: **14 Years**

(57) **CLAIM**

The ornamental design for an IC memory card, as shown and described.

(21) Appl. No.: **29/208,281**

DESCRIPTION

(22) Filed: **Jun. 24, 2004**

Related U.S. Application Data

(62) Division of application No. 29/169,130, filed on Jan. 15, 2002.

(51) **LOC (8) Cl.** **14-02**

(52) **U.S. Cl.** **D14/436**

(58) **Field of Search** D14/435-8, 478-80;
361/736-7, 686, 752; 257/678-9; 40/124.01;
283/75-6, 901; 235/487-95, 441-3, 375;
439/135, 140, 76.1; D8/347

FIG. 1 is a perspective view of a first embodiment of an IC memory card showing our new design; FIG. 2 is a front elevational view thereof; FIG. 3 is a rear elevational view thereof; FIG. 4 is a left side view thereof; FIG. 5 is a right side view thereof; FIG. 6 is a top plan view thereof; FIG. 7 is a bottom plan view thereof. FIG. 8 is a perspective view of a second embodiment of an IC memory card showing our new design; FIG. 9 is a front elevational view thereof; FIG. 10 is a rear elevational view thereof; FIG. 11 is a left side view thereof; FIG. 12 is a right side view thereof; FIG. 13 is a top plan view thereof; and, FIG. 14 is a bottom plan view thereof.

(56) **References Cited**

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The broken line drawings in FIGS. 1, 2, 8 and 9 are for illustrative purposes only and form no part of the claimed design.

1 Claim, 14 Drawing Sheets

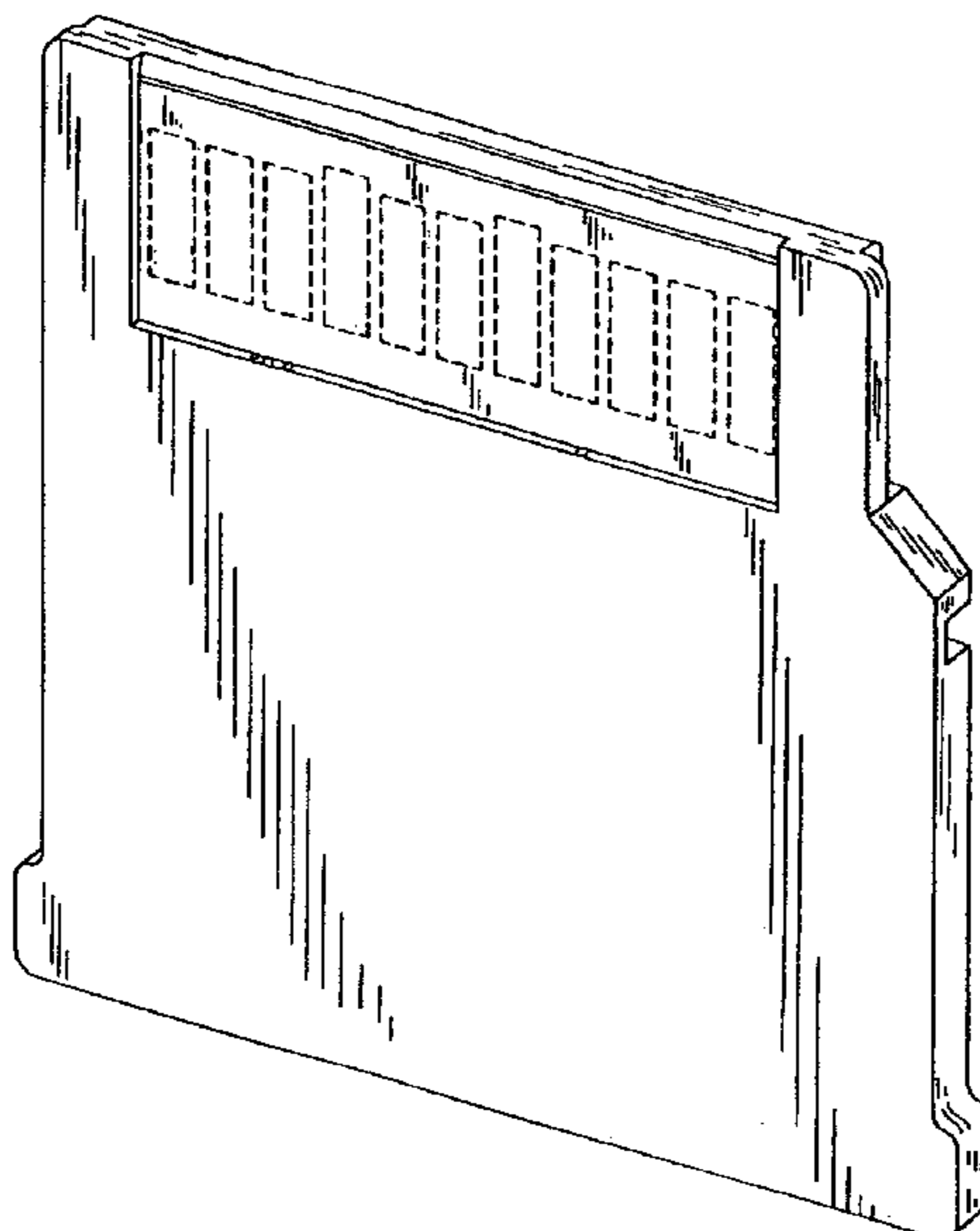


FIG. 1

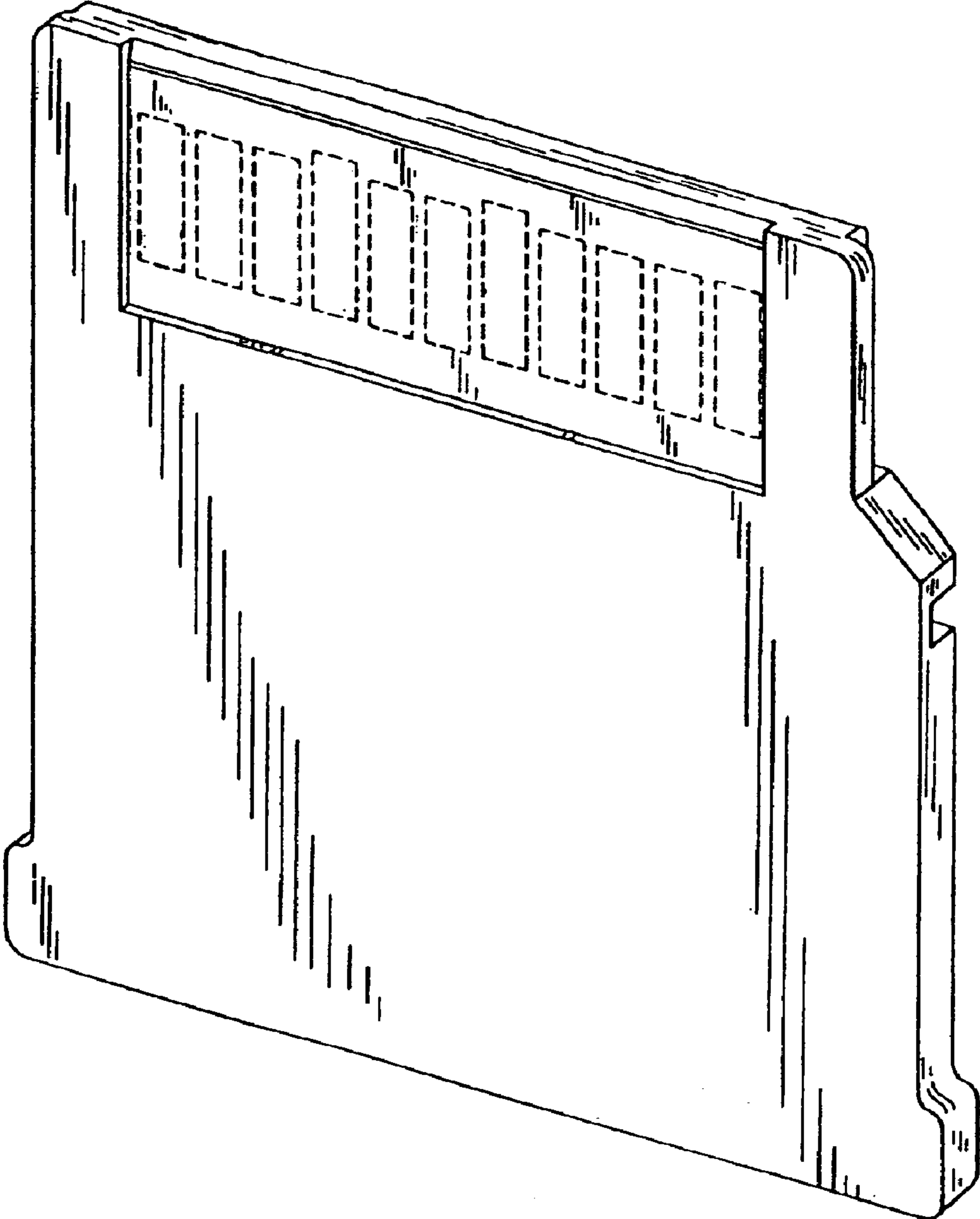


FIG. 2

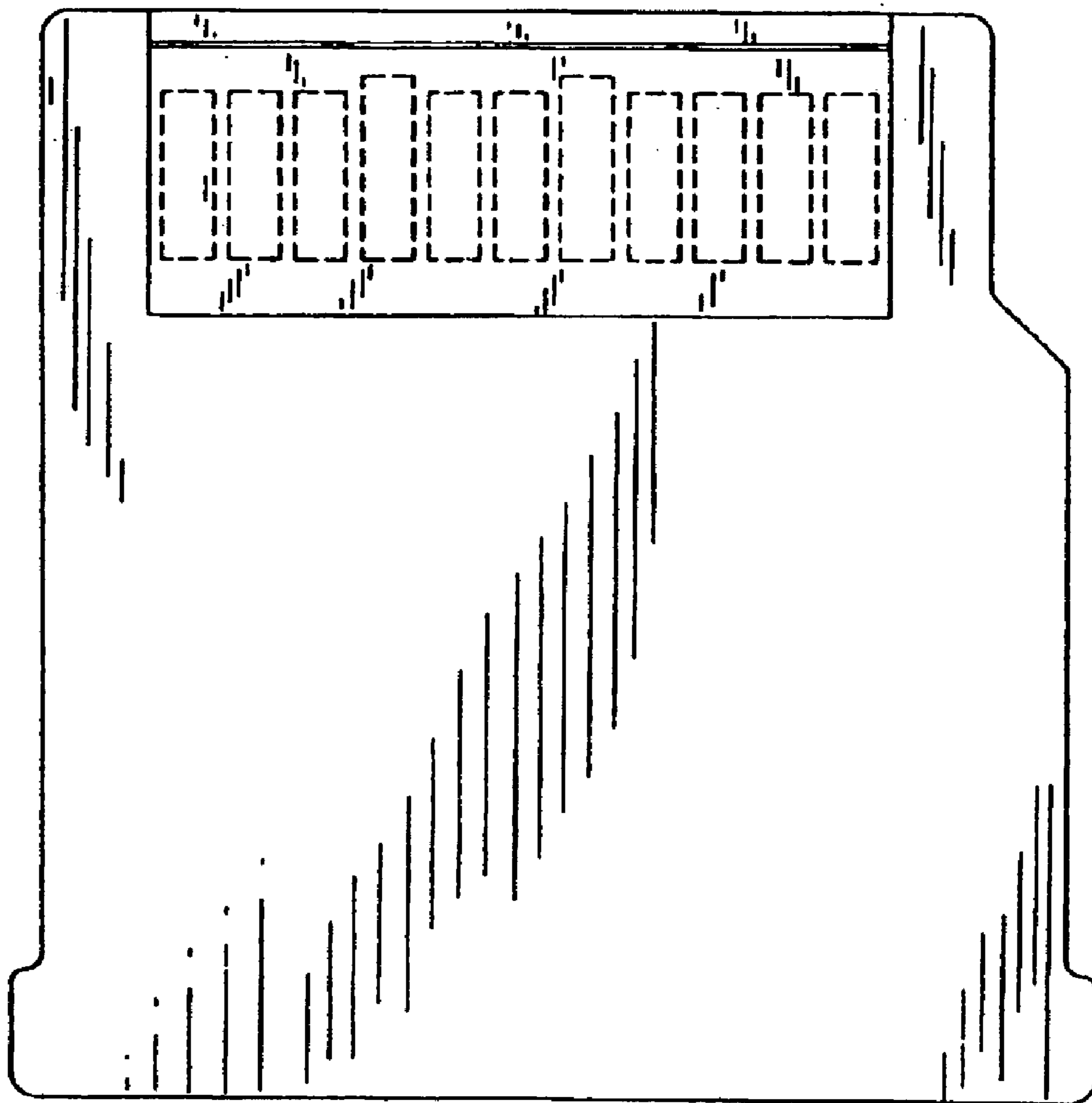


FIG. 3

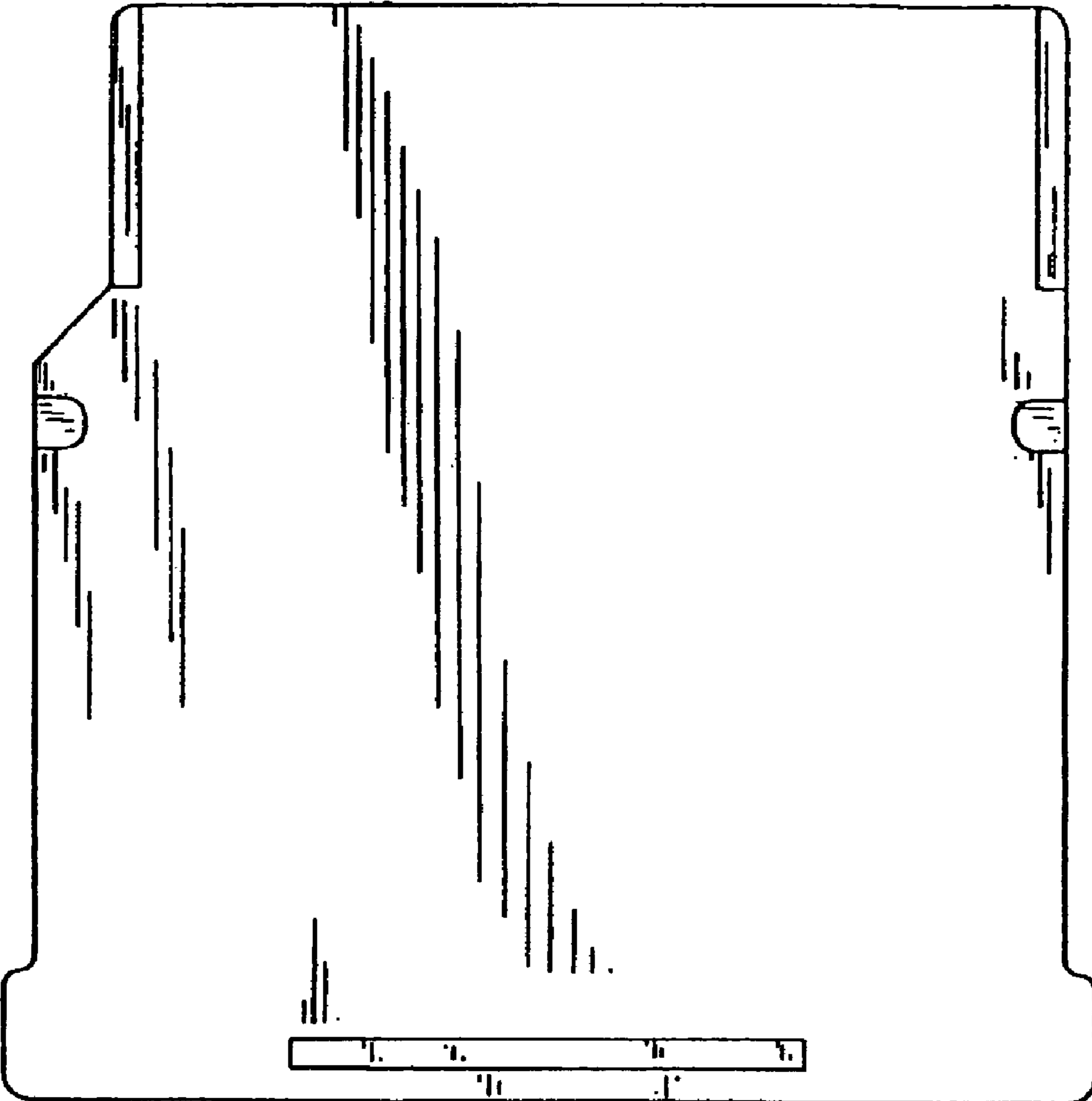


FIG. 4



FIG. 5



FIG. 6



FIG. 7



FIG. 8

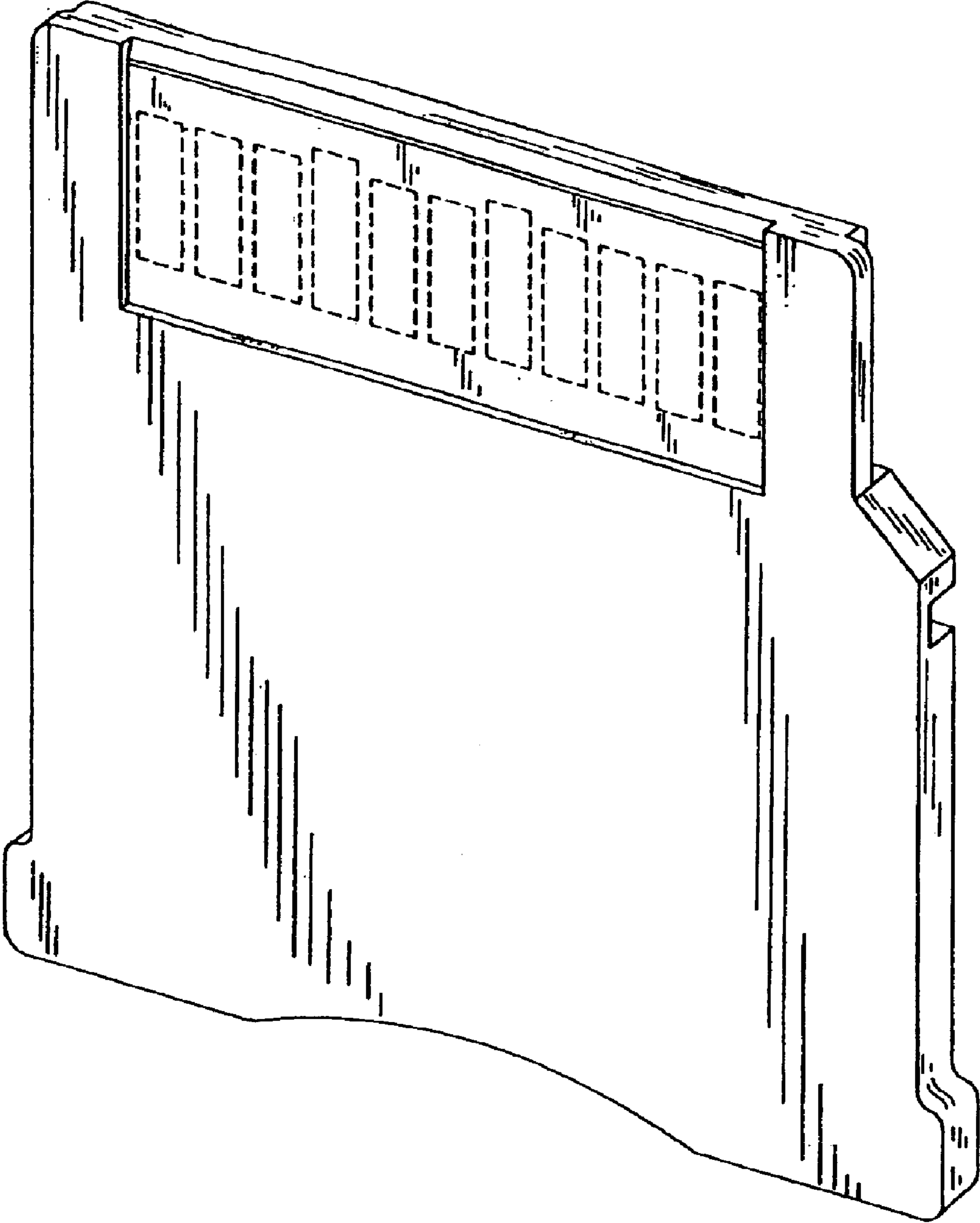


FIG. 9

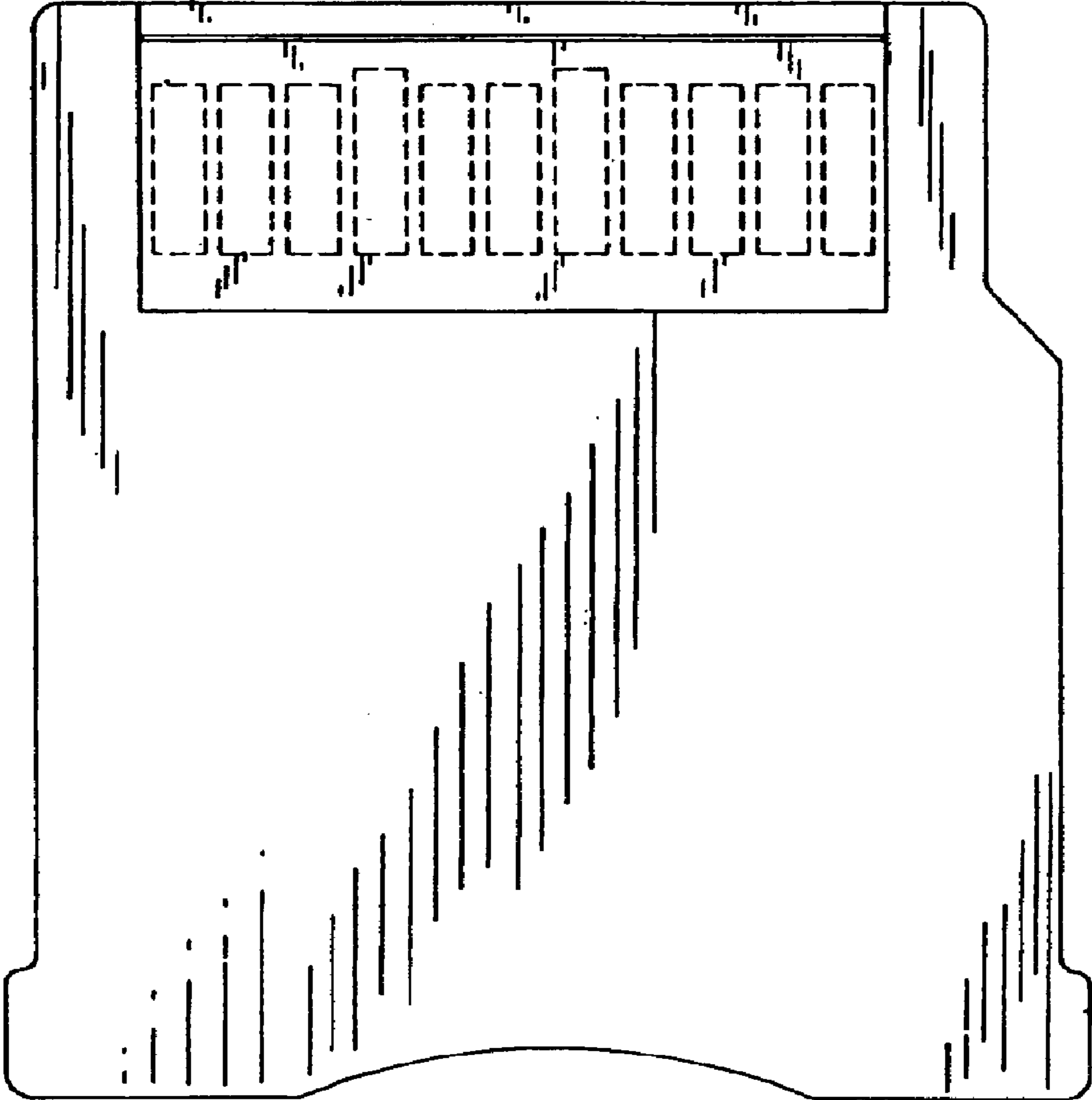


FIG. 10

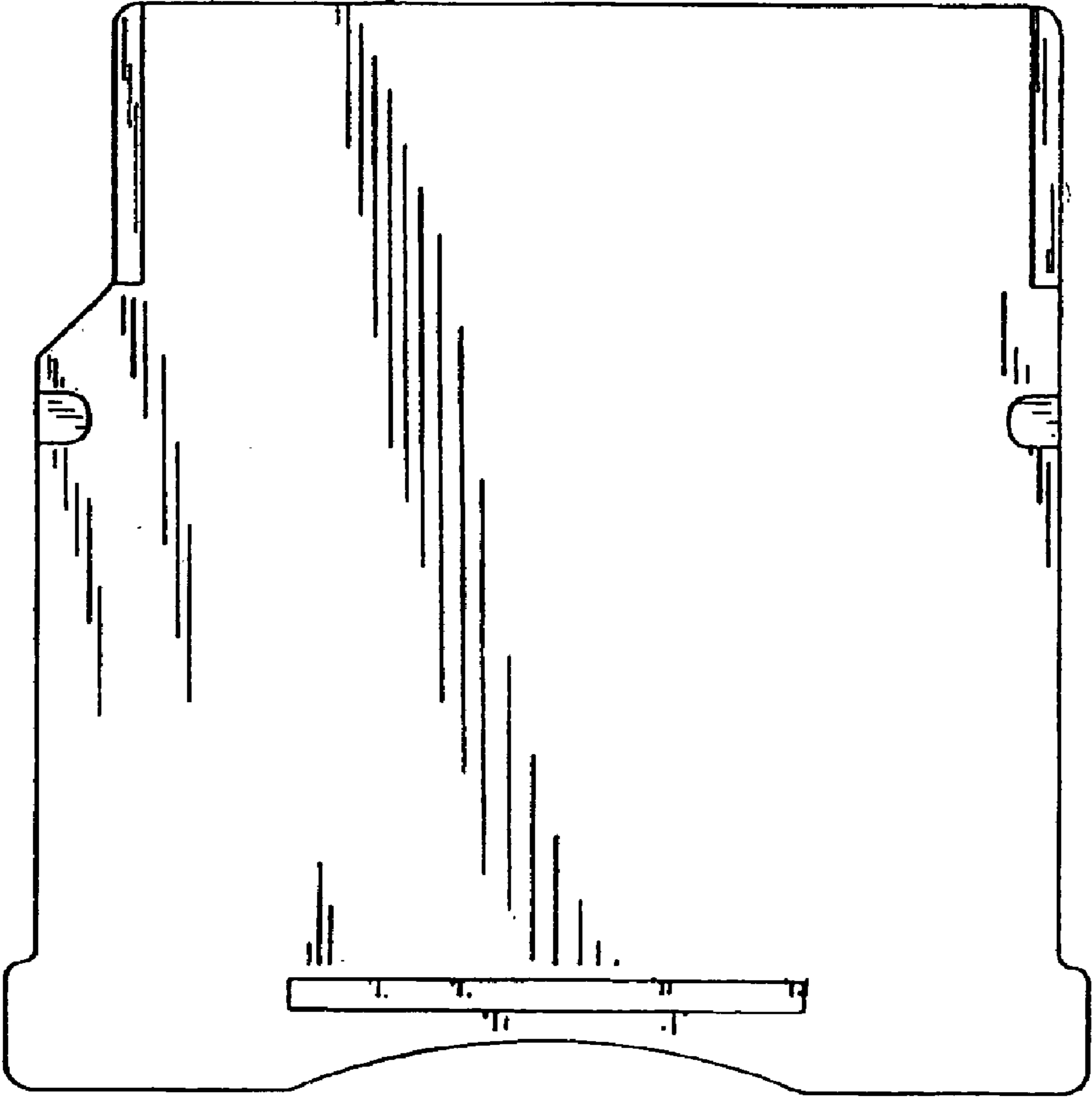


FIG. 11



FIG. 12

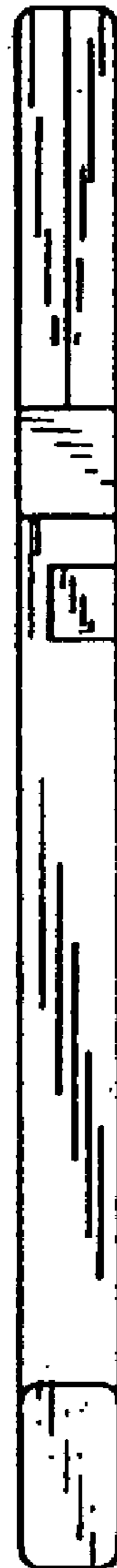


FIG. 13



FIG. 14



UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : Des. 510,579 S
DATED : October 11, 2005
INVENTOR(S) : Hiroyuki Yamada et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page.

Item [62], **Related U.S. Application Data**, delete "Jan. 15," and substitute -- Oct. 15, -- in its place.

Signed and Sealed this

Twenty-fourth Day of January, 2006

A handwritten signature in black ink on a light gray dotted background. The signature reads "Jon W. Dudas" in a cursive style.

JON W. DUDAS

Director of the United States Patent and Trademark Office