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(12) **United States Design Patent** (10) **Patent No.:** **US D507,783 S**
Phipps et al. (45) **Date of Patent:** **** Jul. 26, 2005**

(54) **EMBEDDED CIRCUIT BOARD HOUSING**

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(**) Term: **14 Years**

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(51) **LOC (8) Cl.** **13-03**

(52) **U.S. Cl.** **D13/184**

(58) **Field of Search** D13/182, 184;
D9/418; 174/50, 52.1; 206/706, 707, 709;
220/4.02; 361/600, 601, 730, 736, 748;
438/107

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(57) **CLAIM**

We claim the ornamental design for an embedded circuit board housing, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of an embedded circuit board housing in accordance with the design;

FIG. 2 is a front view of the embedded circuit board housing of FIG. 1;

FIG. 3 is a back view of the embedded circuit board housing of FIG. 1;

FIG. 4 is a left view of the embedded circuit board housing of FIG. 1;

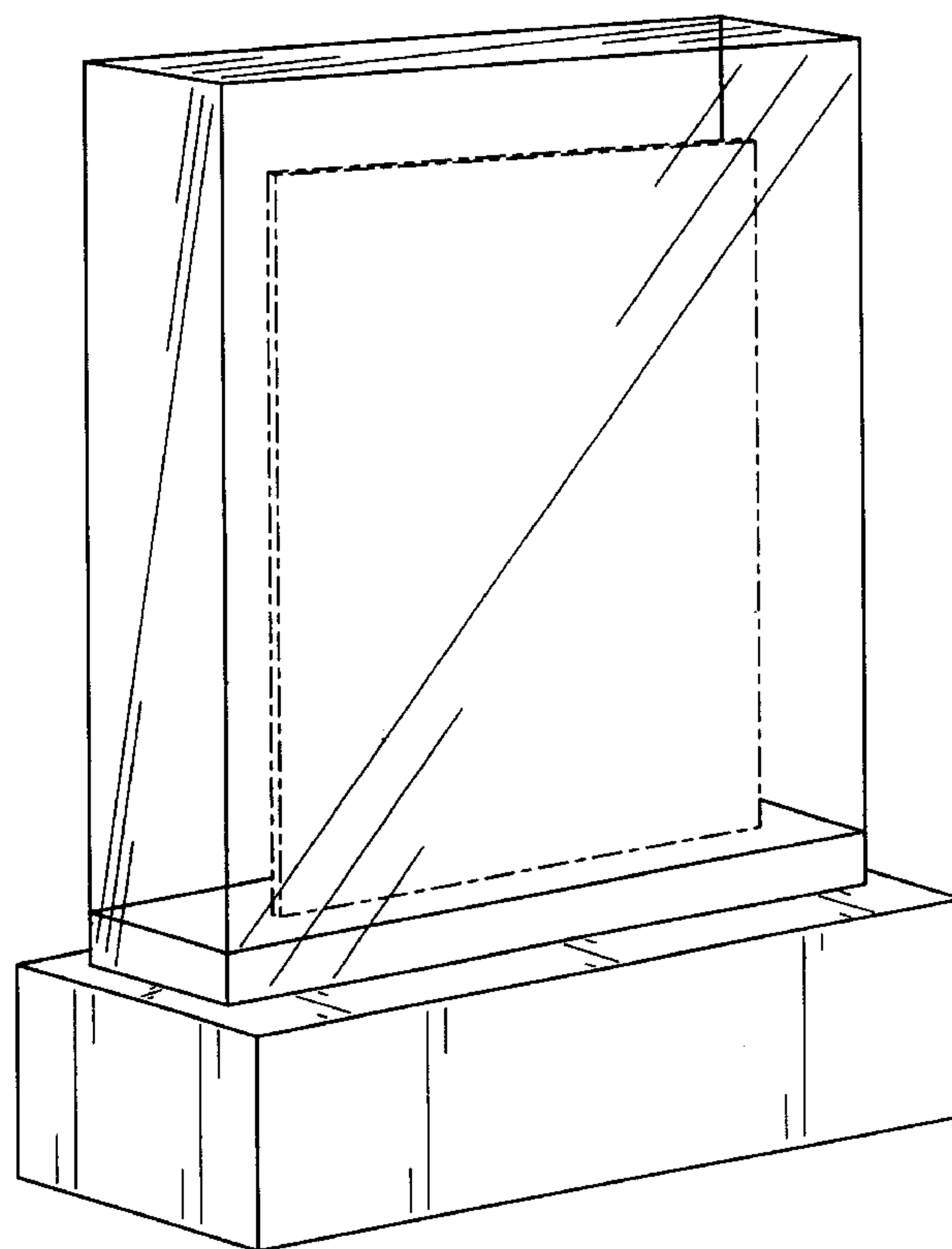
FIG. 5 is a right view of the embedded circuit board housing of FIG. 1;

FIG. 6 is a top view of the embedded circuit board housing of FIG. 1; and,

FIG. 7 is a bottom view of the embedded circuit board housing of FIG. 1.

The broken line drawings of the printed circuit board are for illustrative purposes only and form no part of the claimed design.

1 Claim, 7 Drawing Sheets



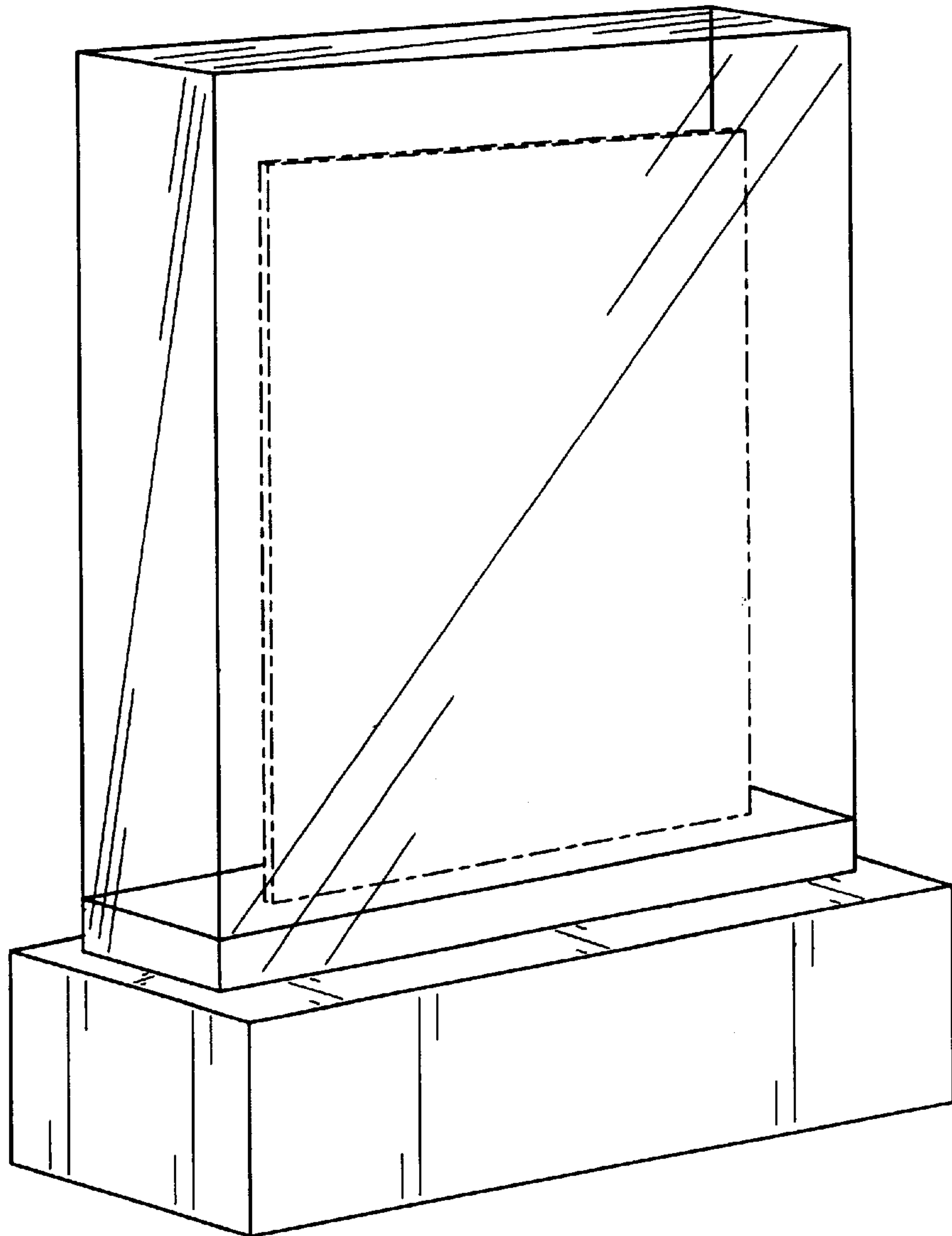


FIG. 1

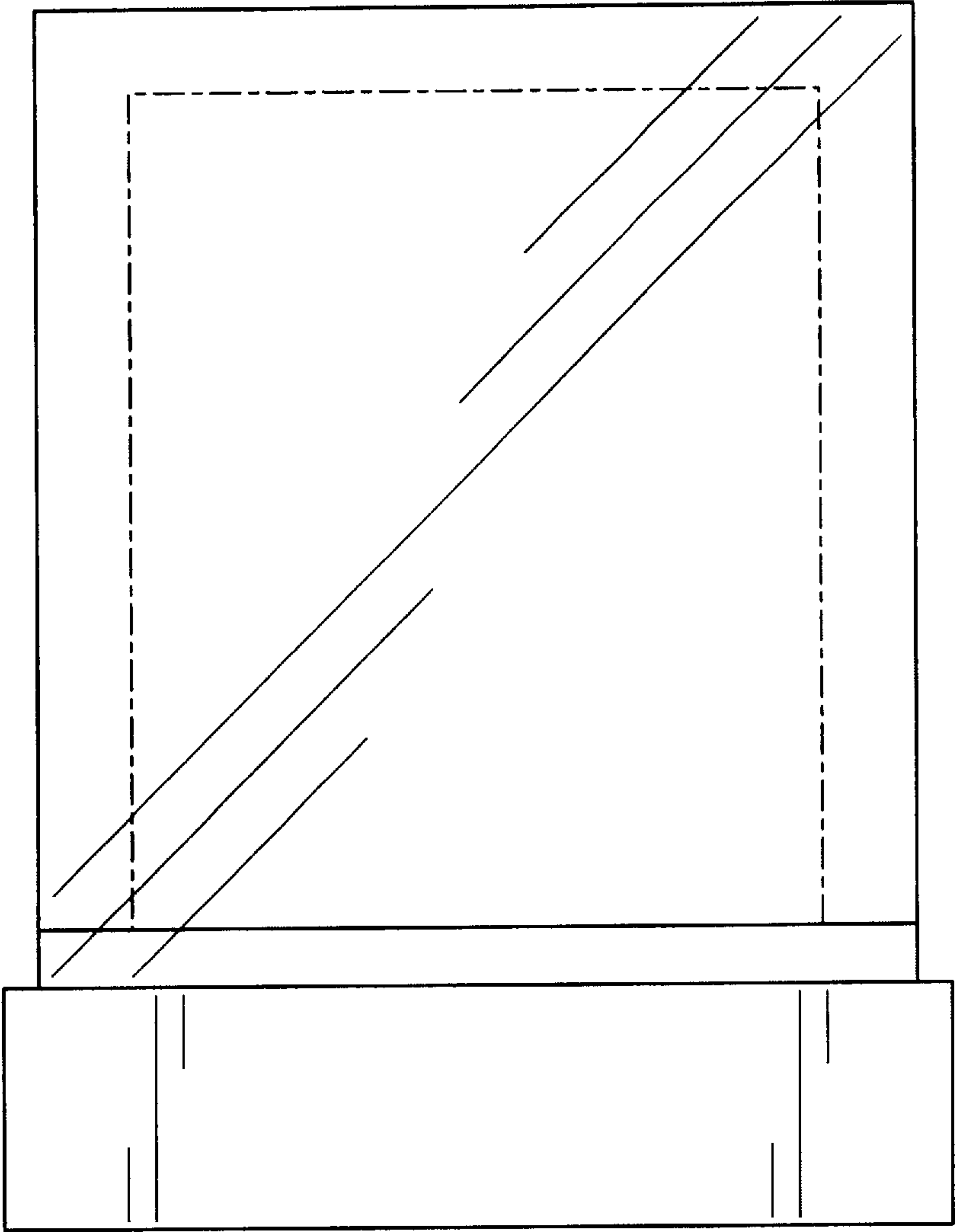


FIG. 2

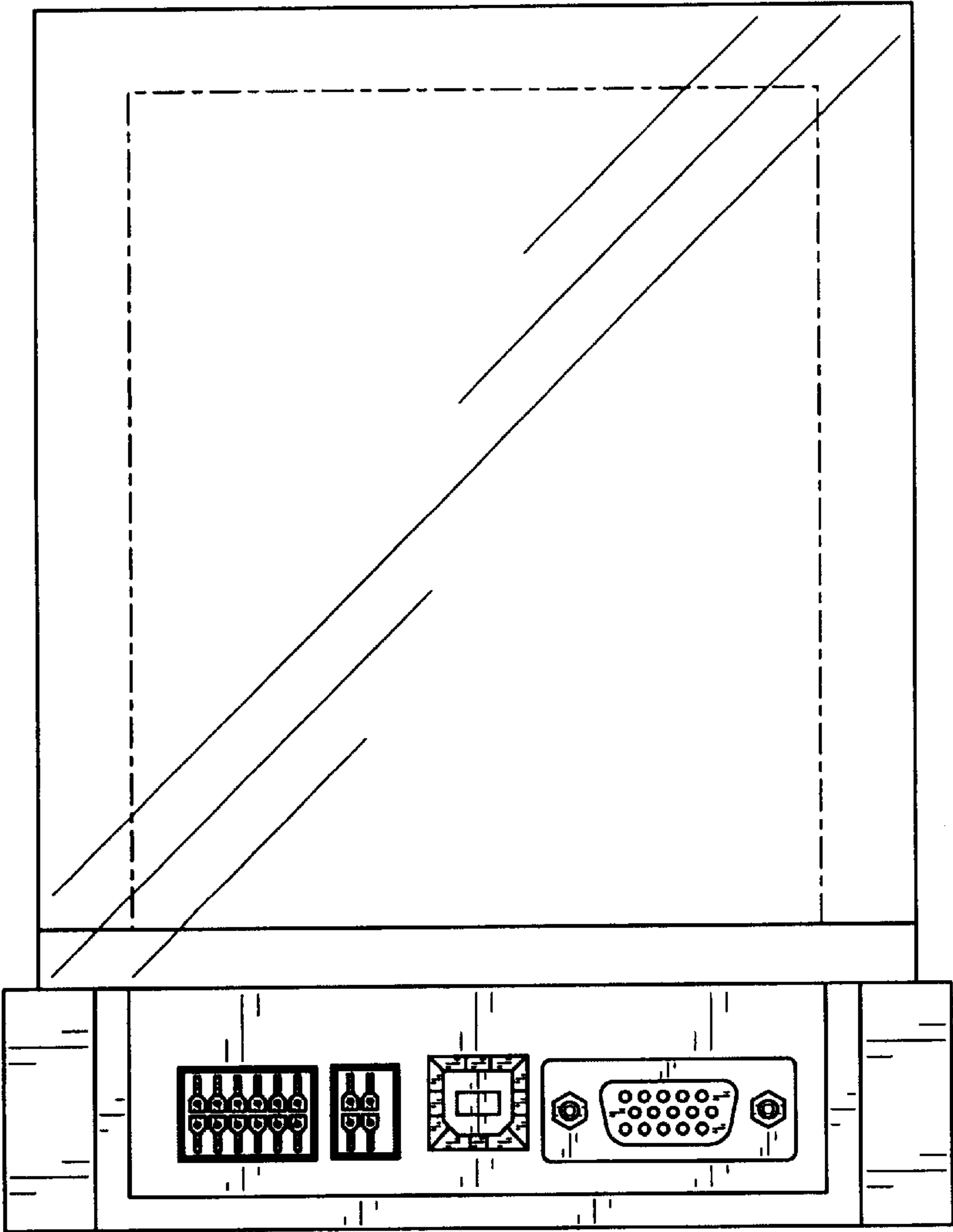


FIG. 3

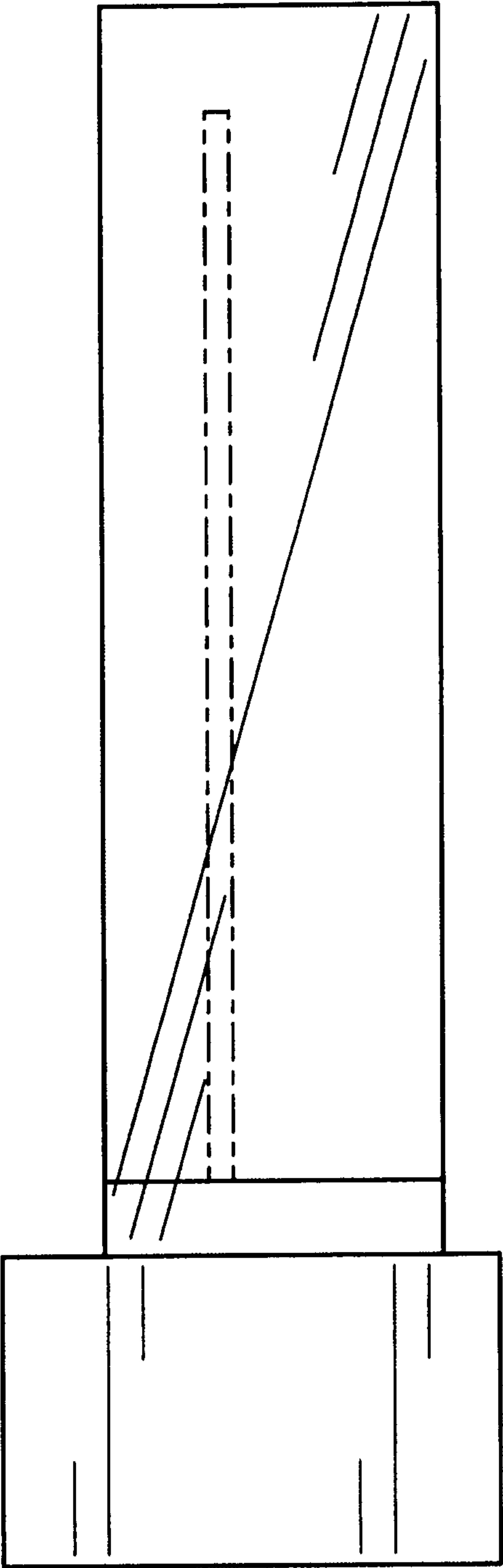


FIG. 4

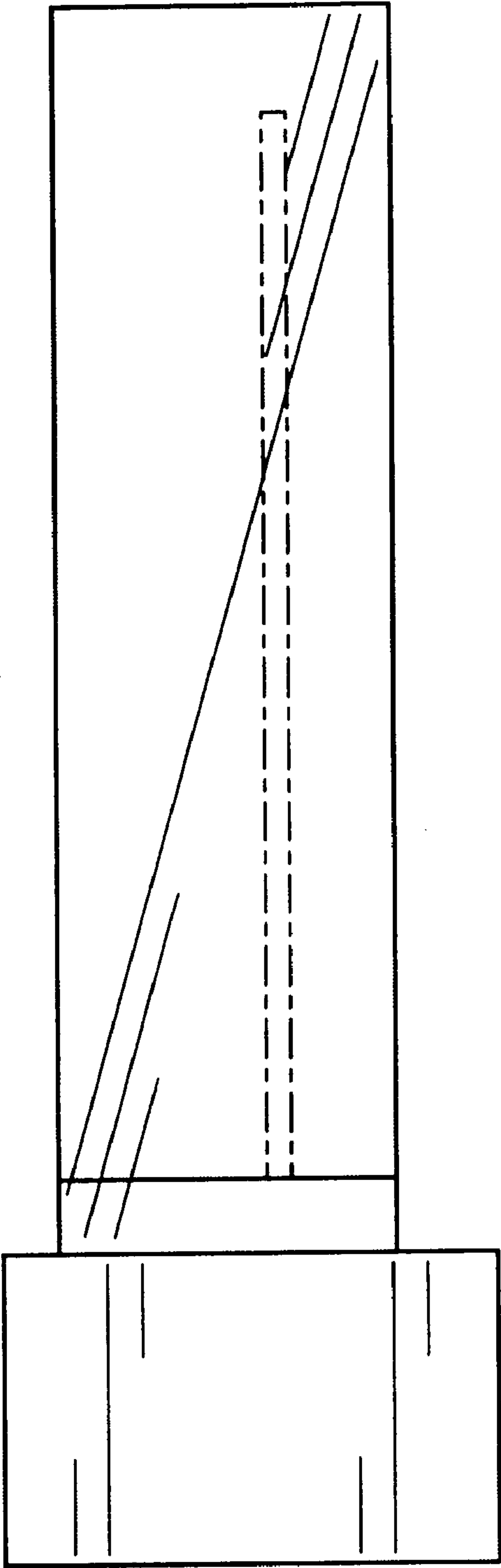


FIG. 5

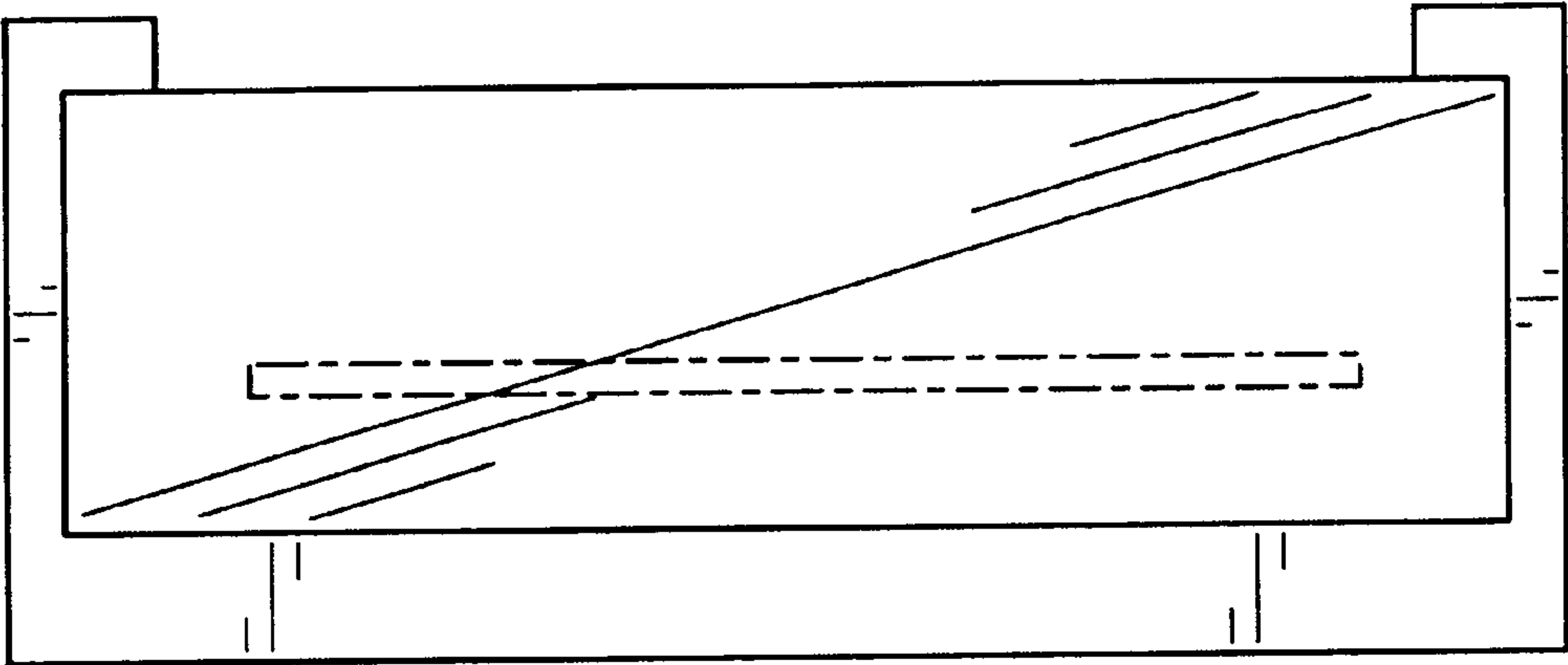


FIG. 6

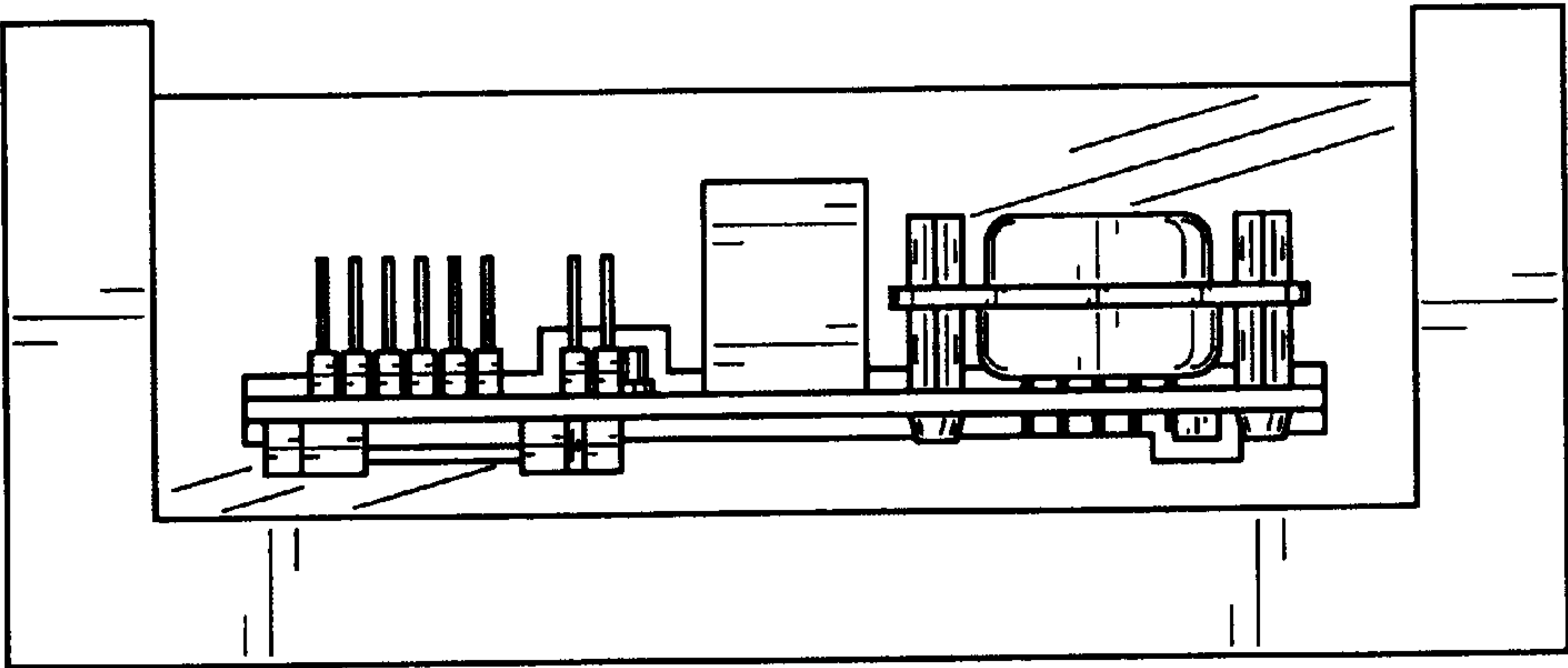


FIG. 7