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(12) **United States Design Patent**
Nakamura et al.

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(45) **Date of Patent:** **** Jul. 19, 2005**

(54) **SEMICONDUCTOR CARRIER**

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(73) Assignee: **Yamaichi Electronics Co., Ltd.**, Tokyo (JP)

(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (8) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Search** D13/182; 257/666,
257/669, 675, 676; 324/755

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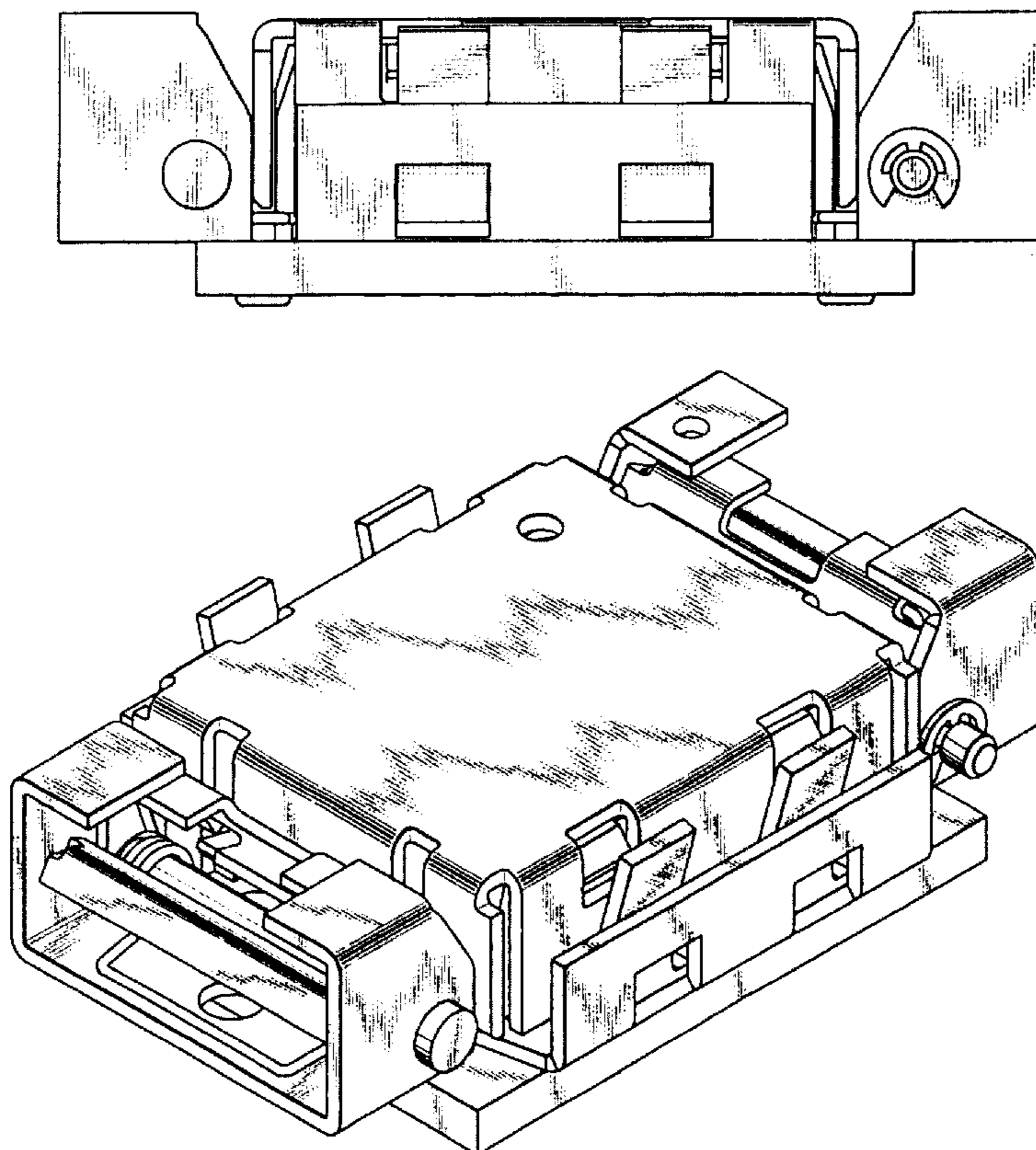
(57) **CLAIM**

The ornamental design for the semiconductor carrier, as shown and described.

DESCRIPTION

FIG. 1 is a front view of a semiconductor carrier showing our new design, the back view being a mirror image thereof; FIG. 2 is a top view of our new design; FIG. 3 is a bottom view of our new design; FIG. 4 is a right-side view of our new design; FIG. 5 is a left-side view of our new design; and, FIG. 6 is a left-front perspective view of our new design.

1 Claim, 3 Drawing Sheets



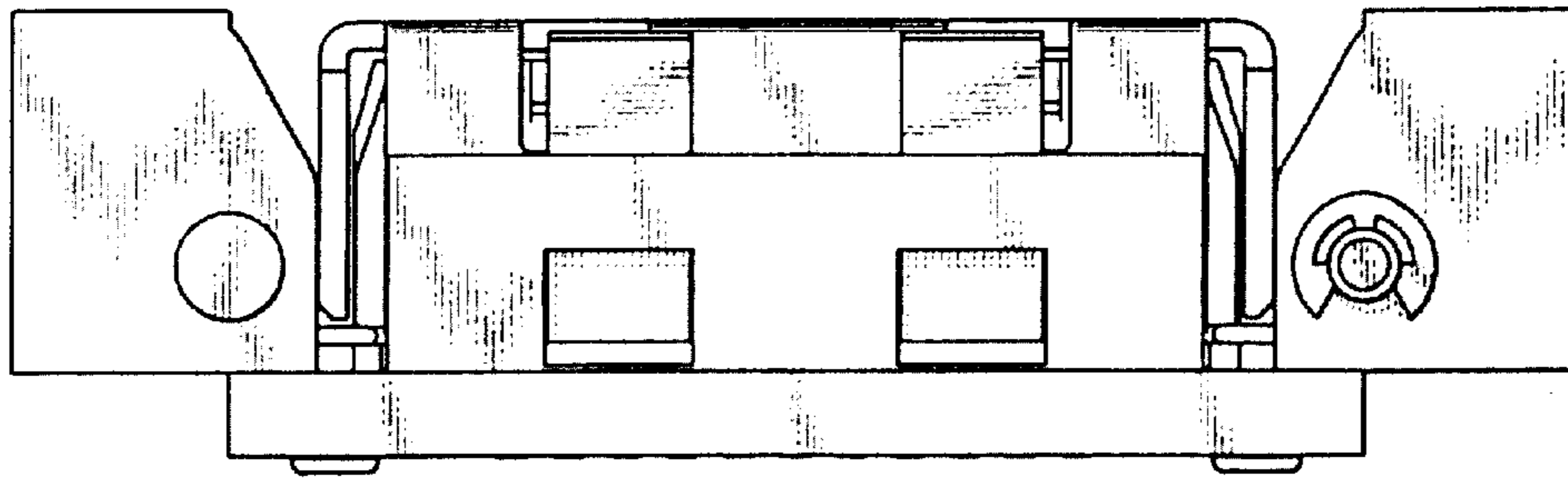


FIG. 1

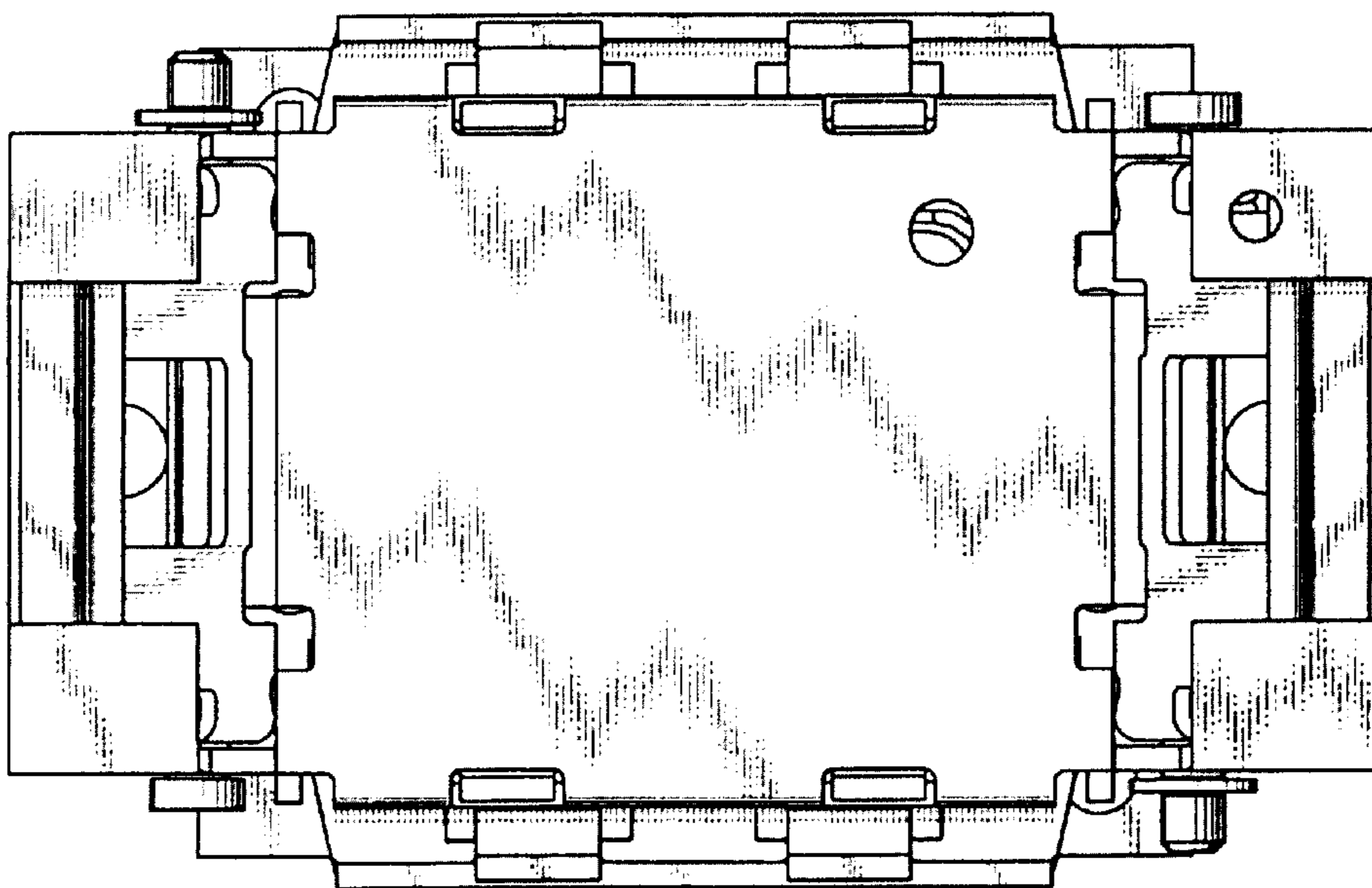


FIG. 2

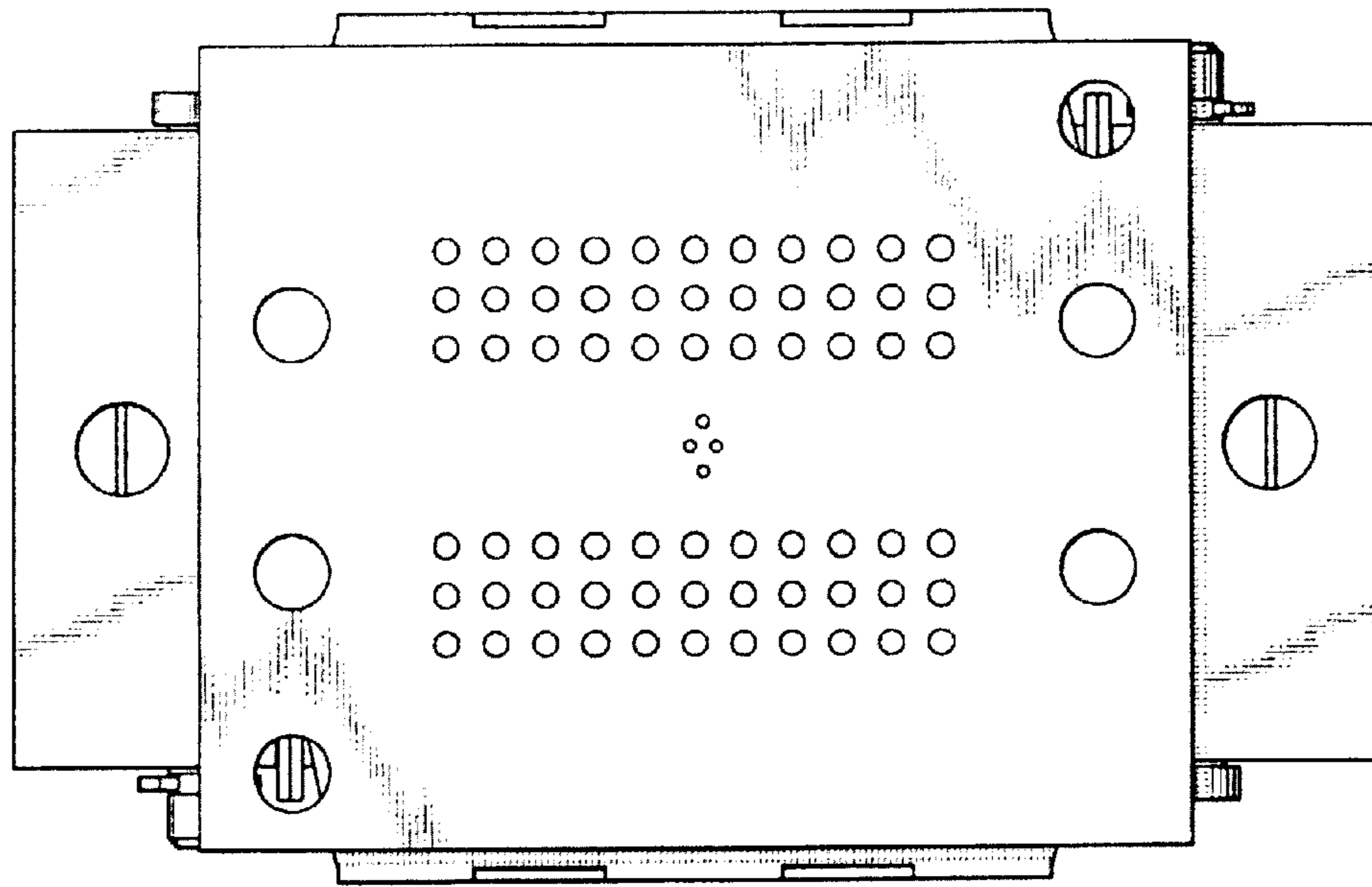


FIG. 3

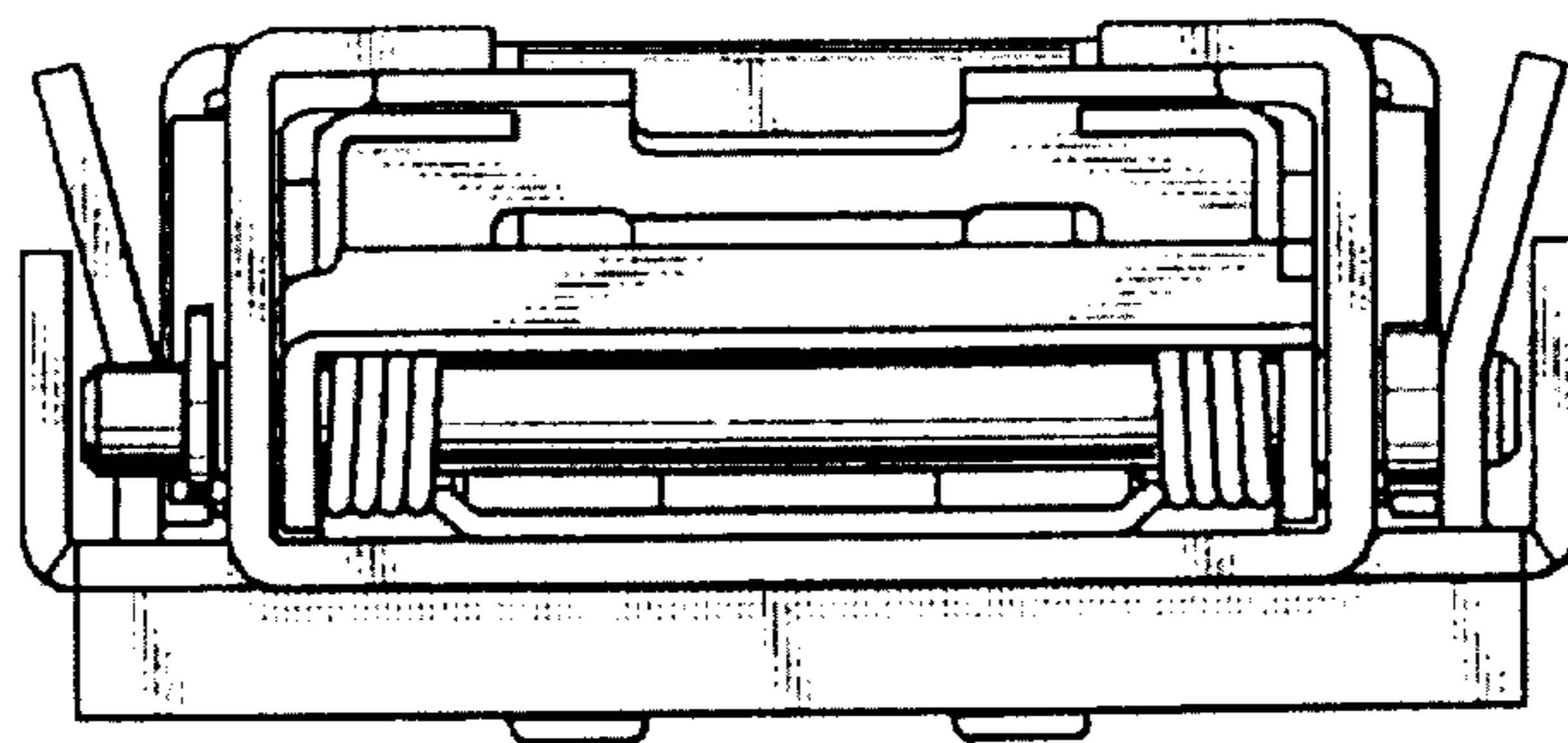


FIG. 4

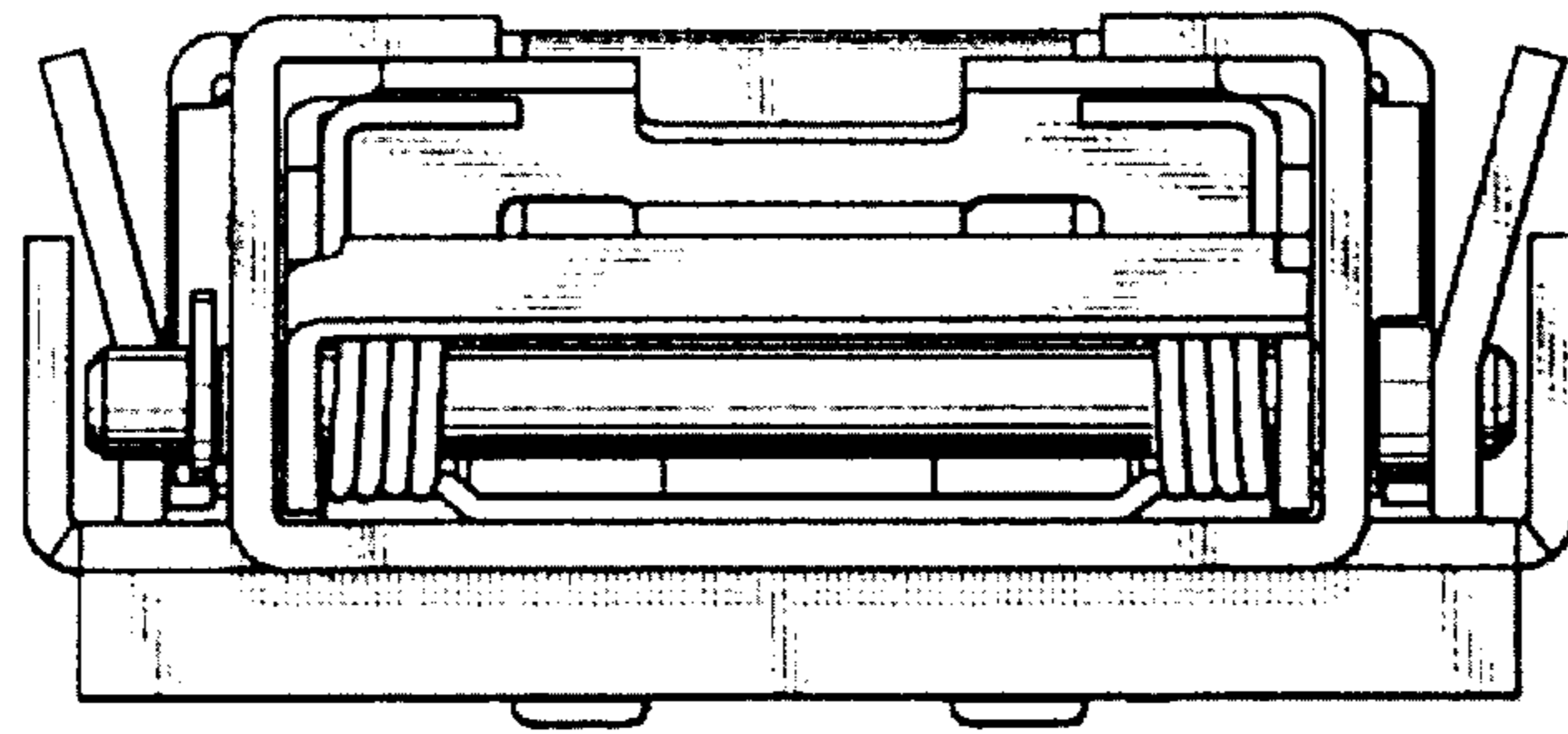


FIG. 5

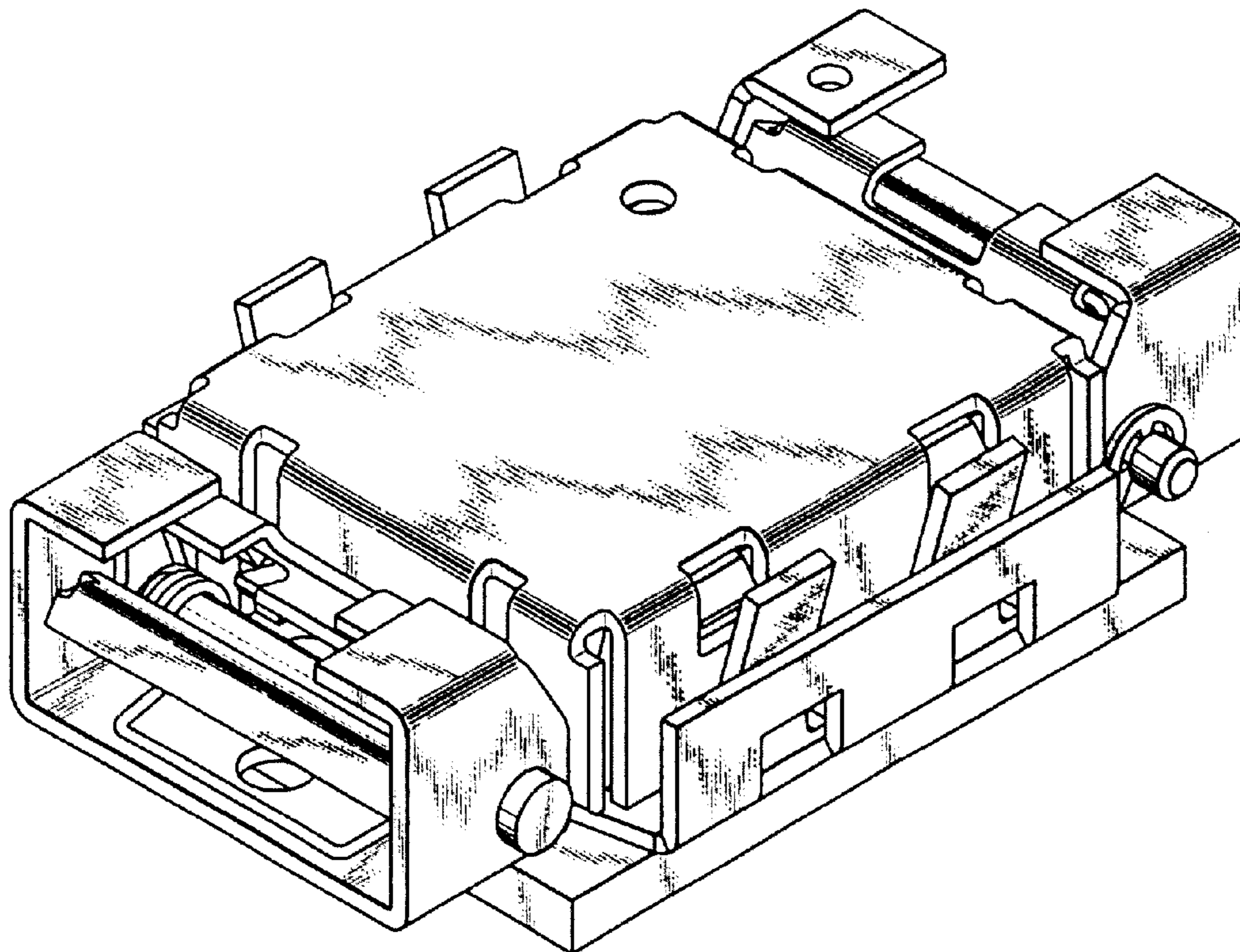


FIG. 6