

US00D505120S

(12) **United States Design Patent** (10) **Patent No.:** **US D505,120 S**
Suenaga (45) **Date of Patent:** **** May 17, 2005**

- (54) **LIGHT EMITTING DIODE**
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 (73) Assignee: **Nichia Corporation, Anan (JP)**
 (**) Term: **14 Years**
 (21) Appl. No.: **29/215,719**
 (22) Filed: **Oct. 25, 2004**

- D439,351 S 3/2001 Kiba et al.
 6,608,334 B1 8/2003 Ishinaga
 D486,801 S 2/2004 Suenaga
 D488,137 S 4/2004 Kamada
 D491,899 S 6/2004 Yagi

FOREIGN PATENT DOCUMENTS

- JP D1046564 S 8/1999
 JP D1146343 S 7/2002
 JP D1146344 S 7/2002

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Related U.S. Application Data

- (62) Division of application No. 29/169,617, filed on Oct. 24, 2002, now Pat. No. Des. 486,801.

(30) Foreign Application Priority Data

- Sep. 26, 2002 (JP) 2002-026314
 Sep. 26, 2002 (JP) 2002-026315

- (51) **LOC (7) Cl.** **13-03**
 (52) **U.S. Cl.** **D13/180**
 (58) **Field of Search** D13/180, 182;
 D10/104, 114; 257/76, 79, 88, 89, 93-99,
 434, 676, 678, 687, 690, 698, 787; 313/498,
 499, 500; 372/45; 361/760, 820, 226, 241,
 249, 250, 307, 555, 800

(56) References Cited

U.S. PATENT DOCUMENTS

- 3,855,606 A 12/1974 Schoberl
 3,914,786 A 10/1975 Grossi
 4,152,624 A 5/1979 Knaebel
 D289,037 S 3/1987 Nishizawa et al.
 5,266,817 A 11/1993 Lin
 RE36,614 E 3/2000 Lumbard et al.
 D432,095 S 10/2000 Seeger et al.

(57) CLAIM

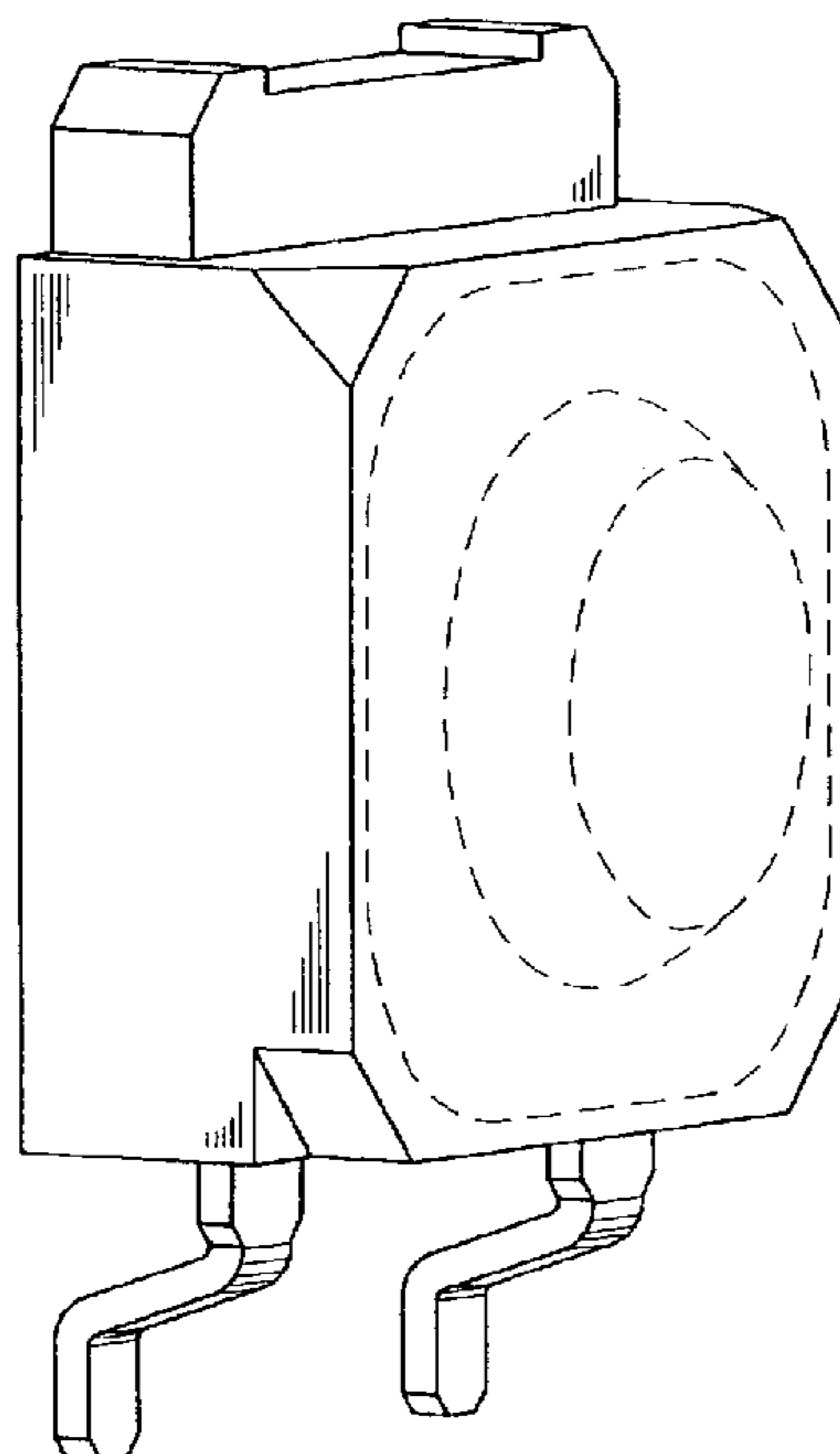
I claim the ornamental design for a light emitting diode, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a light emitting diode showing my new design;
 FIG. 2 is a top plan view of the light emitting diode of FIG. 1;
 FIG. 3 is a left side elevation view of the light emitting diode of FIG. 1;
 FIG. 4 is a front elevation view of the light emitting diode of FIG. 1;
 FIG. 5 is a right side elevation view of the light emitting diode of FIG. 1;
 FIG. 6 is a rear elevation view of the light emitting diode of FIG. 1; and,
 FIG. 7 is a bottom plan view of the light emitting diode of FIG. 1.

The broken line showing of the environment is for illustrative purpose only and forms no part of the claimed design.

1 Claim, 2 Drawing Sheets



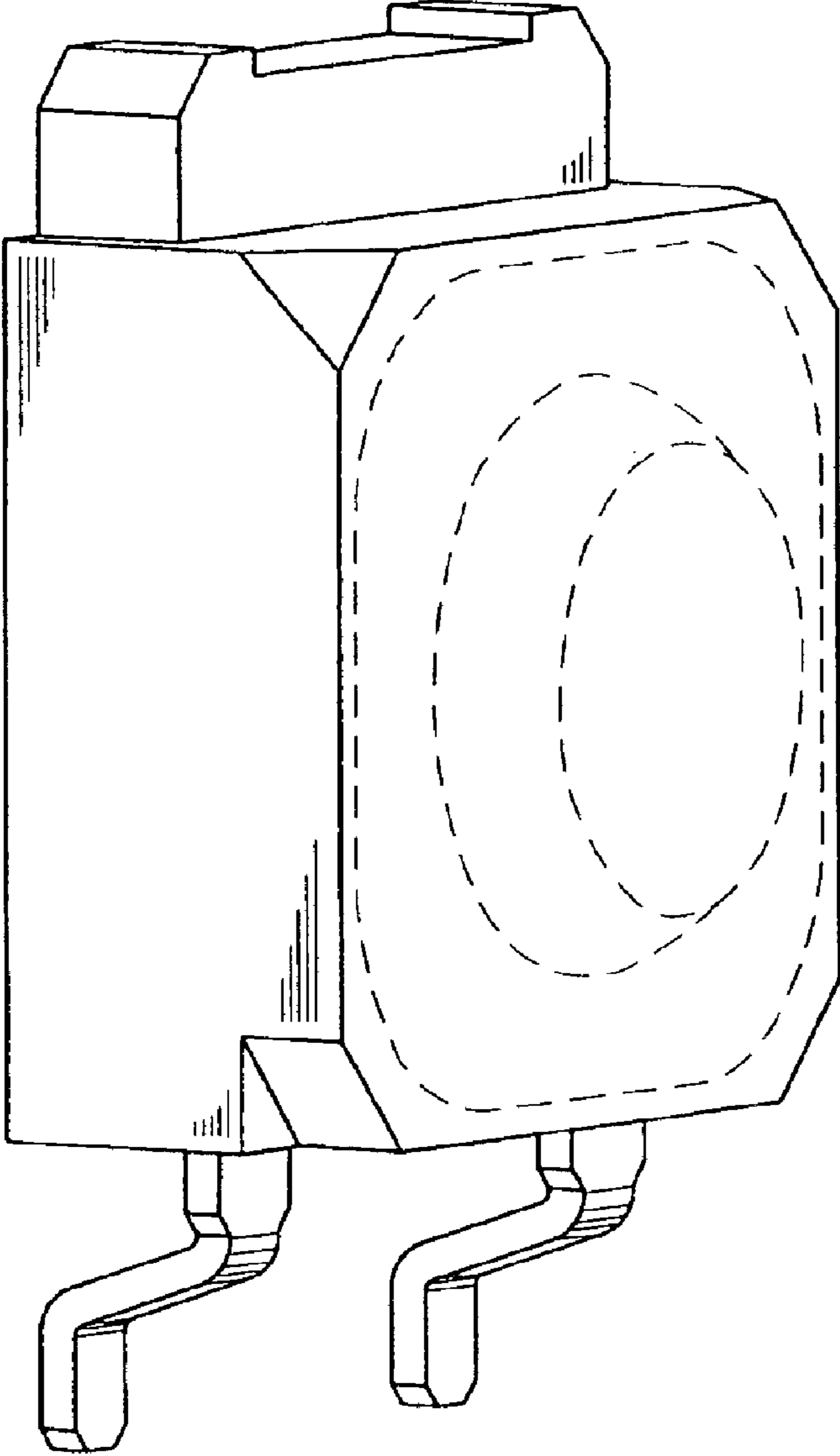


FIG. 1

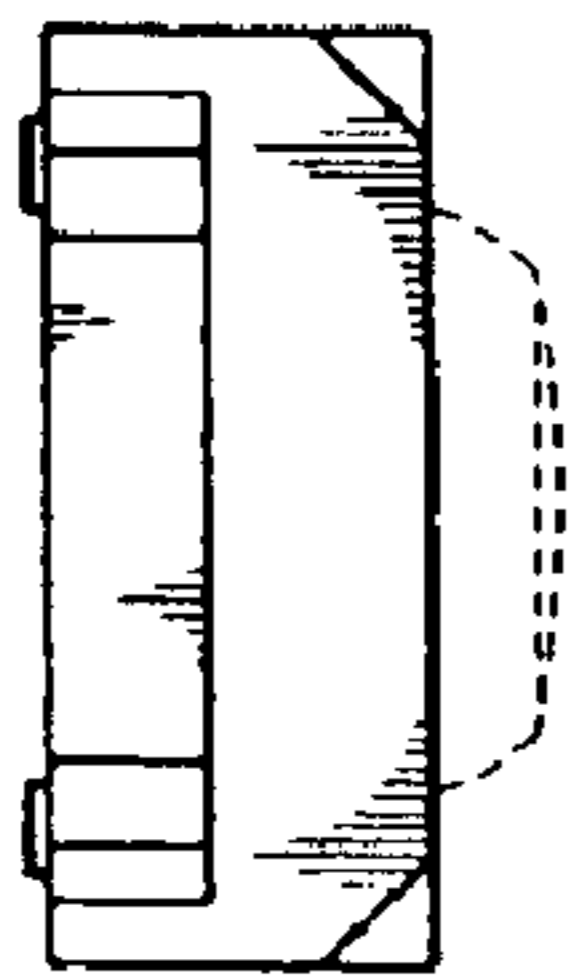


FIG. 2



FIG. 3

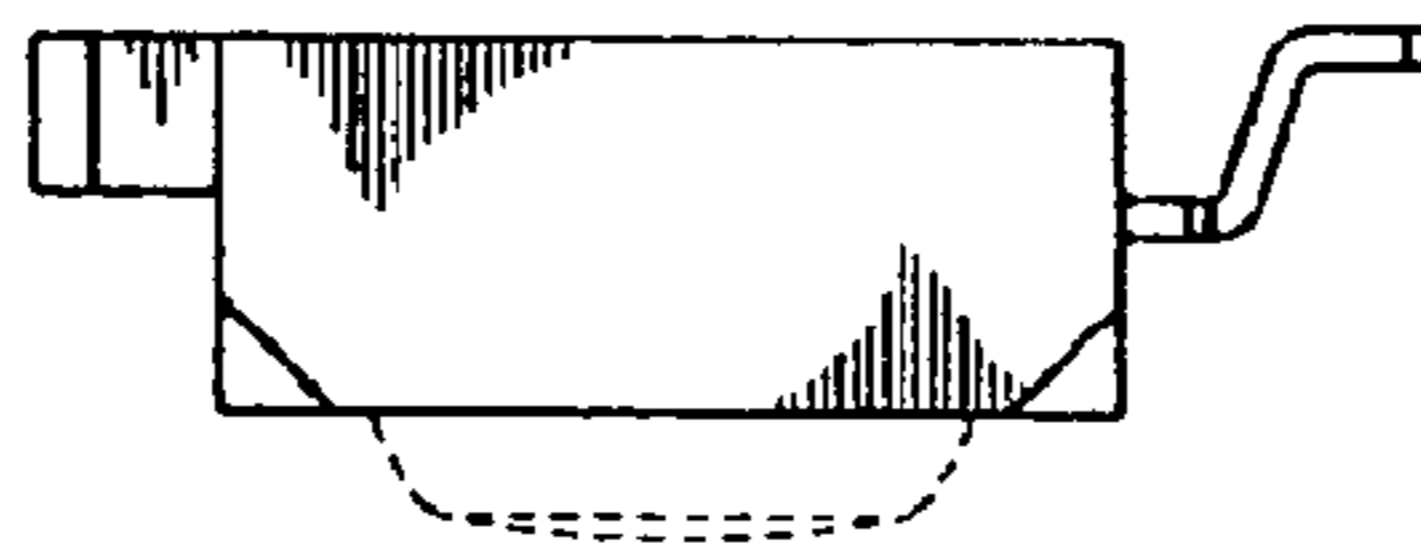


FIG. 4

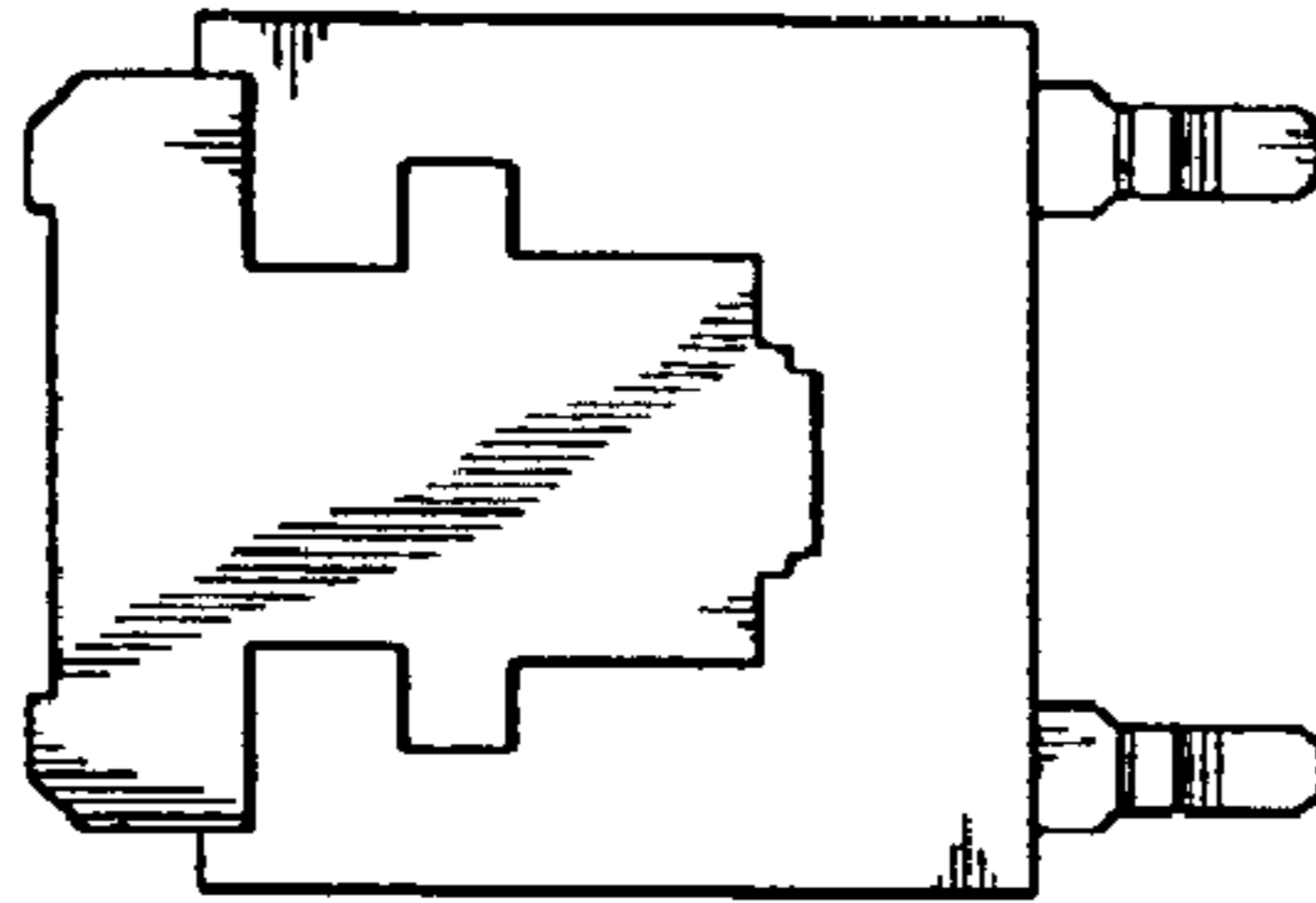


FIG. 5

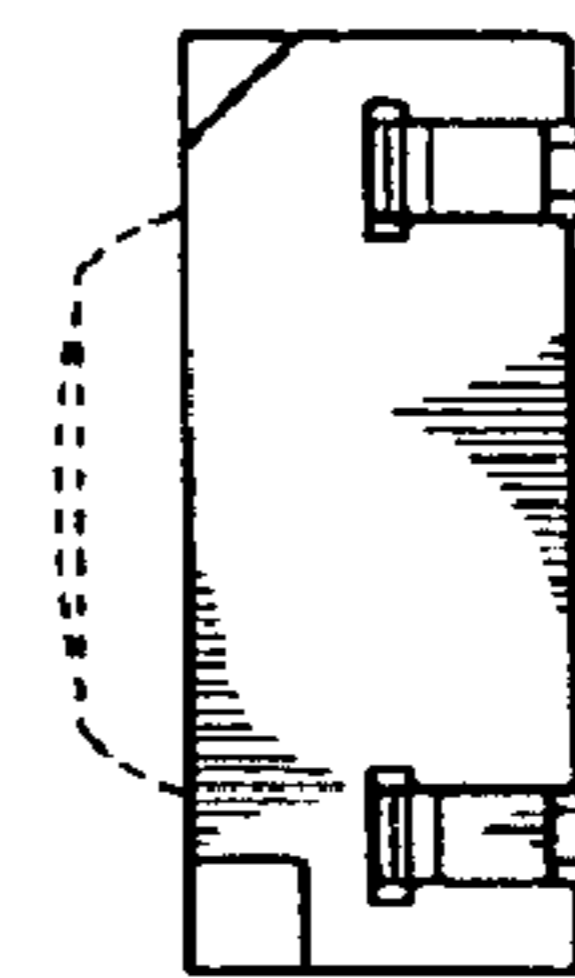


FIG. 6



FIG. 7