

US00D503691S

(12) **United States Design Patent** (10) **Patent No.:** **US D503,691 S**
Standing et al. (45) **Date of Patent:** **** Apr. 5, 2005**

(54) **CONDUCTIVE CLIP FOR A SEMICONDUCTOR PACKAGE**

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(**) Term: **14 Years**

(21) Appl. No.: **29/188,754**

(22) Filed: **Aug. 22, 2003**

Related U.S. Application Data

(62) Division of application No. 09/819,774, filed on Mar. 28, 2001, now Pat. No. 6,624,522.

(51) **LOC (7) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Search** D13/182; 174/52.1, 174/52.2; 257/666, 670, 678, 684, 690, 719, 776, 782, 796; 361/760, 771, 774, 792, 798, 820

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(57) **CLAIM**

The ornamental design for a conductive clip for a semiconductor package, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a conductive clip according to our design;

FIG. 2 is another perspective view of a conductive clip according to our design, the broken lines being included for illustrative purposes and forming no part of the claimed design;

FIG. 3 is a top plan view of a conductive clip according to our design;

FIG. 4 is a bottom plan view of a conductive clip according to our design, the broken lines being included for illustration purposes and forming no part of the claimed design;

FIG. 5 is a side elevational view of a conductive clip according to our design; and,

FIG. 6 is a side elevational view of a conductive clip according to our design.

1 Claim, 2 Drawing Sheets

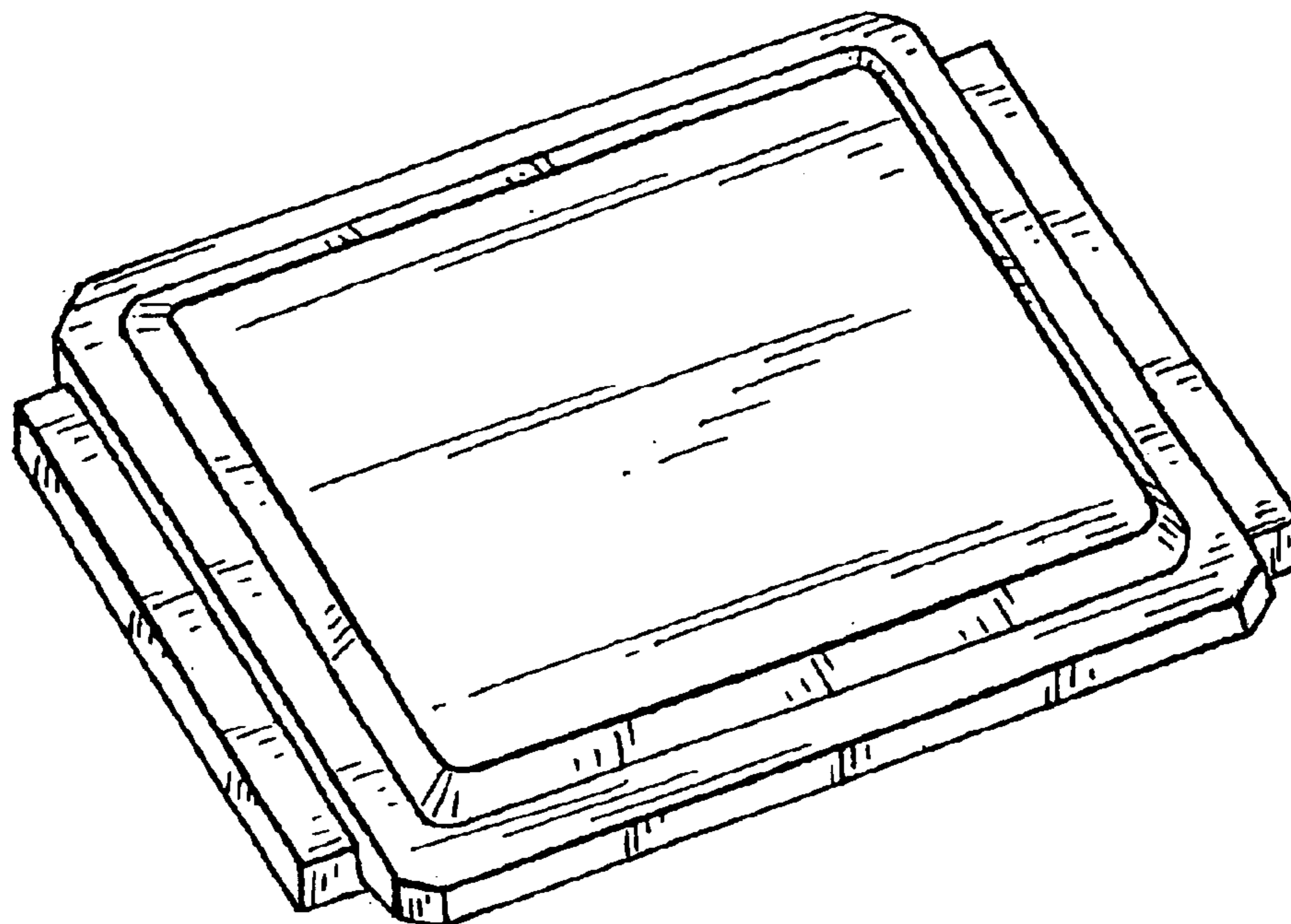


FIG. 1

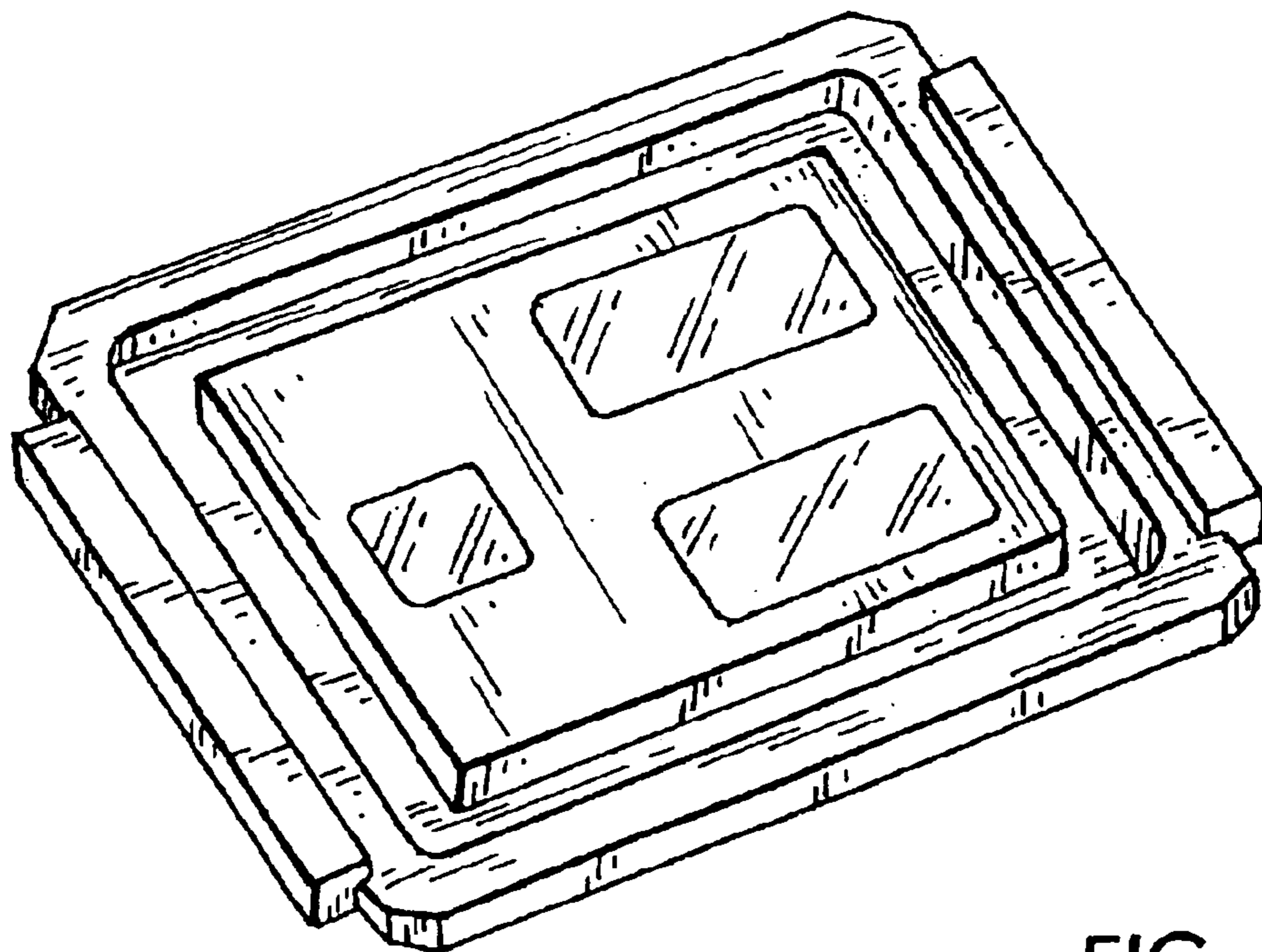
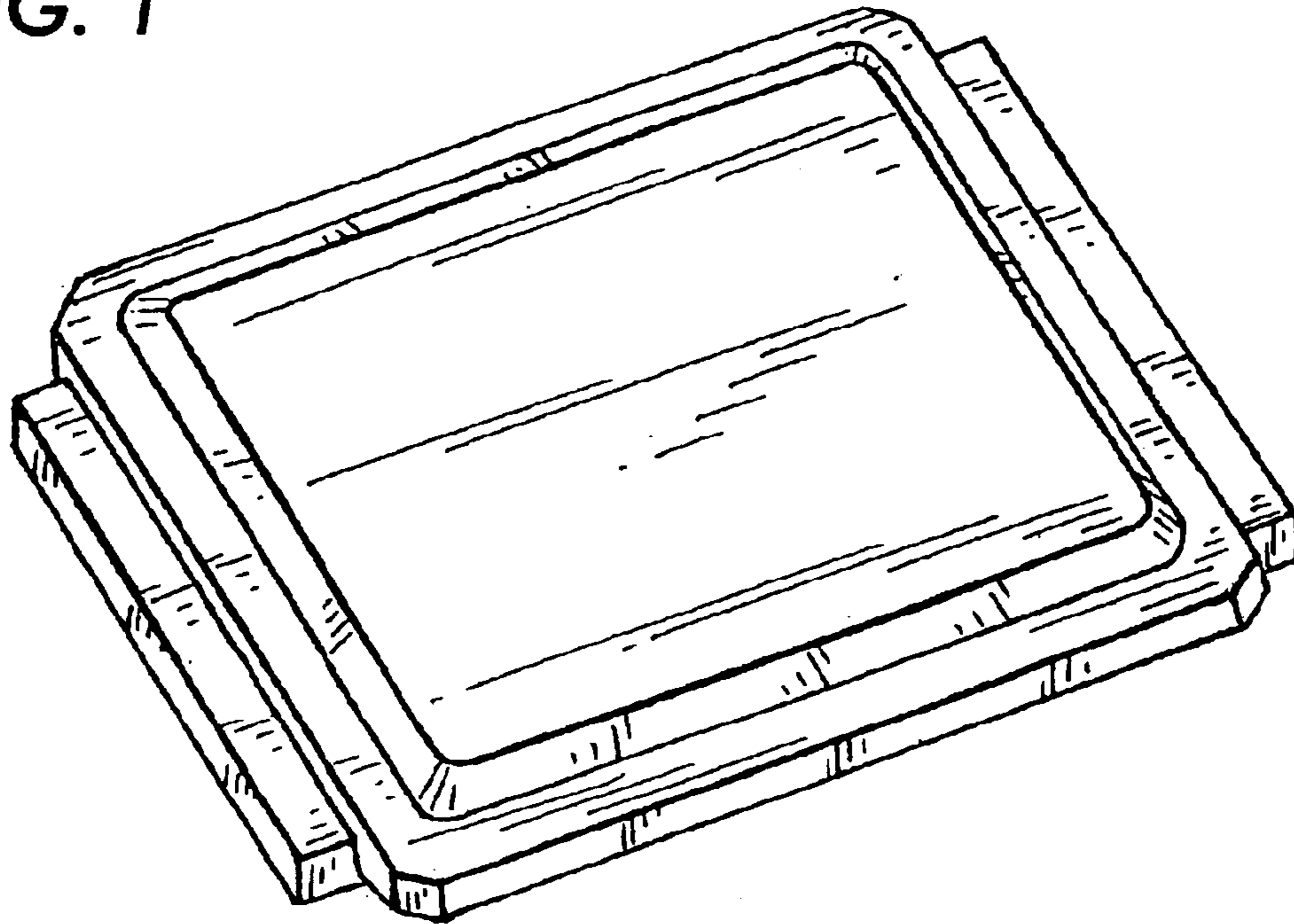


FIG. 2

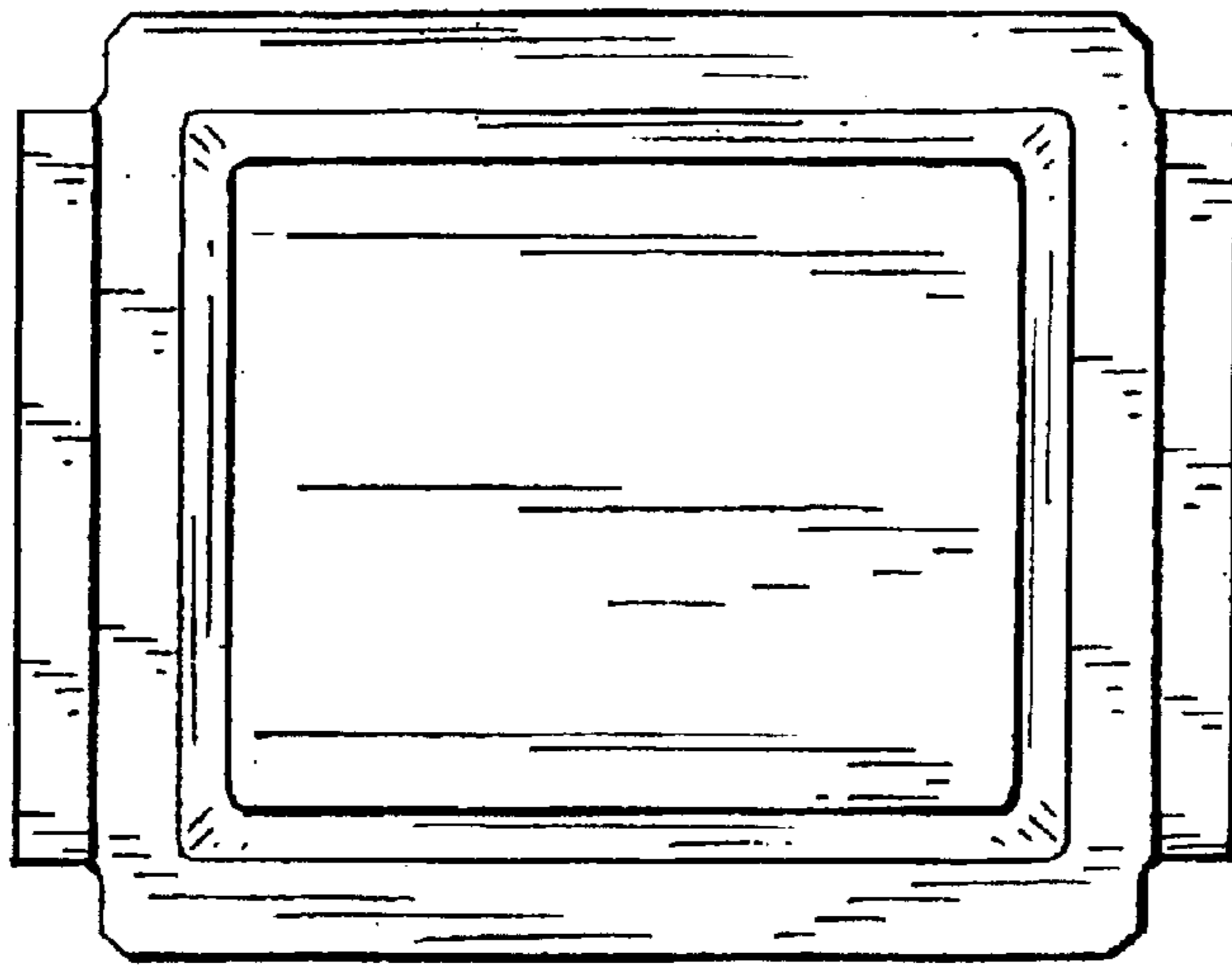


FIG. 3

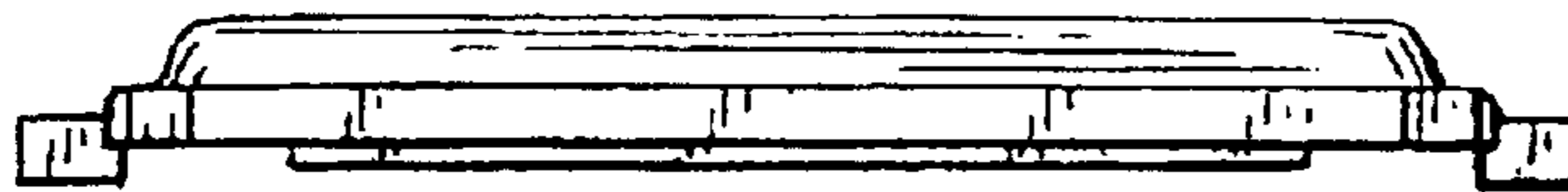


FIG. 5

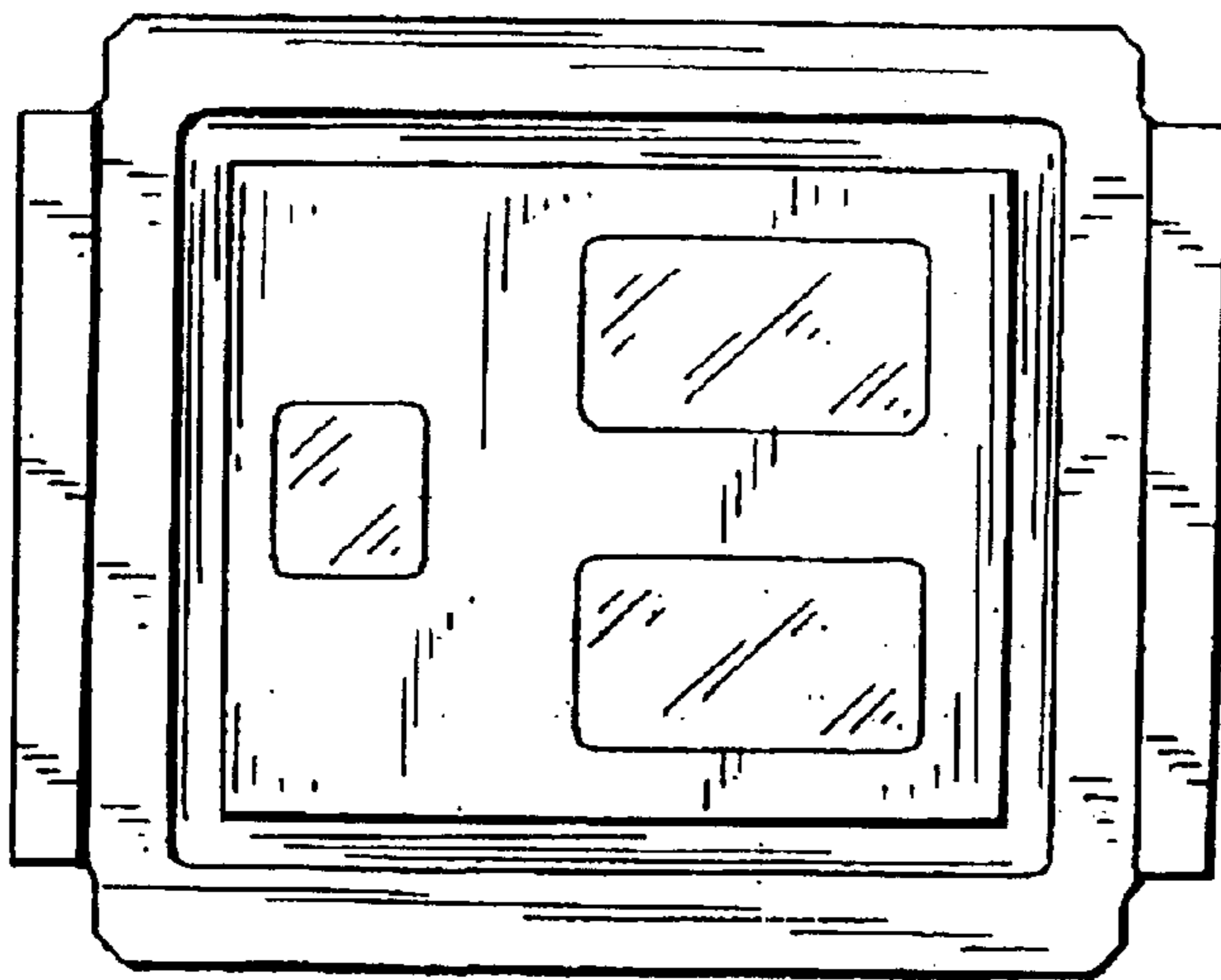


FIG. 4



FIG. 6