

US00D503389S

(12) **United States Design Patent** (10) **Patent No.:** **US D503,389 S**
Dabrowski et al. (45) **Date of Patent:** **** Mar. 29, 2005**

(54) **ELECTRICAL DEVICE HOUSING**
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(**) Term: **14 Years**

Primary Examiner—Stella Reid
Assistant Examiner—Selina Sikder

(21) Appl. No.: **29/208,127**

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(22) Filed: **Jun. 24, 2004**

(57) **CLAIM**

The ornamental design for an electrical device housing, as shown and described.

Related U.S. Application Data

(62) Division of application No. 29/169,263, filed on Oct. 16, 2002.

DESCRIPTION

(51) **LOC (7) Cl.** **13-03**
 (52) **U.S. Cl.** **D13/184**
 (58) **Field of Search** D13/184; 220/3.3,
 220/3.8, 3.92; 174/17 R, 50, 50.52, 52.1,
 58; 312/109, 263, 292, 293.1–293.3, 294;
 361/600, 601, 622, 679, 686, 720, 724,
 728, 748, 752, 796; 379/419, 428.01, 440,
 441, 442; 429/27, 34

FIG. 1 is a front, top, and right side view of an electrical device housing showing our new design.
 FIG. 2 shows a front view of FIG. 1.
 FIG. 3 shows a left side view of FIG. 1.
 FIG. 4 shows a rear view of FIG. 1.
 FIG. 5 shows a right side view of FIG. 1.
 FIG. 6 shows a top plan view of FIG. 1; and,
 FIG. 7 shows a bottom plan view of FIG. 1.

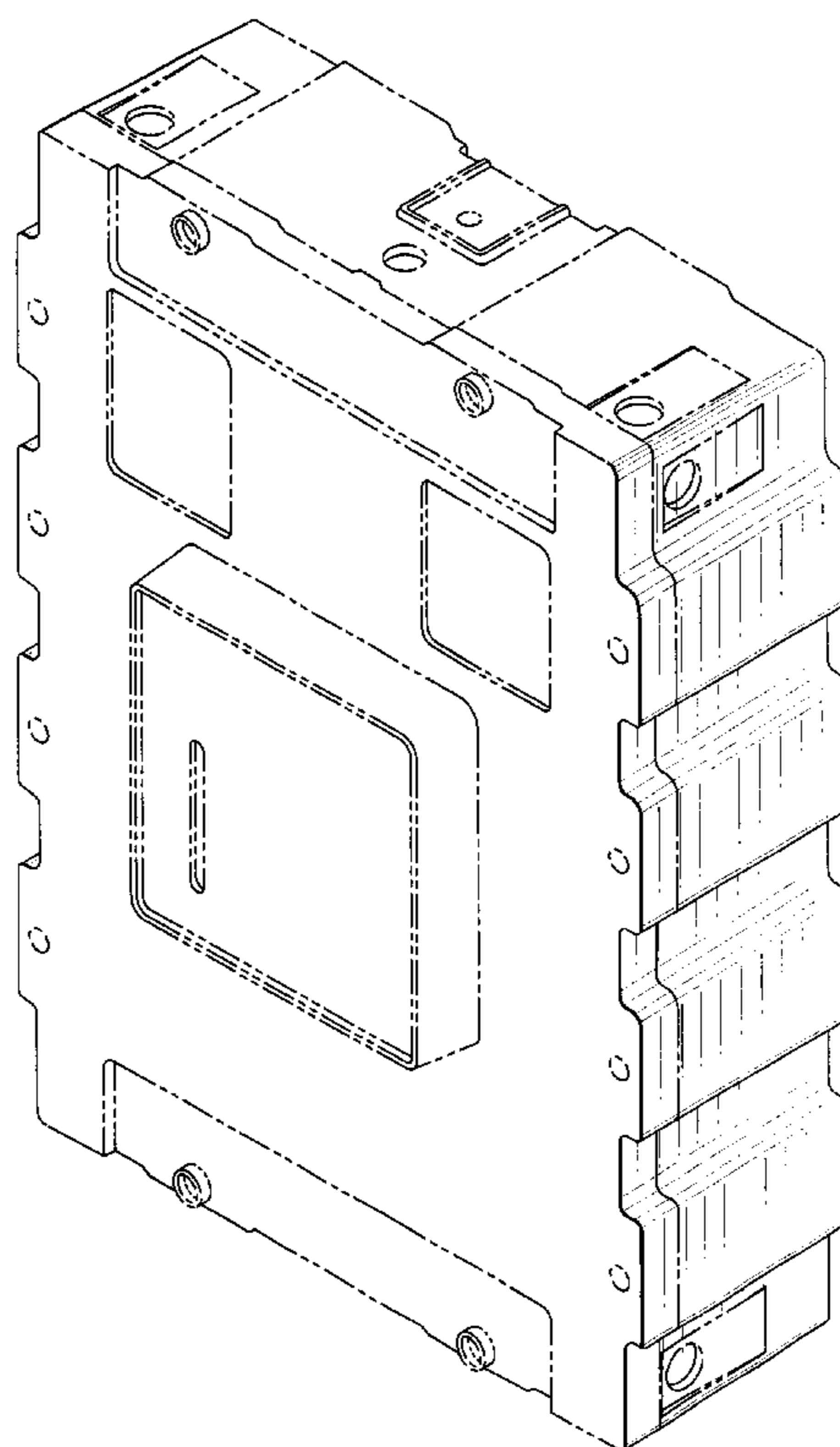
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The broken line showing of the environment is for illustrative purpose only and forms no part of the claimed design.

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1 Claim, 4 Drawing Sheets



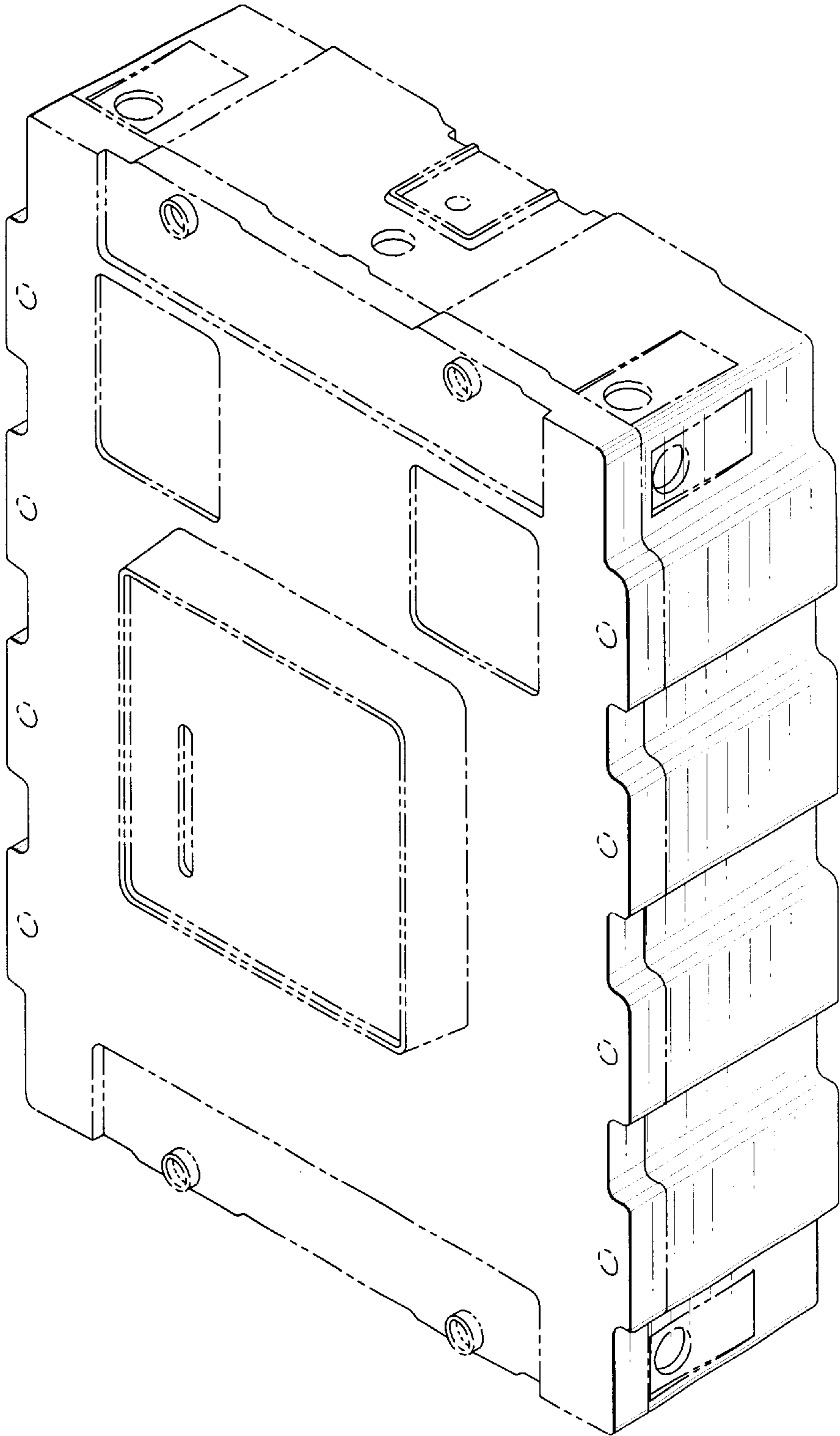


FIG. 1

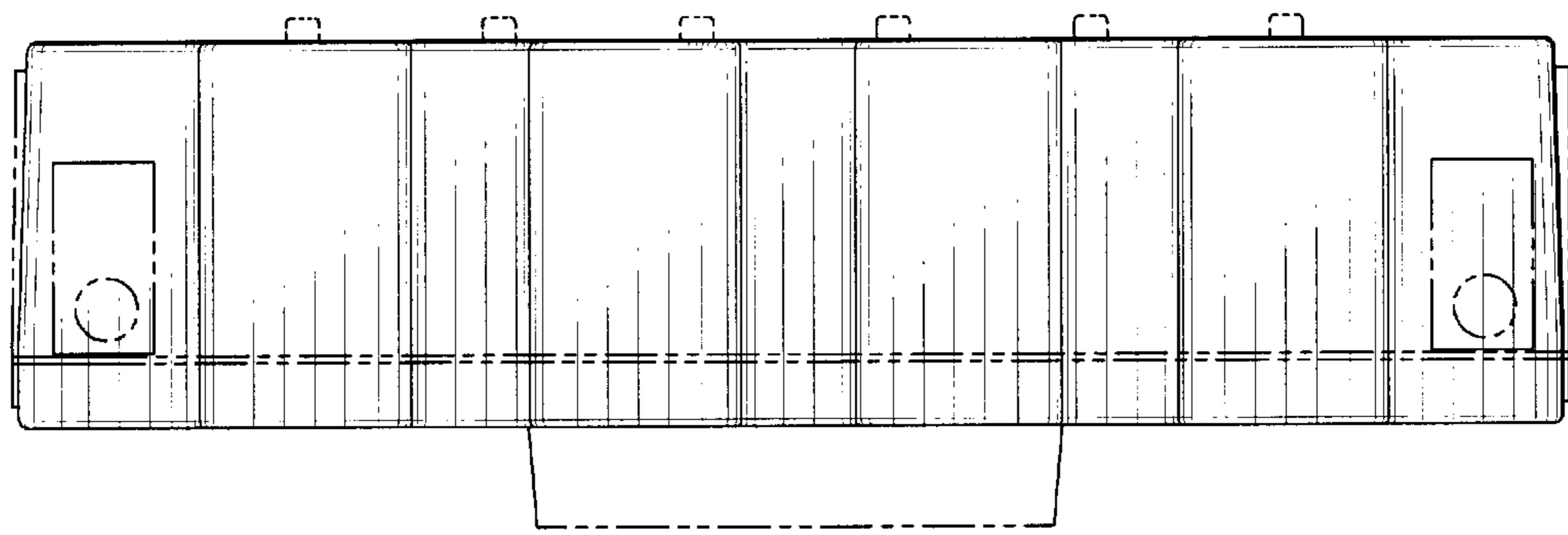


FIG. 3

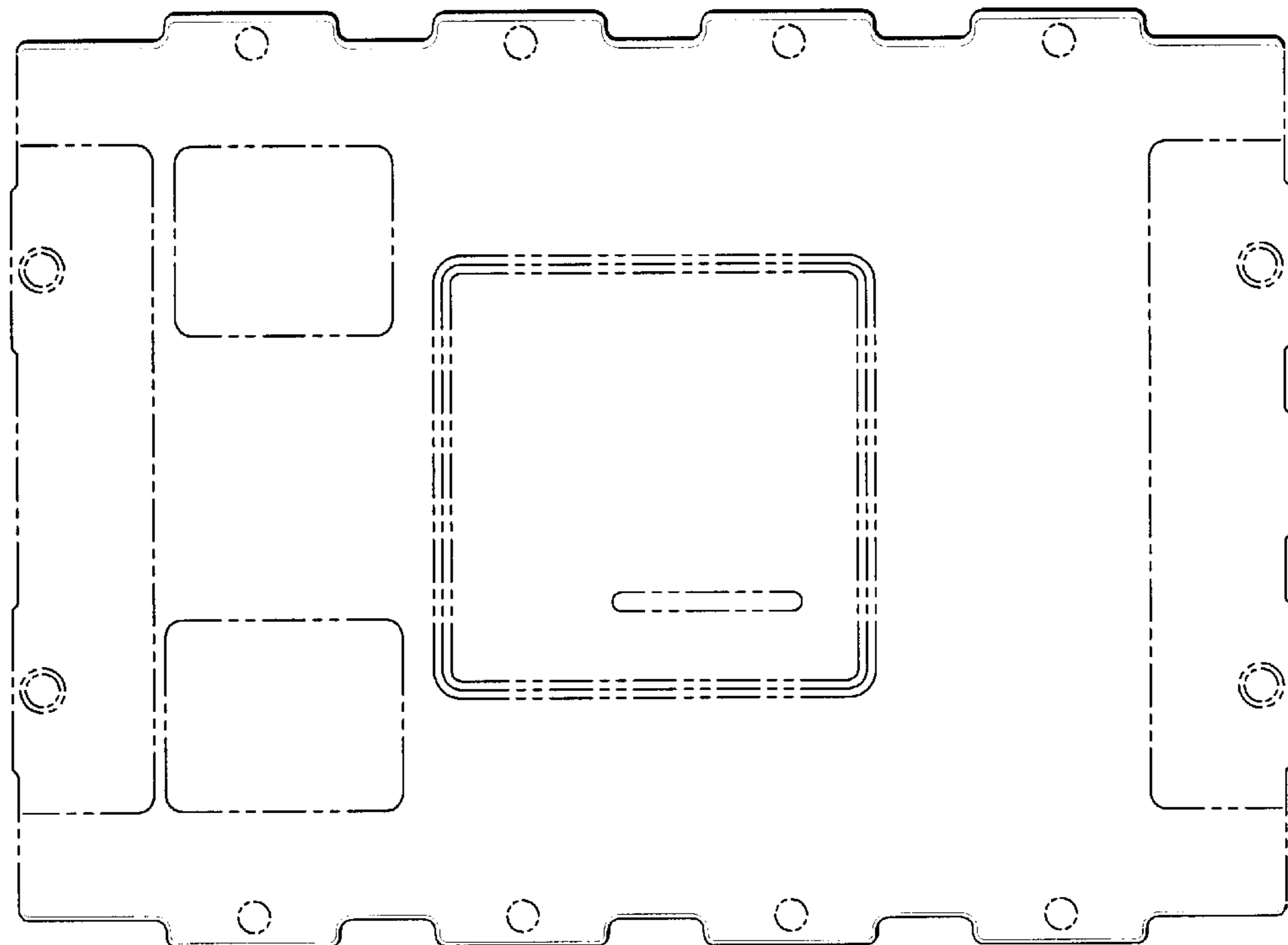


FIG. 2

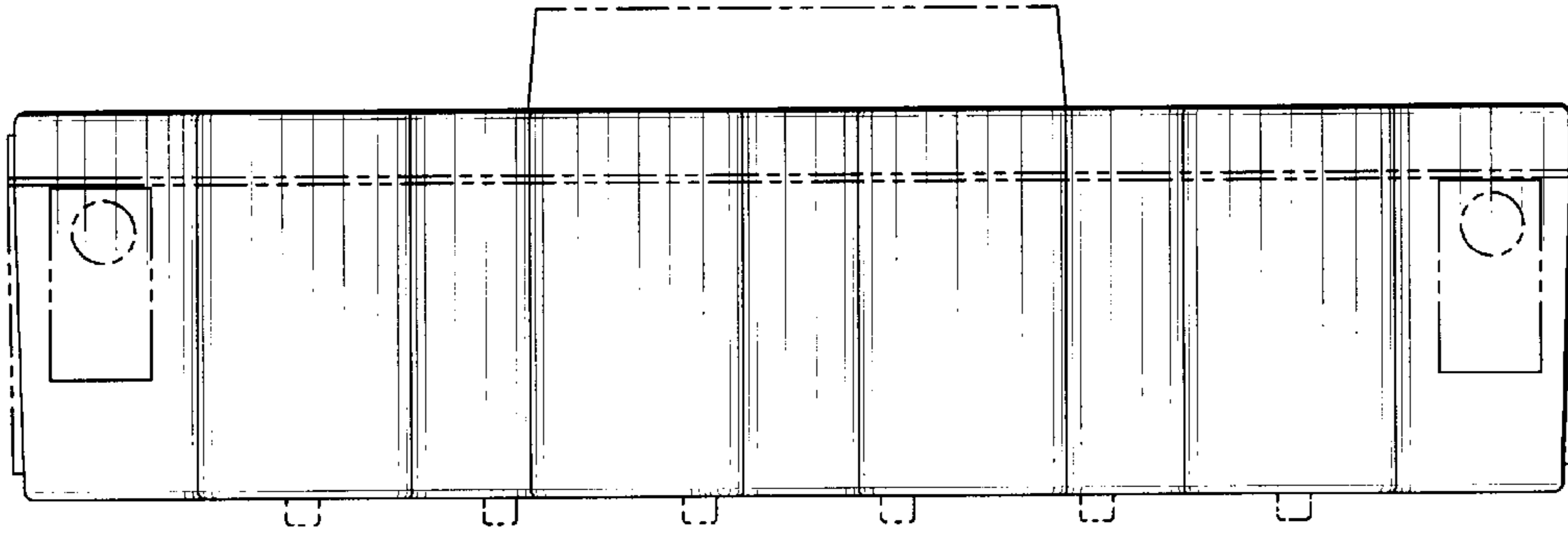


FIG. 5

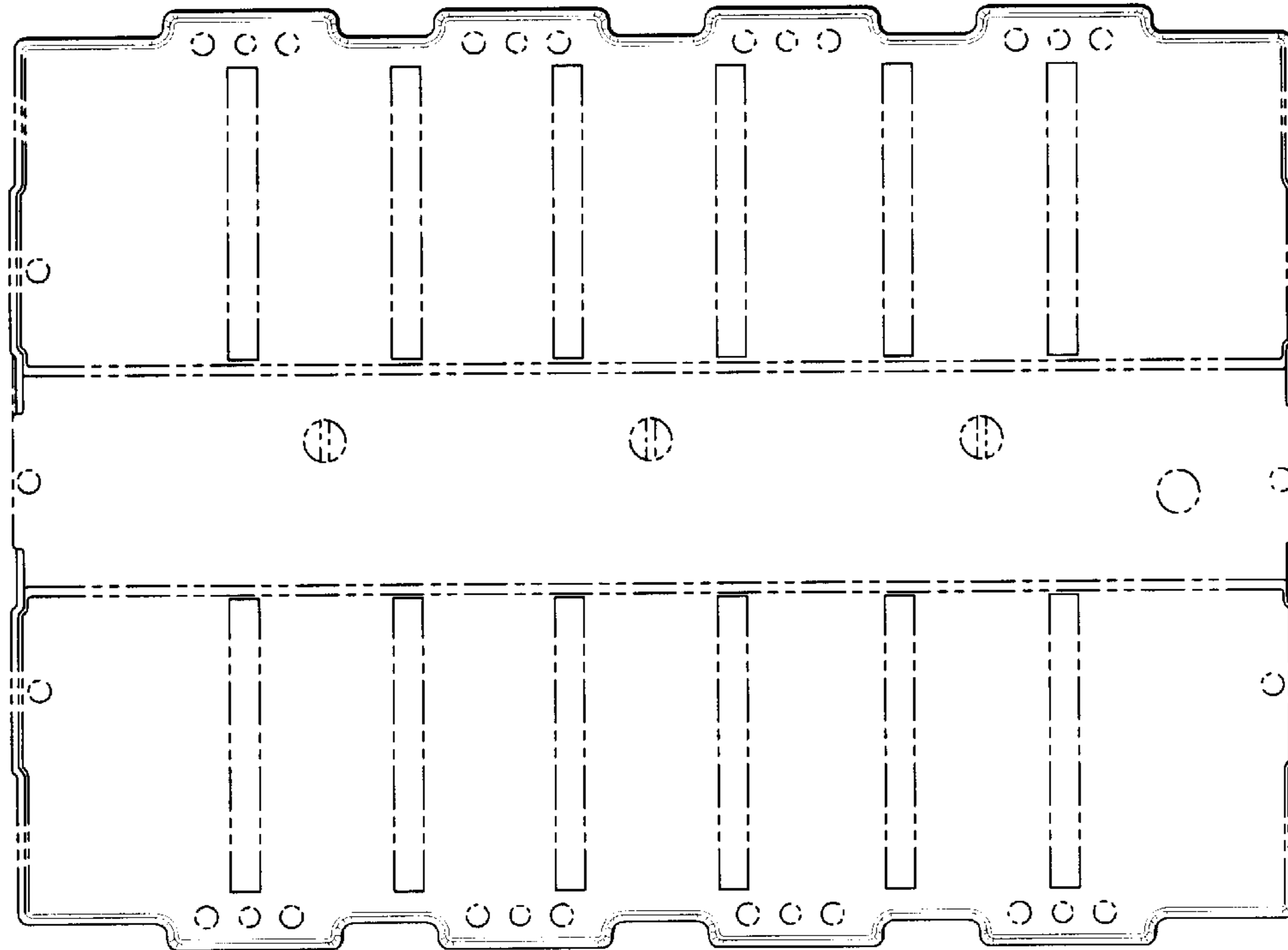


FIG. 4

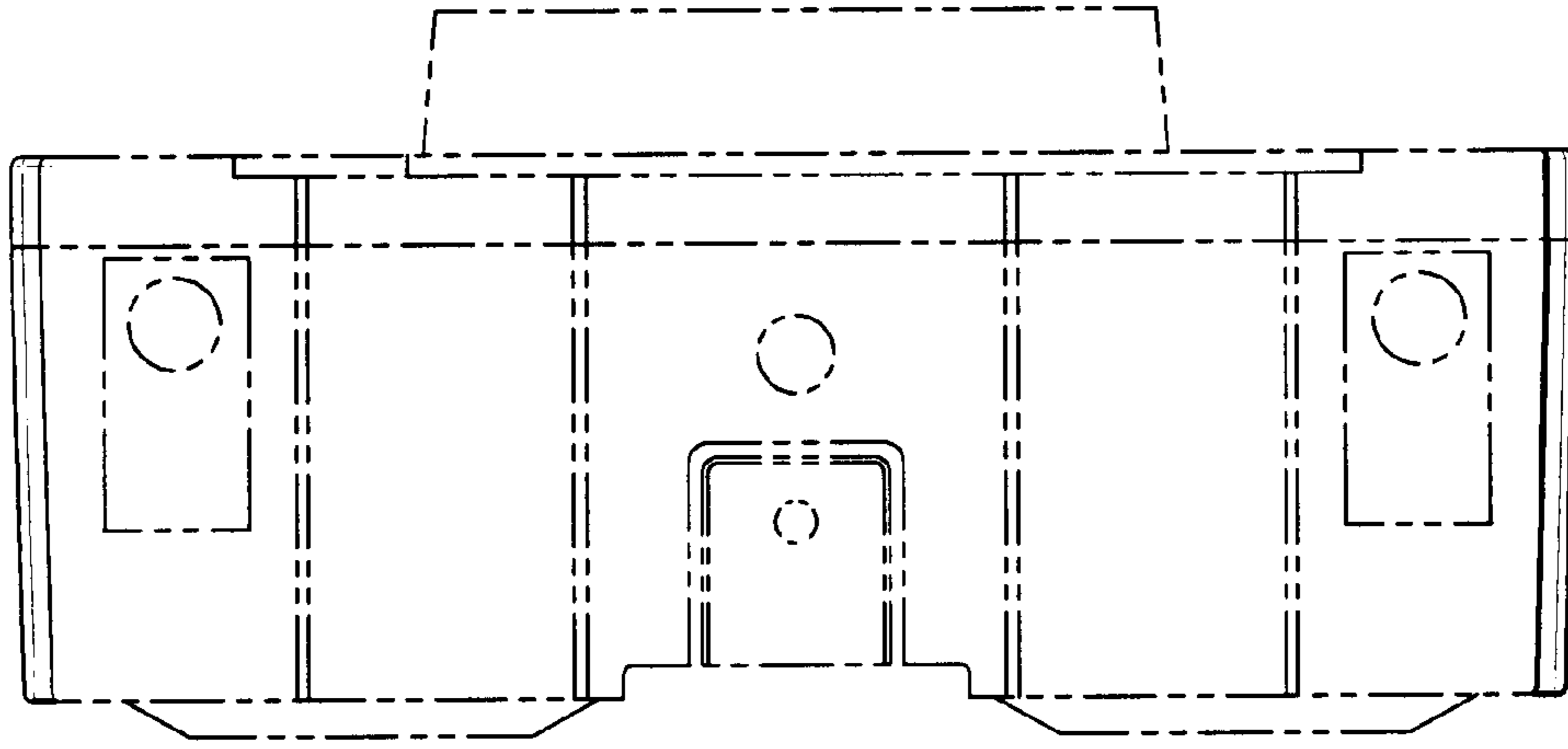


FIG. 6

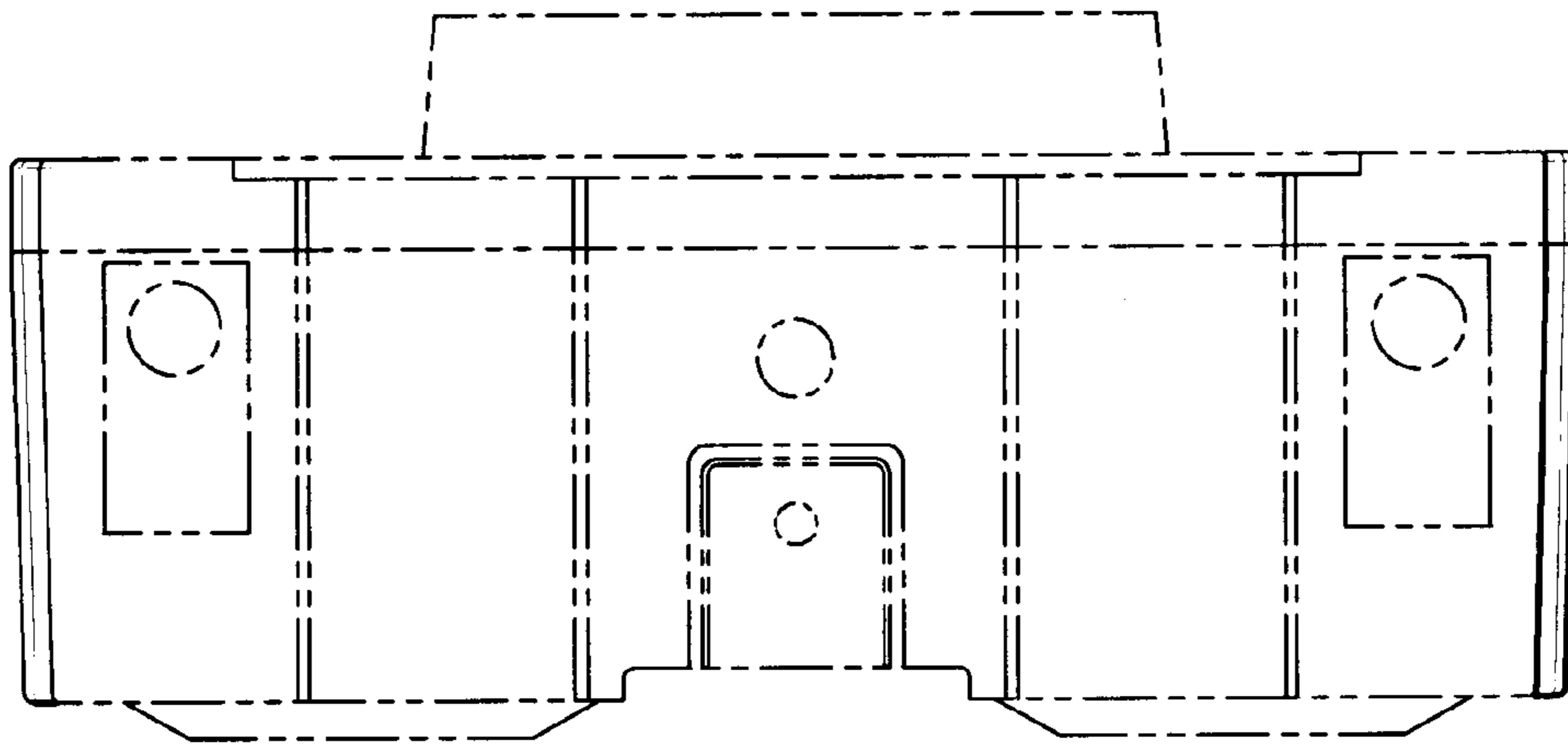


FIG. 7