US00D503383S

(12) **United States Design Patent** (10) **Patent No.: US D503,383 S**
Moriwake et al. (45) **Date of Patent: ** Mar. 29, 2005**

(54) **ELECTRICAL CONNECTOR**

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(73) Assignee: **J.S.T. Mfg. Co., Ltd.**, Osaka (JP)

(**) Term: **14 Years**

(21) Appl. No.: **29/161,086**

(22) Filed: **May 21, 2002**

(30) **Foreign Application Priority Data**

Nov. 26, 2001 (JP) 2001-034545
Nov. 26, 2001 (JP) 2001-034546
Nov. 26, 2001 (JP) 2001-034547
Nov. 26, 2001 (JP) 2001-034548
Nov. 26, 2001 (JP) 2001-034549

(51) **LOC (7) Cl.** **13-03**
(52) **U.S. Cl.** **D13/147**
(58) **Field of Search** D13/133, 146, D13/147; 439/79, 108, 607, 609, 610, 540.1, 541.5

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(57) **CLAIM**

We claim the ornamental design for an electrical connector, as shown and described.

DESCRIPTION

FIG. **1** is a front elevation view of an electrical connector showing our new design;

FIG. **2** is a rear elevation view of the electrical connector;

FIG. **3** is a top plan view of the electrical connector;

FIG. **4** is a bottom plan view of the electrical connector;

FIG. **5** is a left side elevation view of the electrical connector;

FIG. **6** is a right side elevation view of the electrical connector;

FIG. **7** is a perspective view of the front, top and right side of the electrical connector; and,

FIG. **8** is a reduced top plan view of the electrical connector in a connecting condition. The printed circuit board and the counterpart electrical connector are drawn in broken lines for illustrative purposes only and form no part of the claimed design.

1 Claim, 4 Drawing Sheets

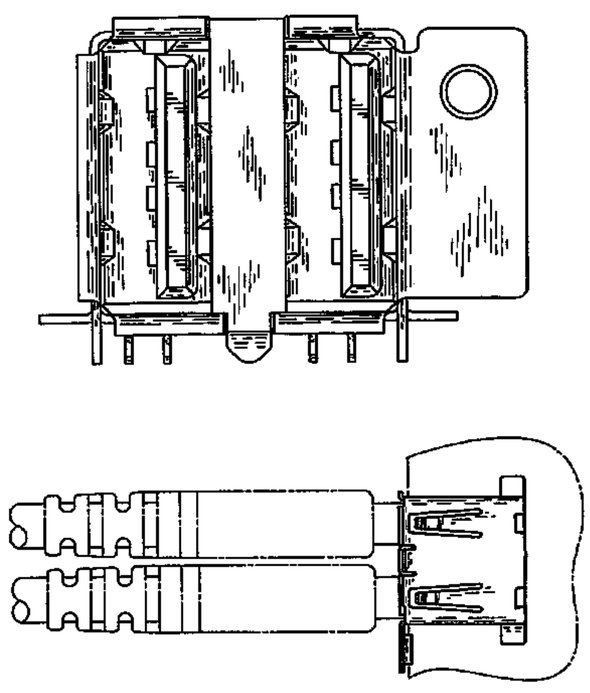

FIG. 1

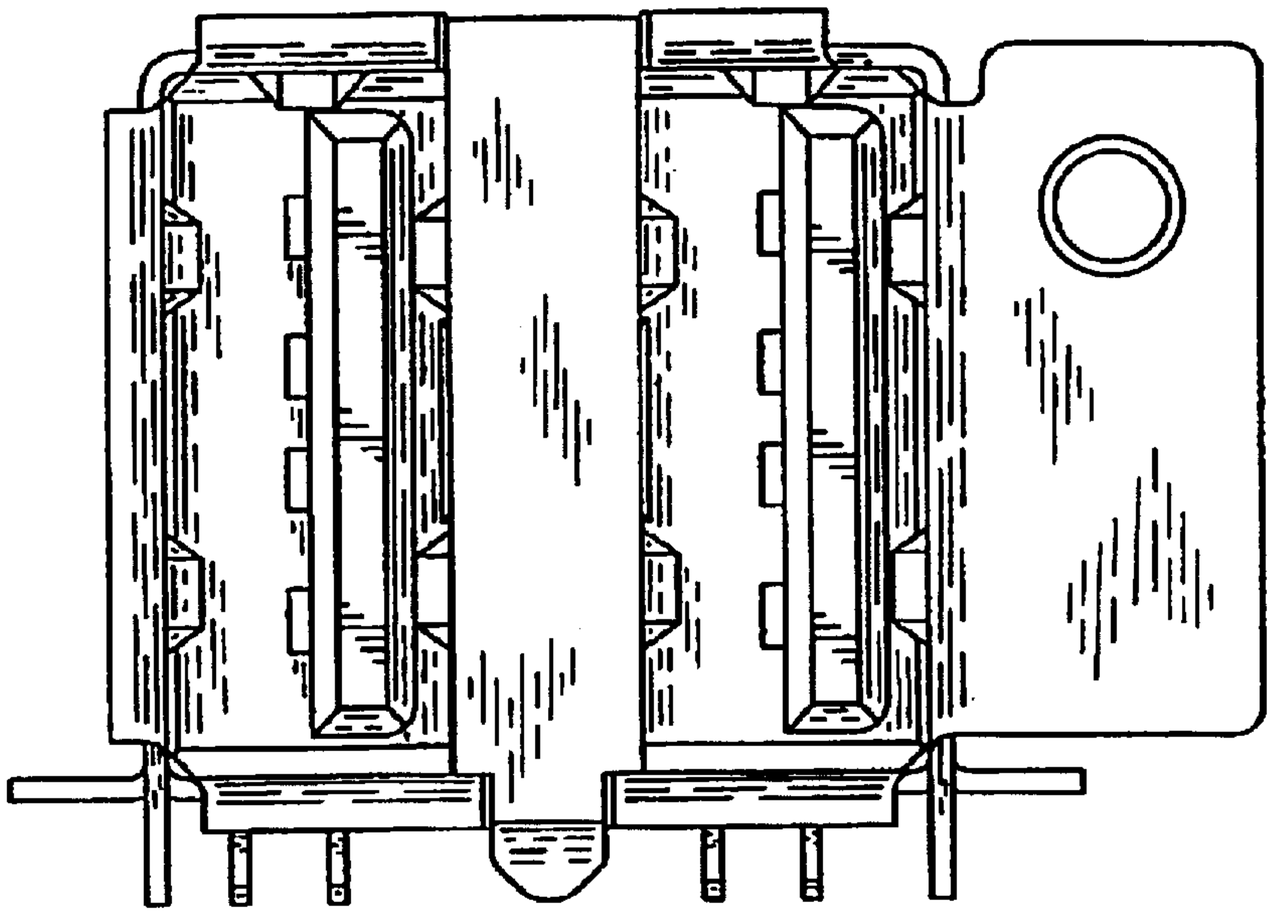

FIG. 2

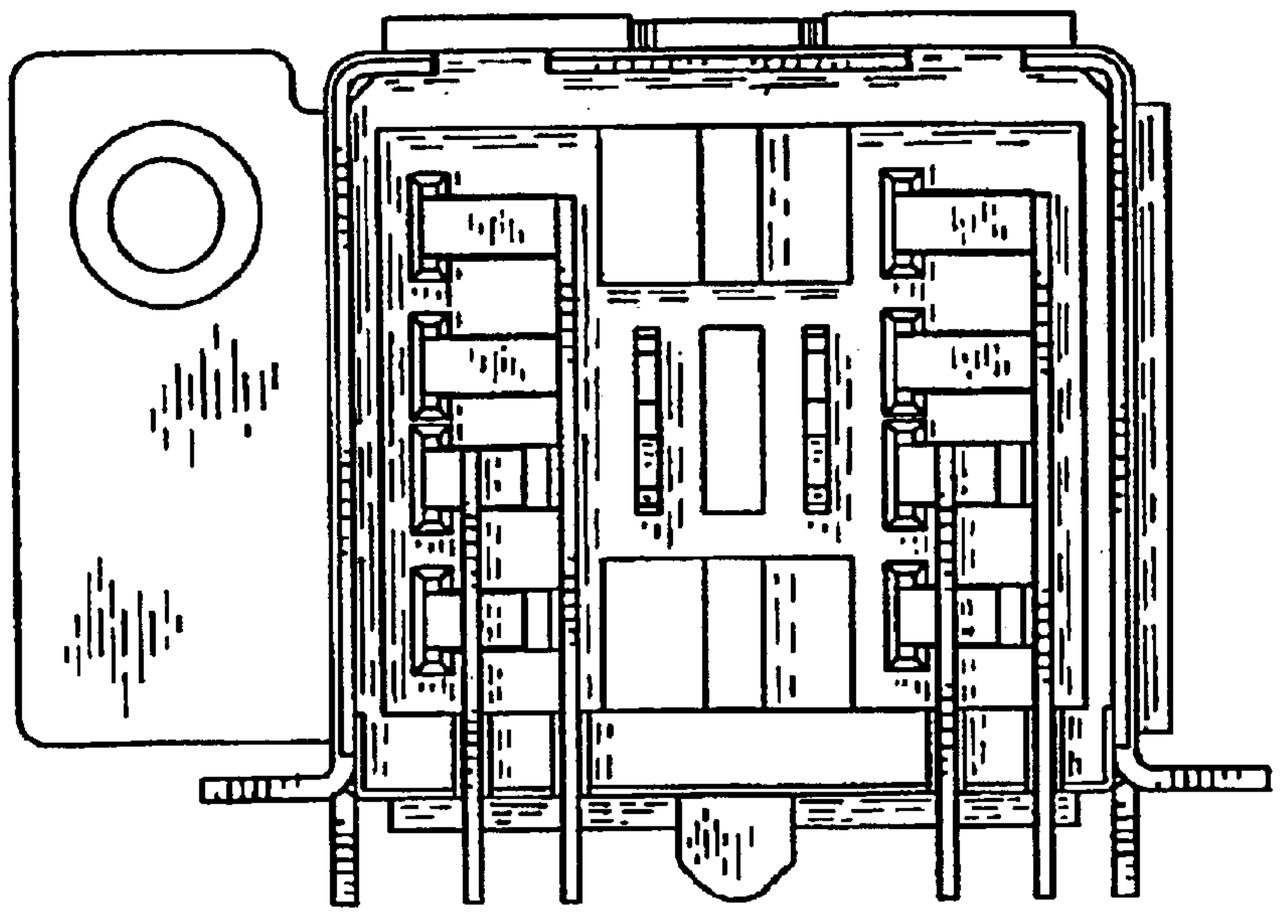

FIG. 3

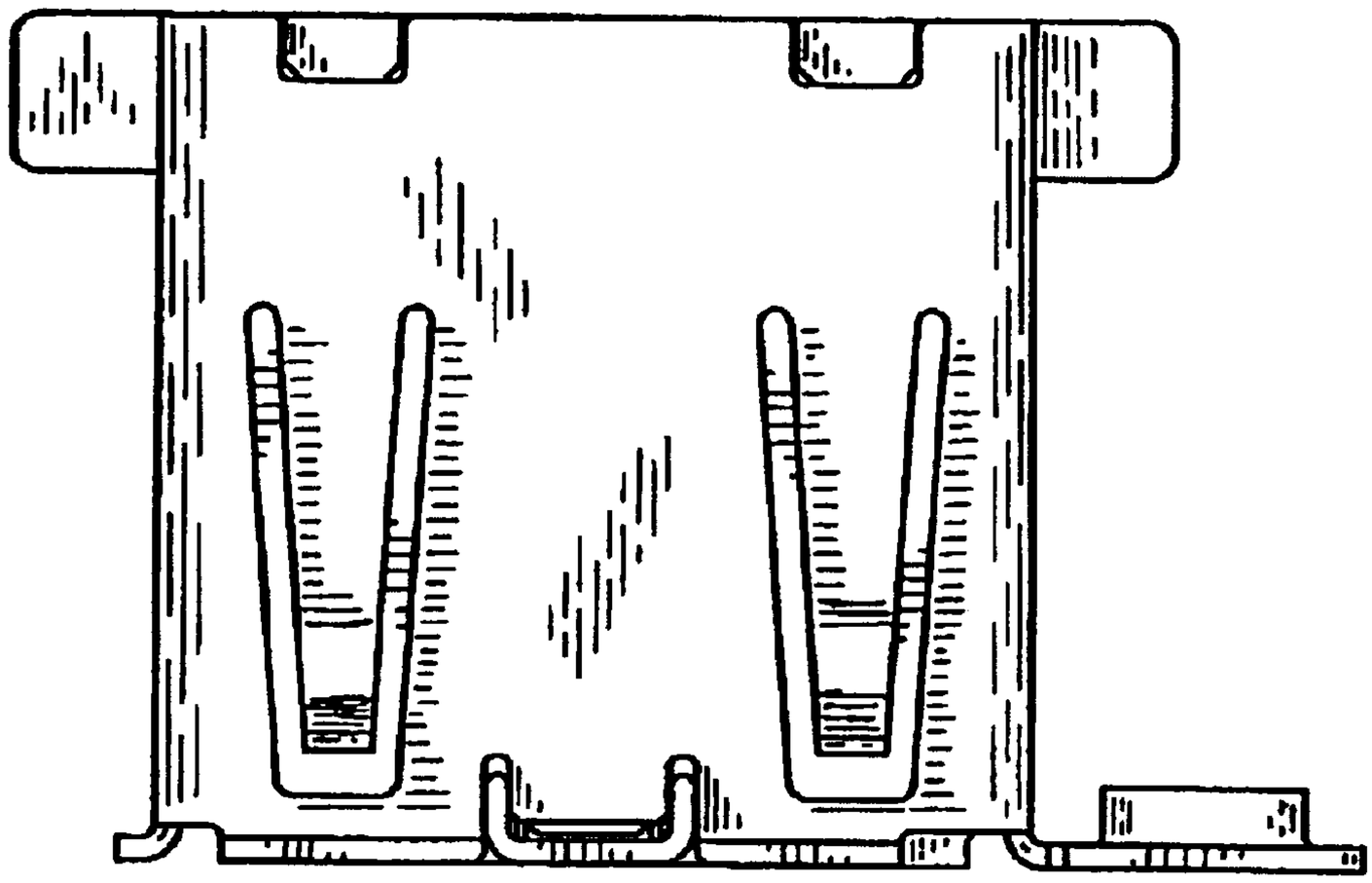

FIG. 4

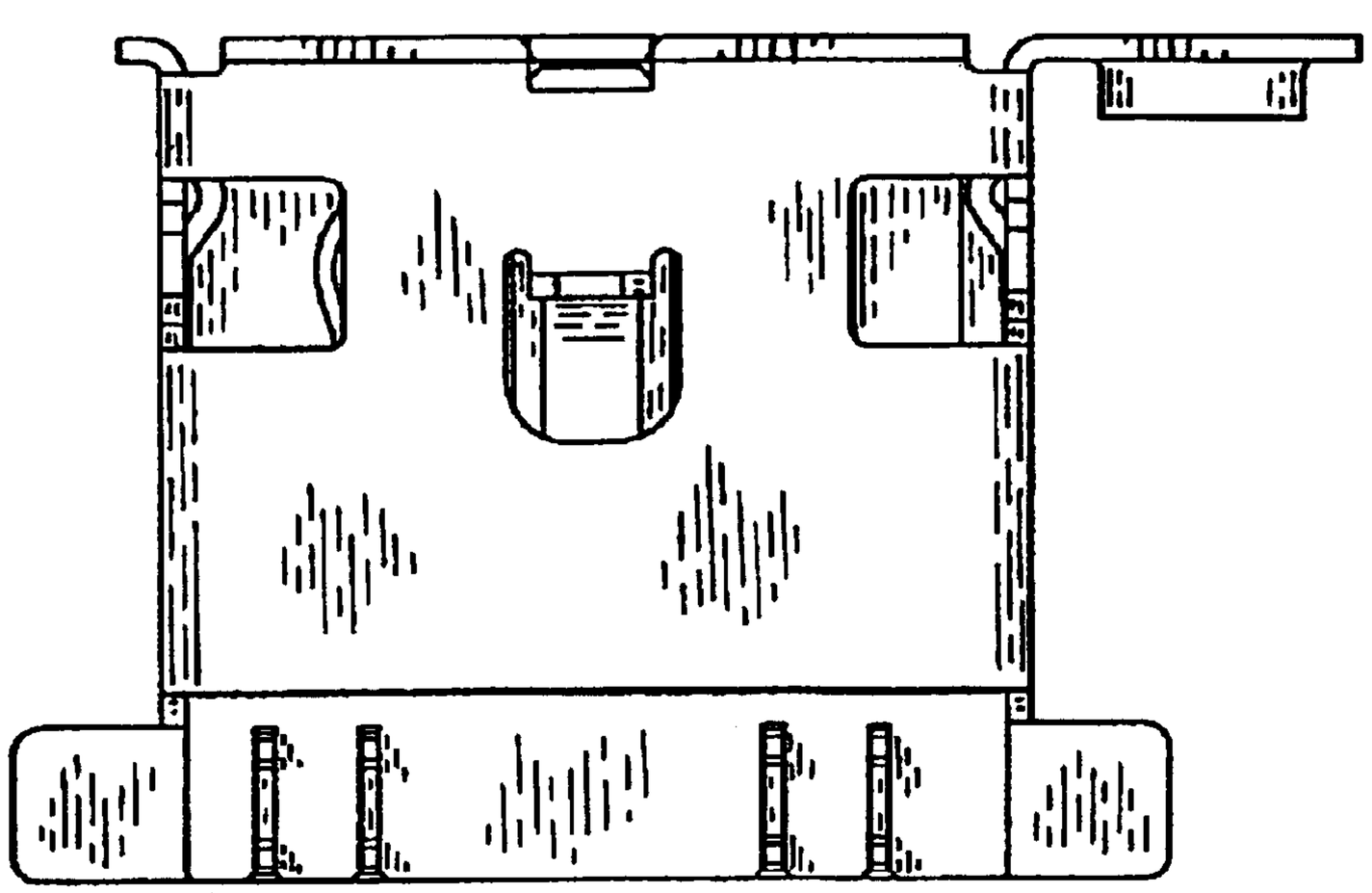

FIG. 5

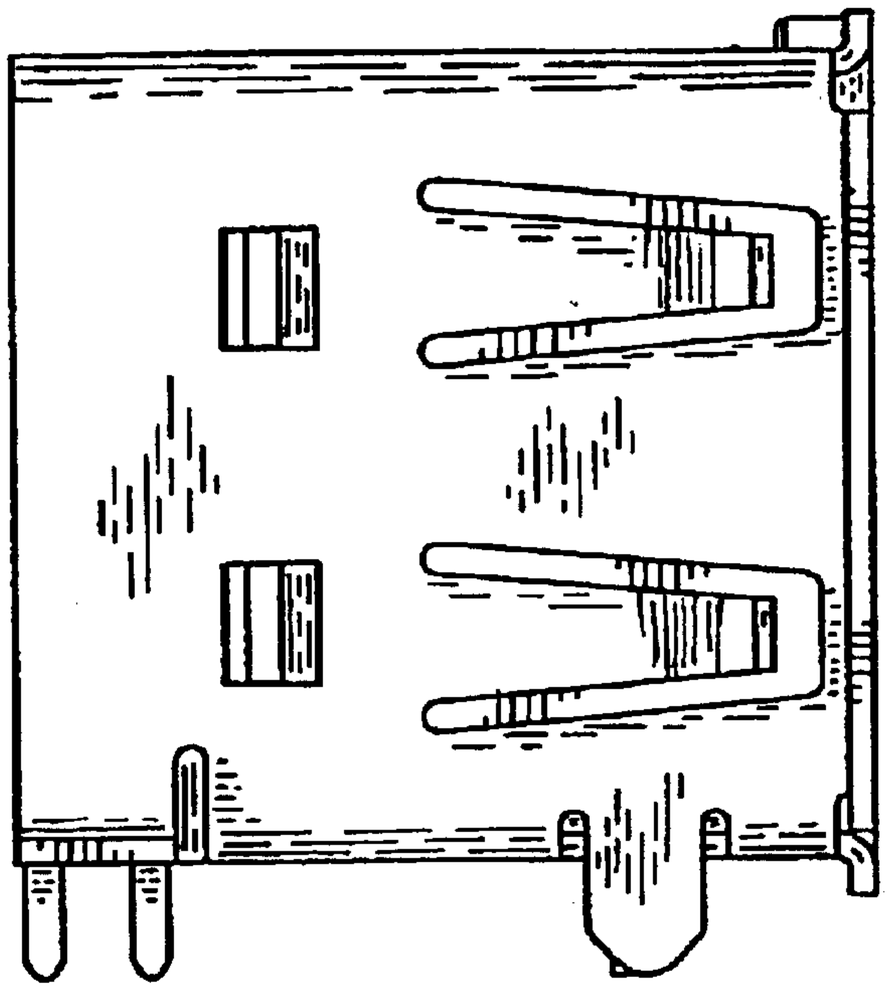

FIG. 6

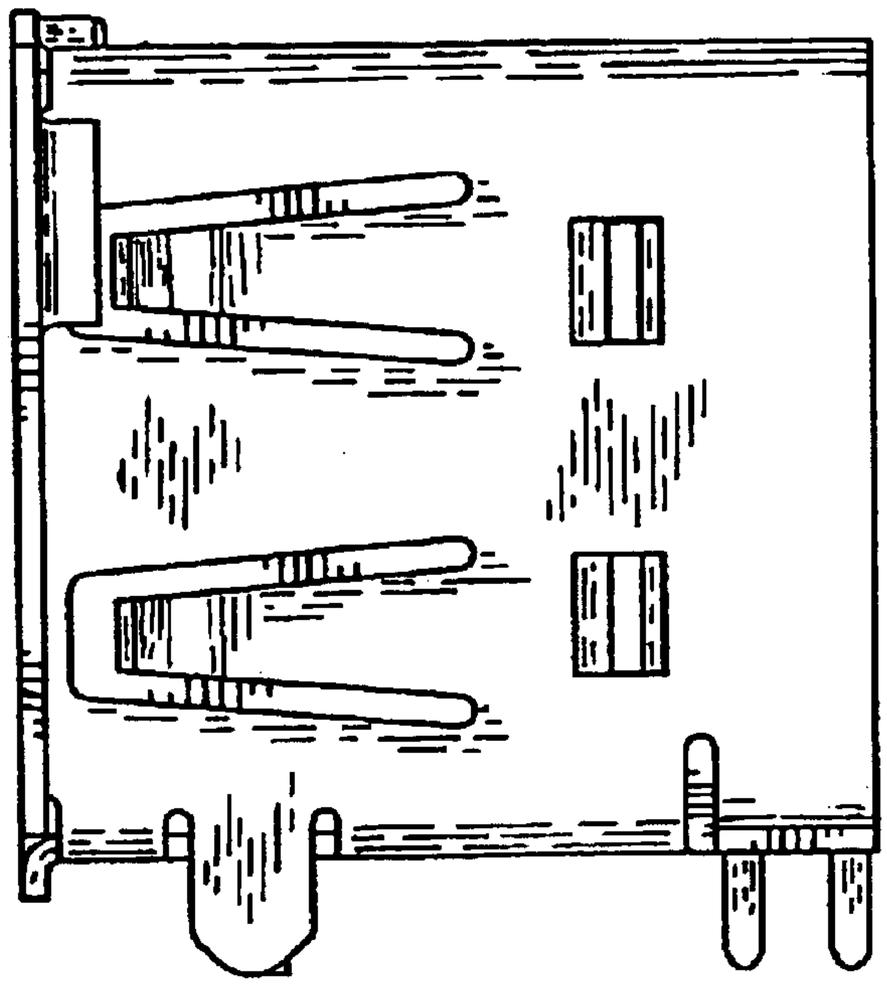

FIG. 7

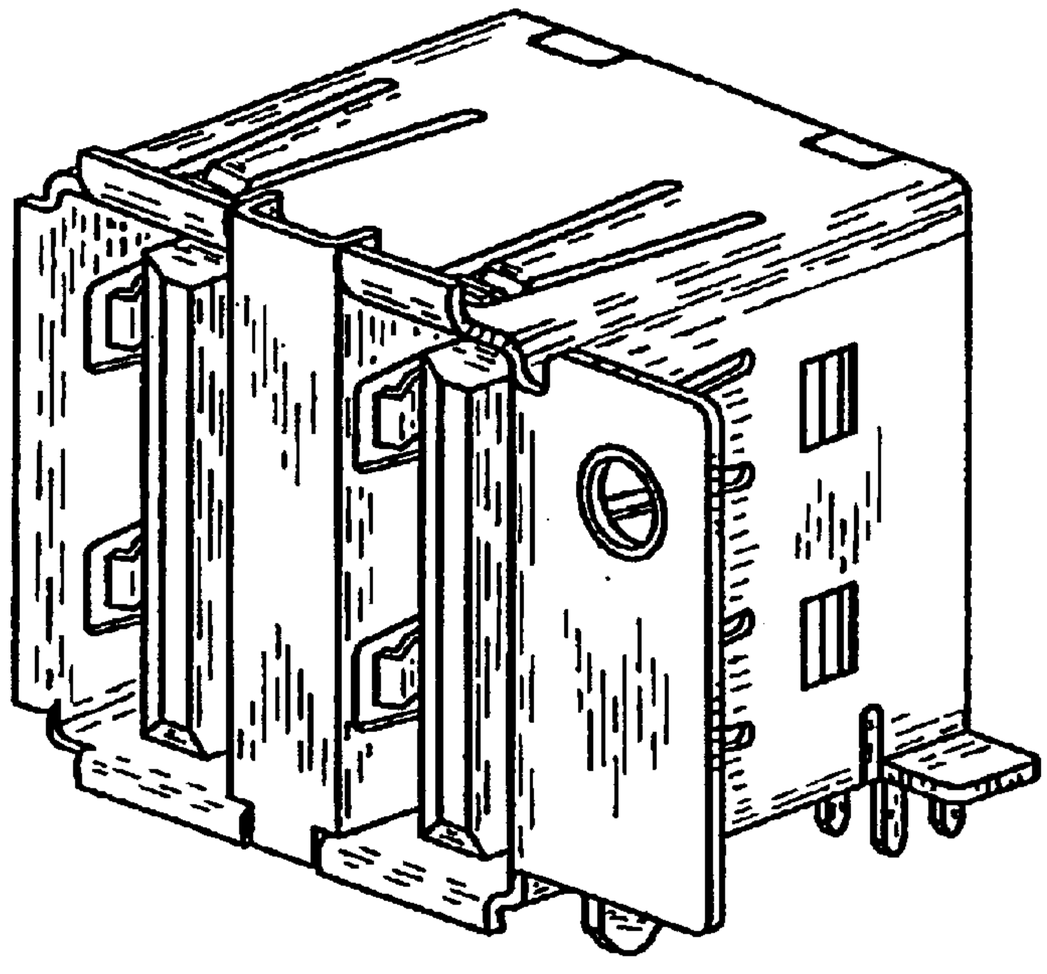

FIG. 8