



US00D502698S

(12) **United States Design Patent**  
**Mah**

(10) **Patent No.:** **US D502,698 S**  
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(54) **ELECTRONICS HOUSING**

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(73) Assignee: **Daka Development Ltd.**, Kowloon (HK)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/196,645**

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(51) **LOC (7) Cl.** ..... **14-01**

(52) **U.S. Cl.** ..... **D14/226; D14/137**

(58) **Field of Search** ..... D14/137, 138, D14/225, 226, 227, 228, 229; 379/431-434; 381/122, 161, 357, 176, 168-169; 181/158, 242; 455/550.1, 566, 575; 345/102-103

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

2,196,342 A \* 4/1940 Ruttenberg ..... 381/357  
D195,966 S \* 8/1963 Spilman ..... D14/225  
D244,780 S \* 6/1977 Kobayashi ..... D14/225

5,966,671 A \* 10/1999 Mitchell et al. .... 455/550.1  
D416,912 S \* 11/1999 Hsieh ..... D14/225  
6,073,034 A \* 6/2000 Jacobsen et al. .... 455/566  
D459,718 S \* 7/2002 Ma et al. .... D14/225

\* cited by examiner

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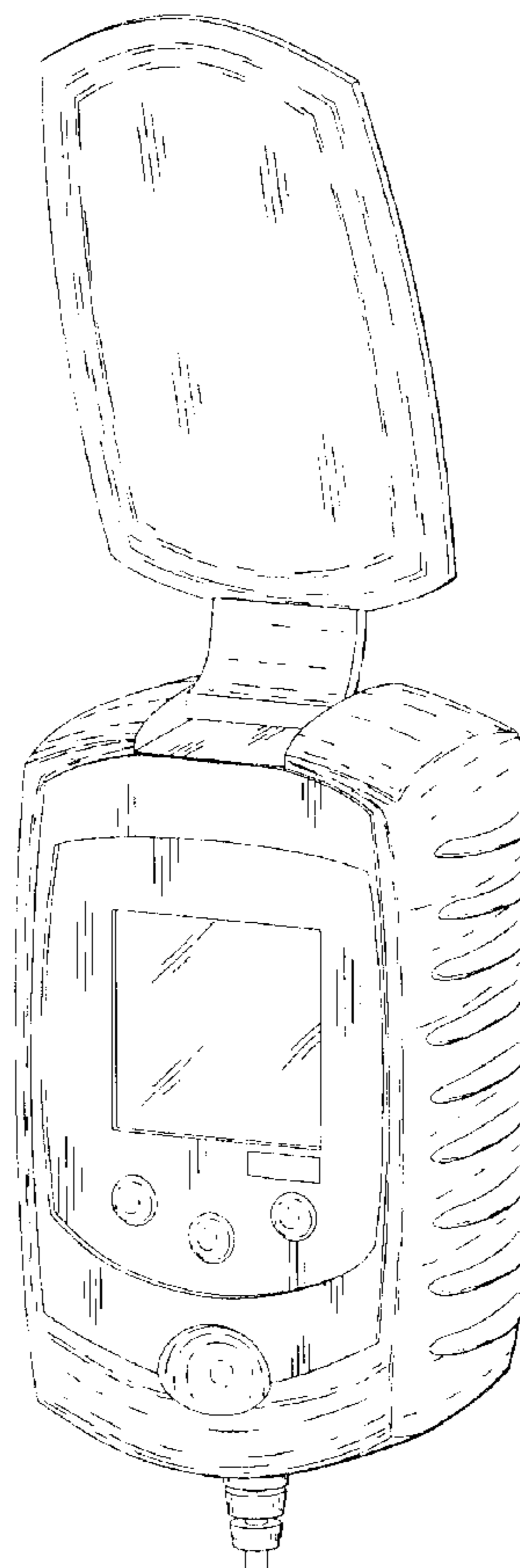
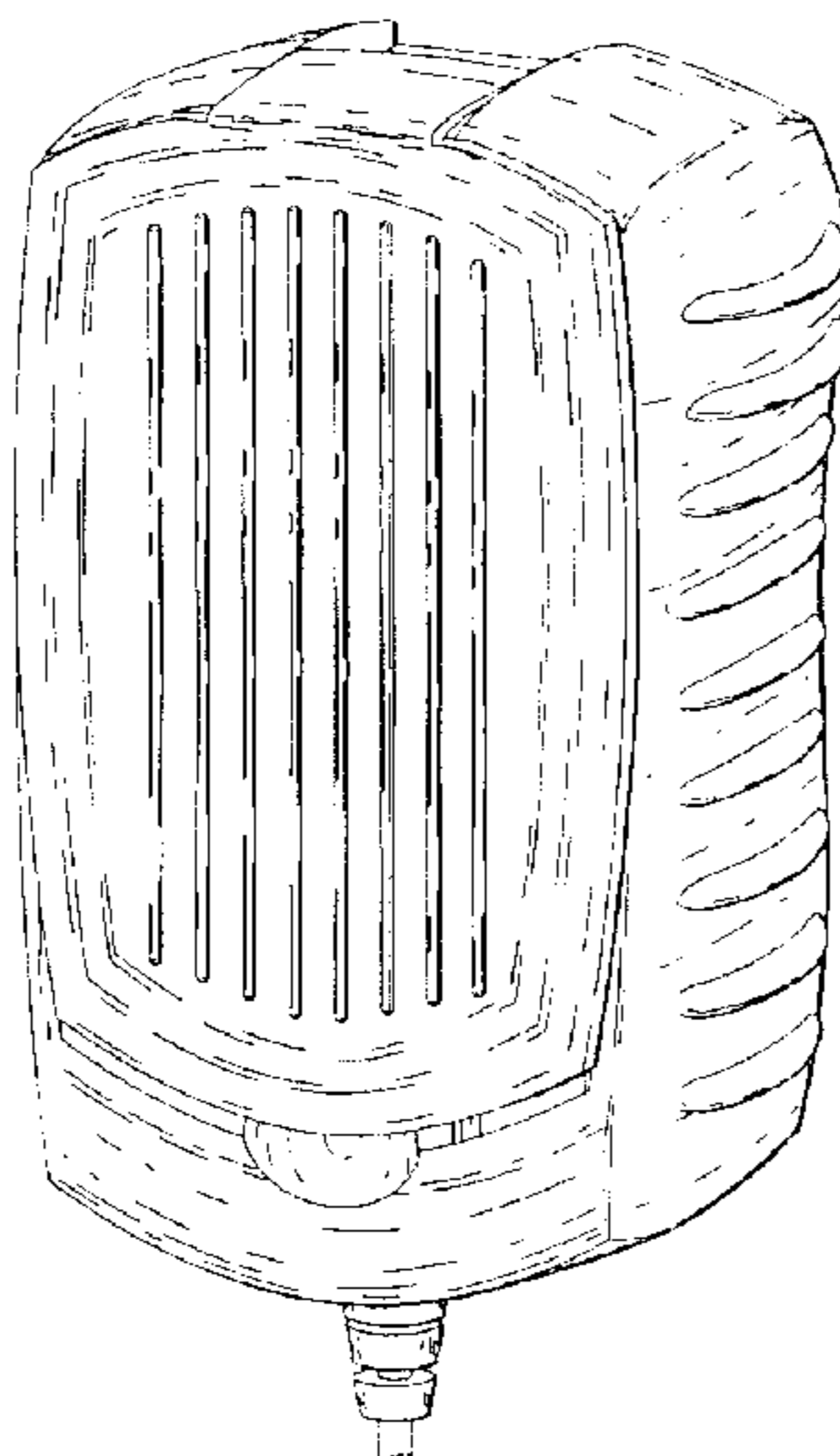
(57) **CLAIM**

The ornamental design for a electronics housing, as shown and described.

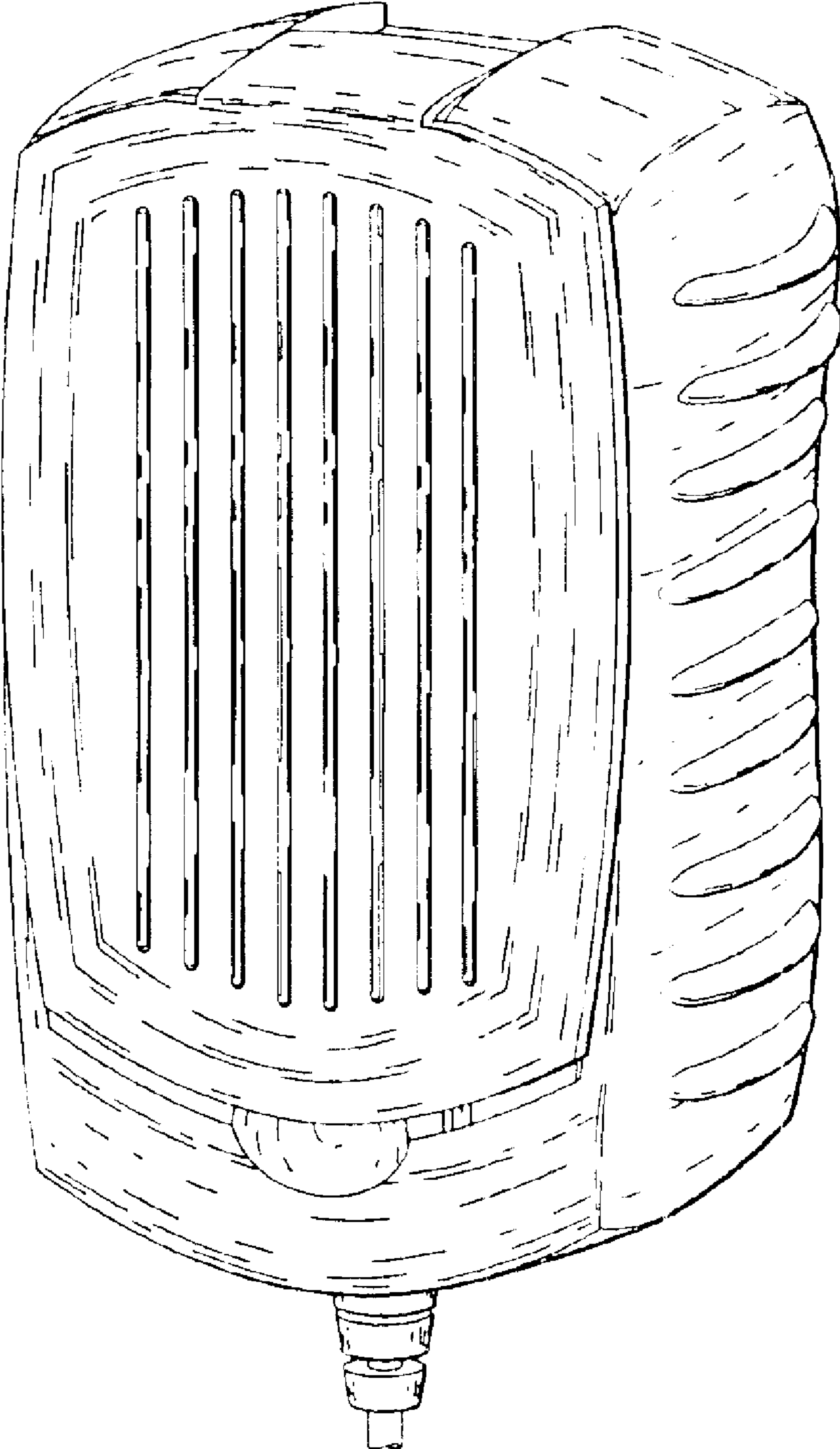
**DESCRIPTION**

FIG. 1 is a perspective view of the electronics housing; FIG. 2 is a perspective view of the electronics housing of FIG. 1 and shown in open position; FIG. 3 is a bottom end view; FIG. 4 is a front view; FIG. 5 is a rear view; FIG. 6 is a right side view and a mirror image of a left side view; and, FIG. 7 is a top end view.

**1 Claim, 4 Drawing Sheets**



*Fig. 1*



*Fig. 2*

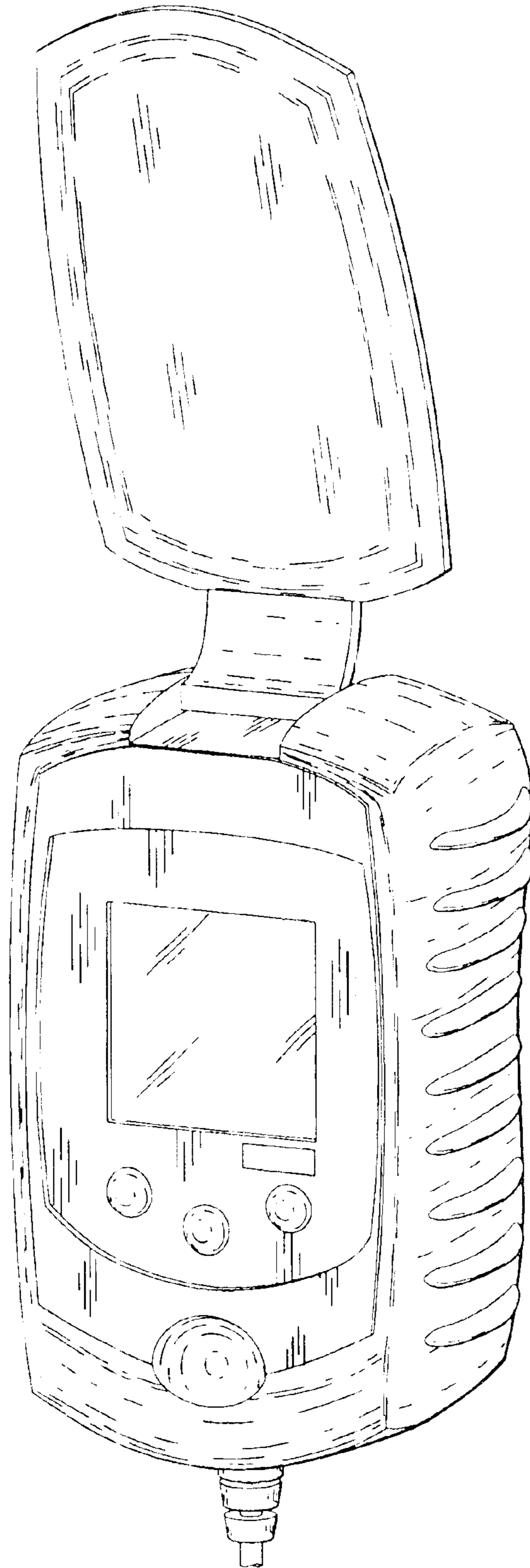


Fig. 5

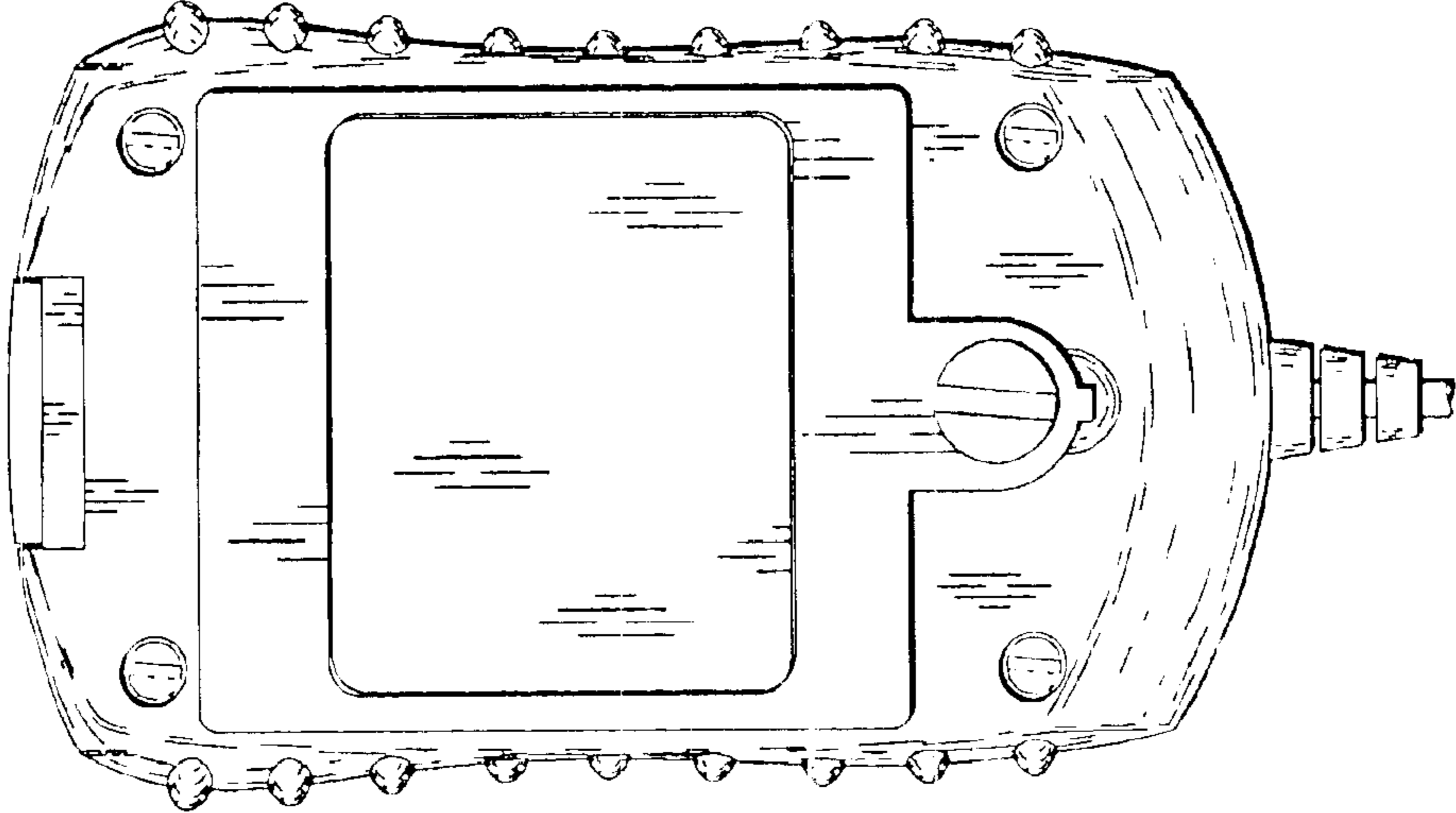
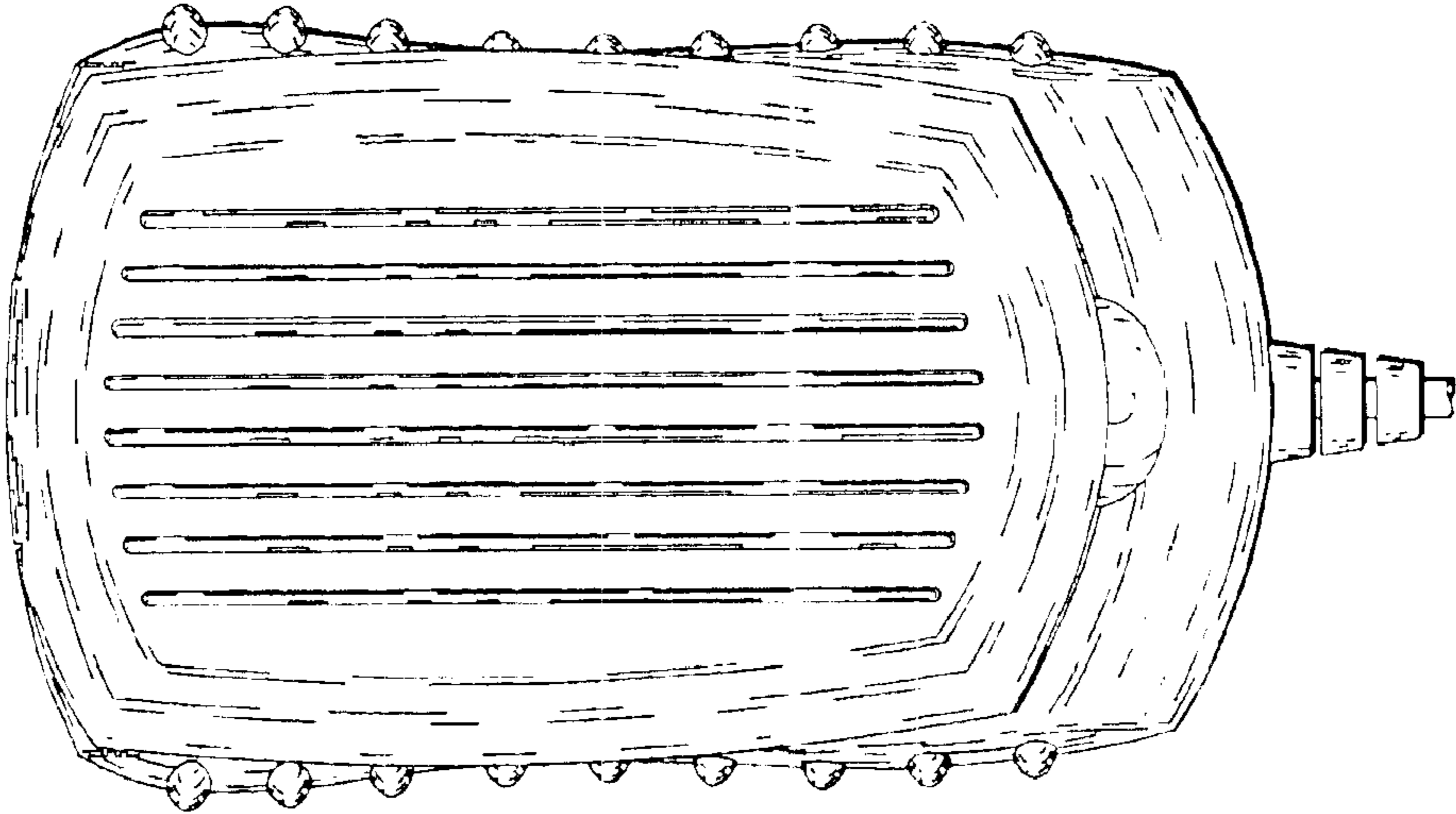
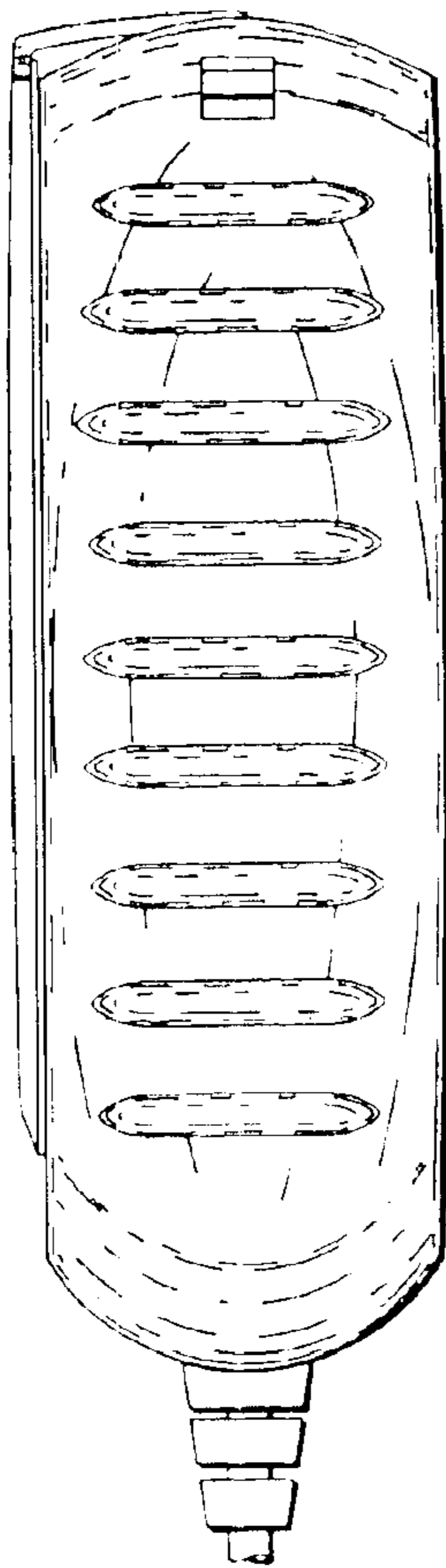


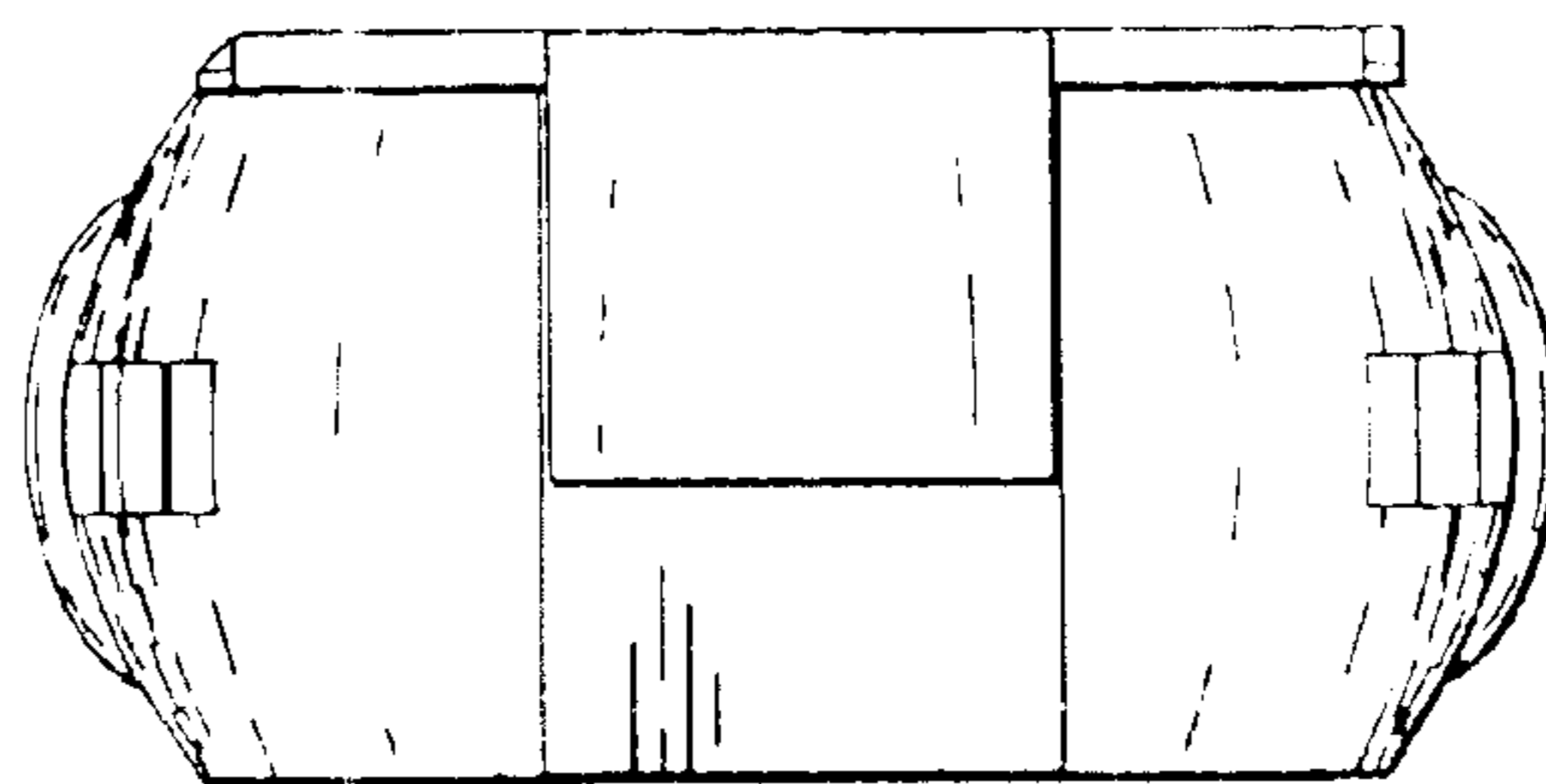
Fig. 4



*Fig. 6*



*Fig. 7*



*Fig. 3*

