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(12) **United States Design Patent**
Iida et al.

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(54) **FLUORESCENT LAMP**
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(**) Term: **14 Years**

CN 3290715D 4/2003
CN 3290716D 4/2003
CN 3314417D 8/2003
JP S61-144561 9/1986
JP D969870 12/1996
JP D969870-1 12/1996
JP H0 9-45283 2/1997

* cited by examiner

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(30) **Foreign Application Priority Data**

Mar. 8, 2004 (JP) 2004-006865

(51) **LOC (7) Cl.** **26-04**
(52) **U.S. Cl.** **D26/3**
(58) **Field of Search** D26/1-4; 313/313,
313/315, 318, 485, 493, 567, 569; 315/52,
53, 56, 58

(57) **CLAIM**

The ornamental design for a fluorescent lamp, as shown and described.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D448,499 S * 9/2001 Choi D26/3
D465,292 S * 11/2002 Ko D26/3

FOREIGN PATENT DOCUMENTS

CN 2534674 Y 2/2003
CN 3279129D 2/2003
CN 3279130D 2/2003
CN 3289323D 4/2003

DESCRIPTION

FIG. 1 is a top perspective view of a fluorescent lamp of the present invention;
FIG. 2 is a front view thereof;
FIG. 3 is a first side view thereof;
FIG. 4 is a second side view thereof;
FIG. 5 is a top view thereof;
FIG. 6 is a bottom view thereof; and,
FIG. 7 is a rear view thereof.

The ornamental design which is claimed is shown in solid lines in the drawings. Any broken lines in the drawings are for illustrative purposes only and form no part of the claimed design.

1 Claim, 2 Drawing Sheets

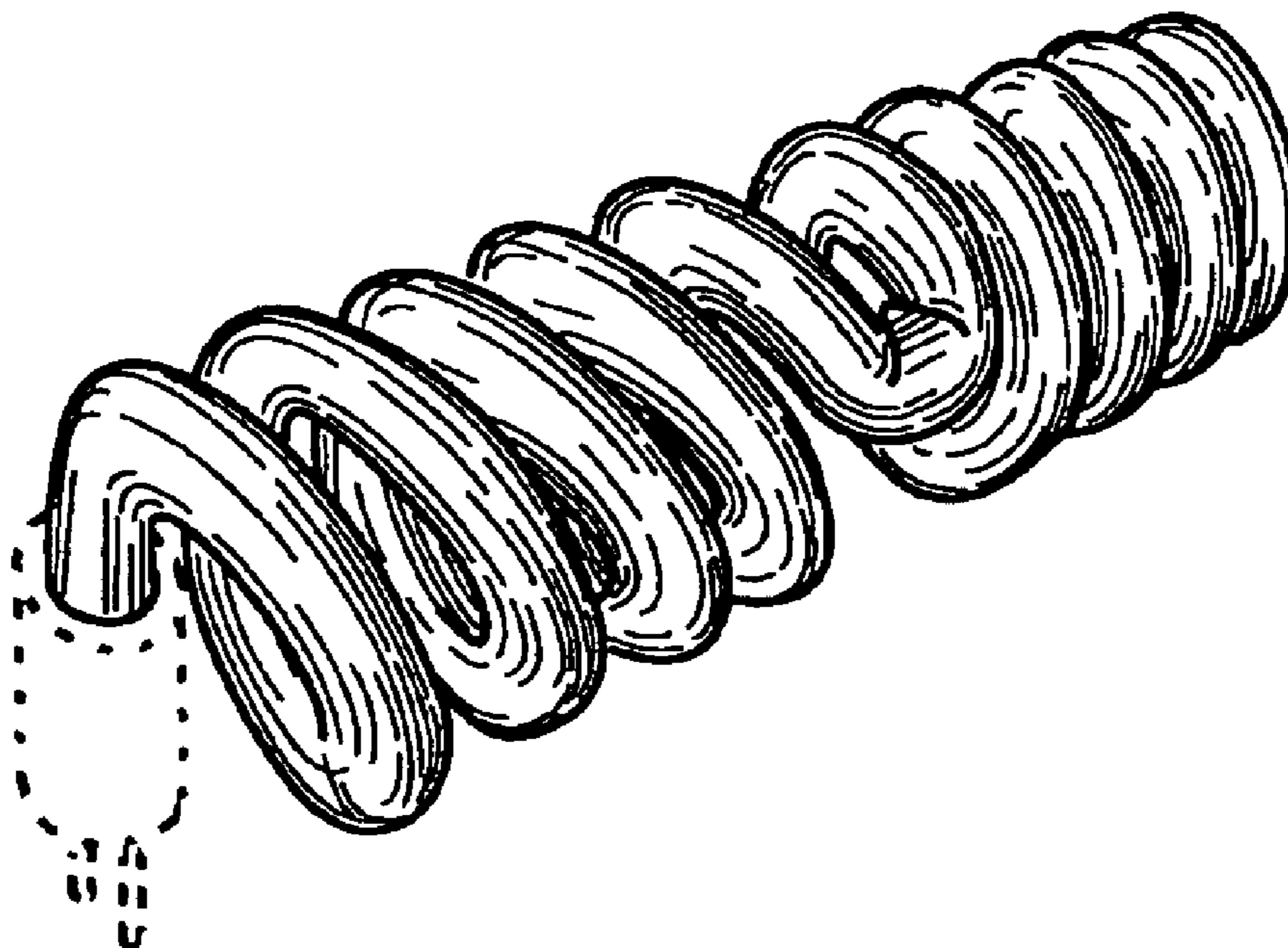


FIG.1

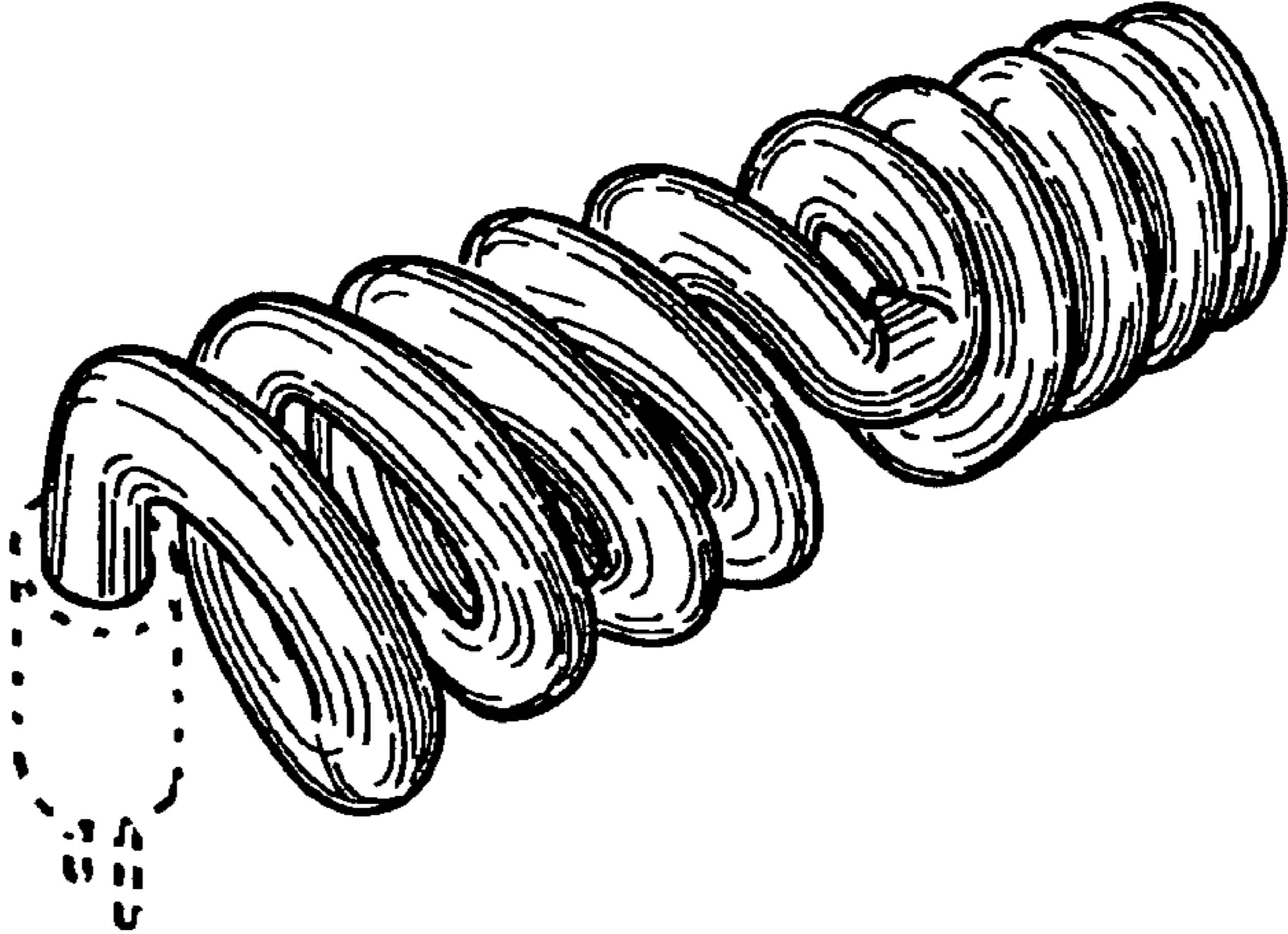


FIG.2

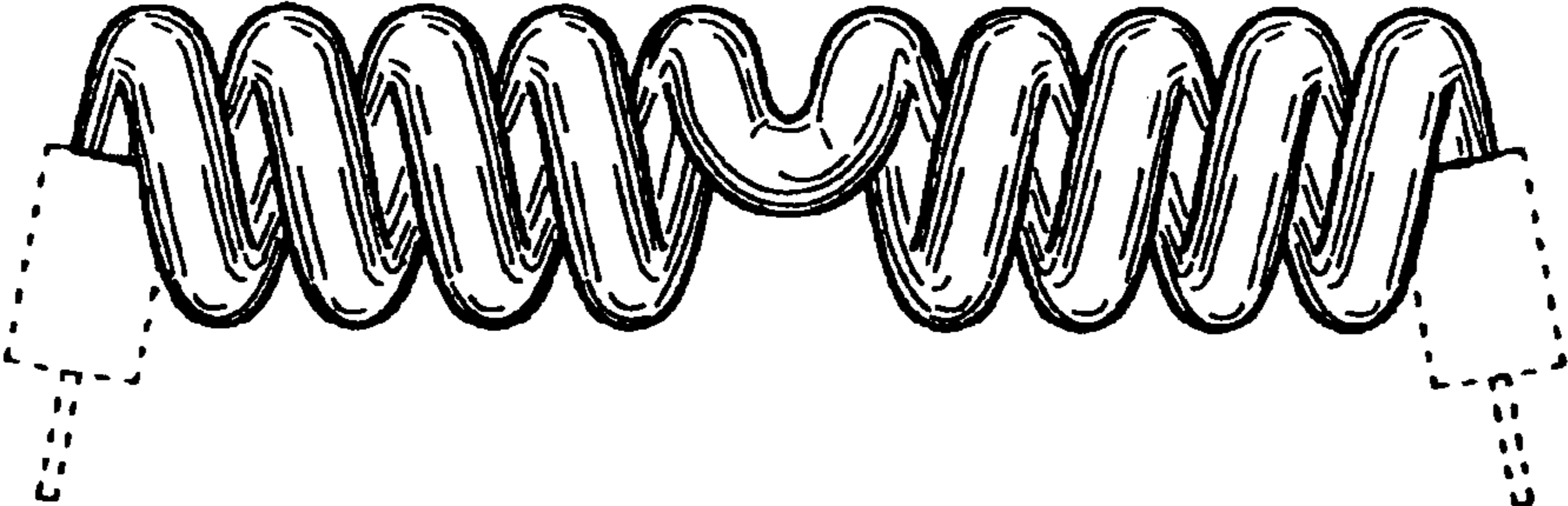


FIG.3

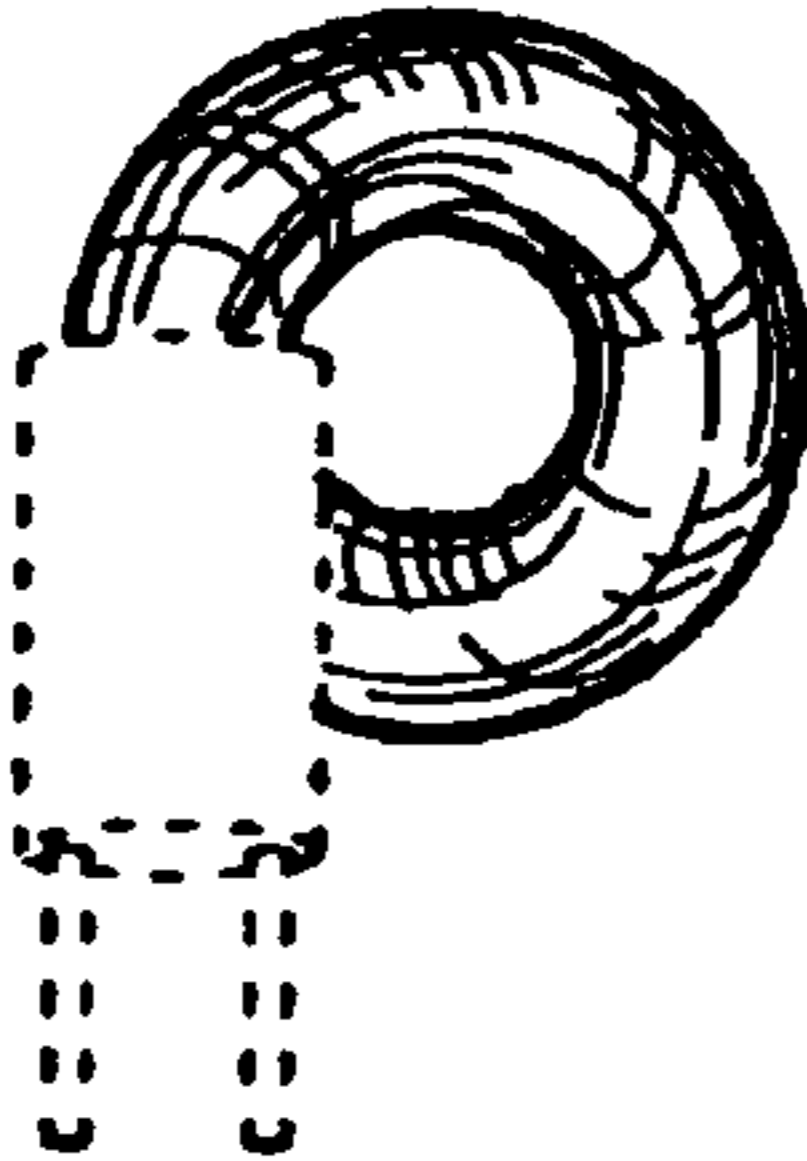


FIG.4

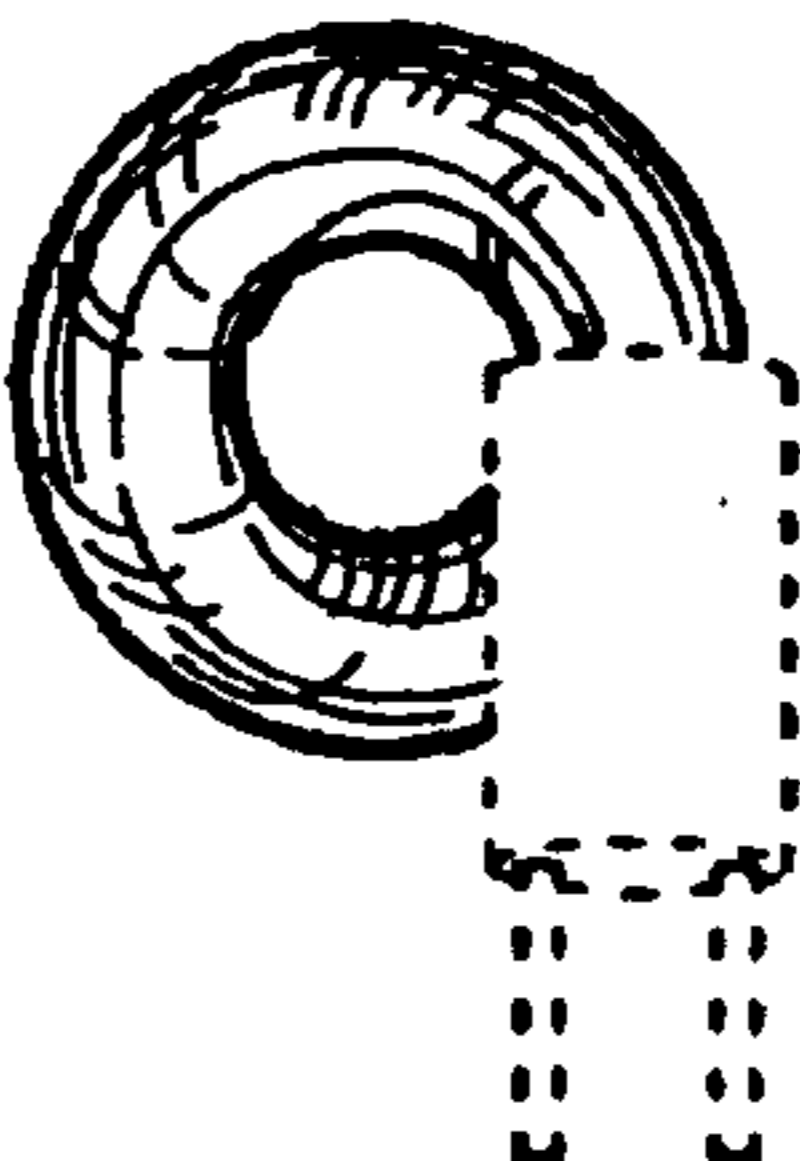


FIG.5

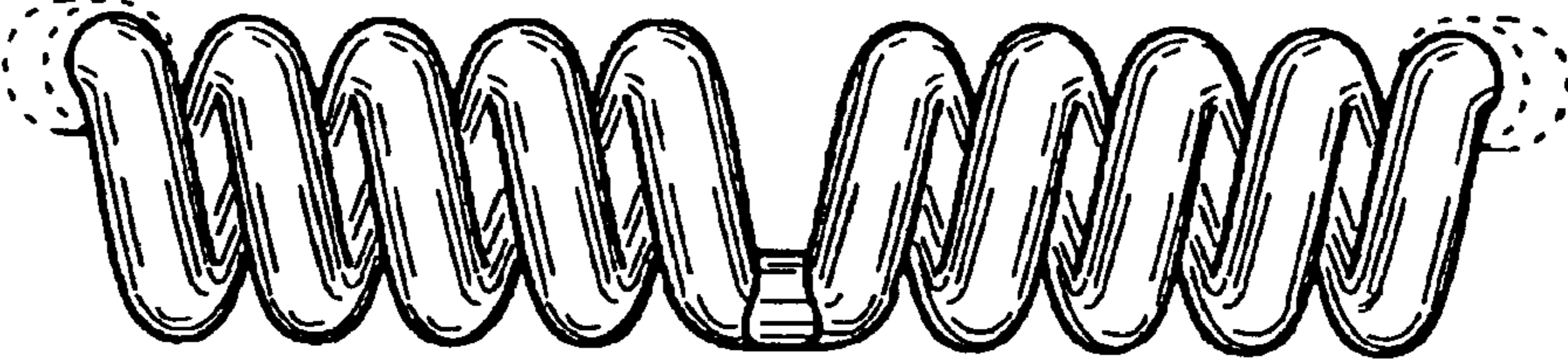


FIG.6

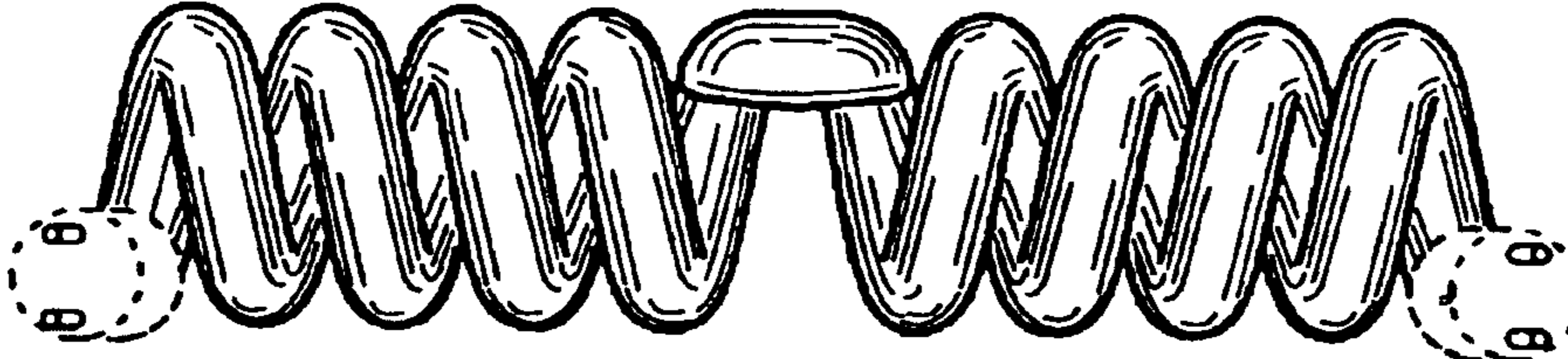


FIG.7

