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(12) **United States Design Patent**
Yugaya

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(45) **Date of Patent:** **** Jan. 18, 2005**

(54) **ELECTRONIC COMPUTER**

JP 1121435 9/2001
JP 1121983 9/2001

(75) **Inventor:** **Yuji Yugaya, Nakano-ku (JP)**

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(73) **Assignee:** **Kabushiki Kaisha Toshiba (JP)**

ASUS World, The Art of Technology, Catalog Page (M2 Series), Apr. 2003.

(**) **Term:** **14 Years**

PC Magazine, The Independent Guide To Personal Computing, vol. 14, No. 14, pp. 156, 167, and 212, Aug. 1995.

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* cited by examiner

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(30) **Foreign Application Priority Data**

Primary Examiner—Freda S. Nunn

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(74) *Attorney, Agent, or Firm*—Banner & Witcoff, Ltd.

(51) **LOC (7) Cl.** **14-02**

(57) **CLAIM**

(52) **U.S. Cl.** **D14/318**

The ornamental design for an electronic computer, as shown and described.

(58) **Field of Search** D14/315–328;
D18/1, 2, 7; 235/145 A, 145 R; 341/22,
23; 345/156, 168, 169, 172, 173; 349/12,
58; 361/680–686

DESCRIPTION

(56) **References Cited**

FIG. 1 is a top, front and right side perspective view of an electronic computer showing my new design, FIG. 2 is a front elevational view thereof, FIG. 3 is a rear elevational view thereof, FIG. 4 is a left side elevational view thereof, FIG. 5 is a right side elevational view thereof, FIG. 6 is a top plan view thereof, FIG. 7 is a bottom plan view thereof, FIG. 8 is a front elevational view showing the article in a closed position thereof, FIG. 9 is a right side elevational view showing the article in the closed position thereof; and, FIG. 10 is a top plan view showing the article in the closed position thereof.

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1 Claim, 5 Drawing Sheets

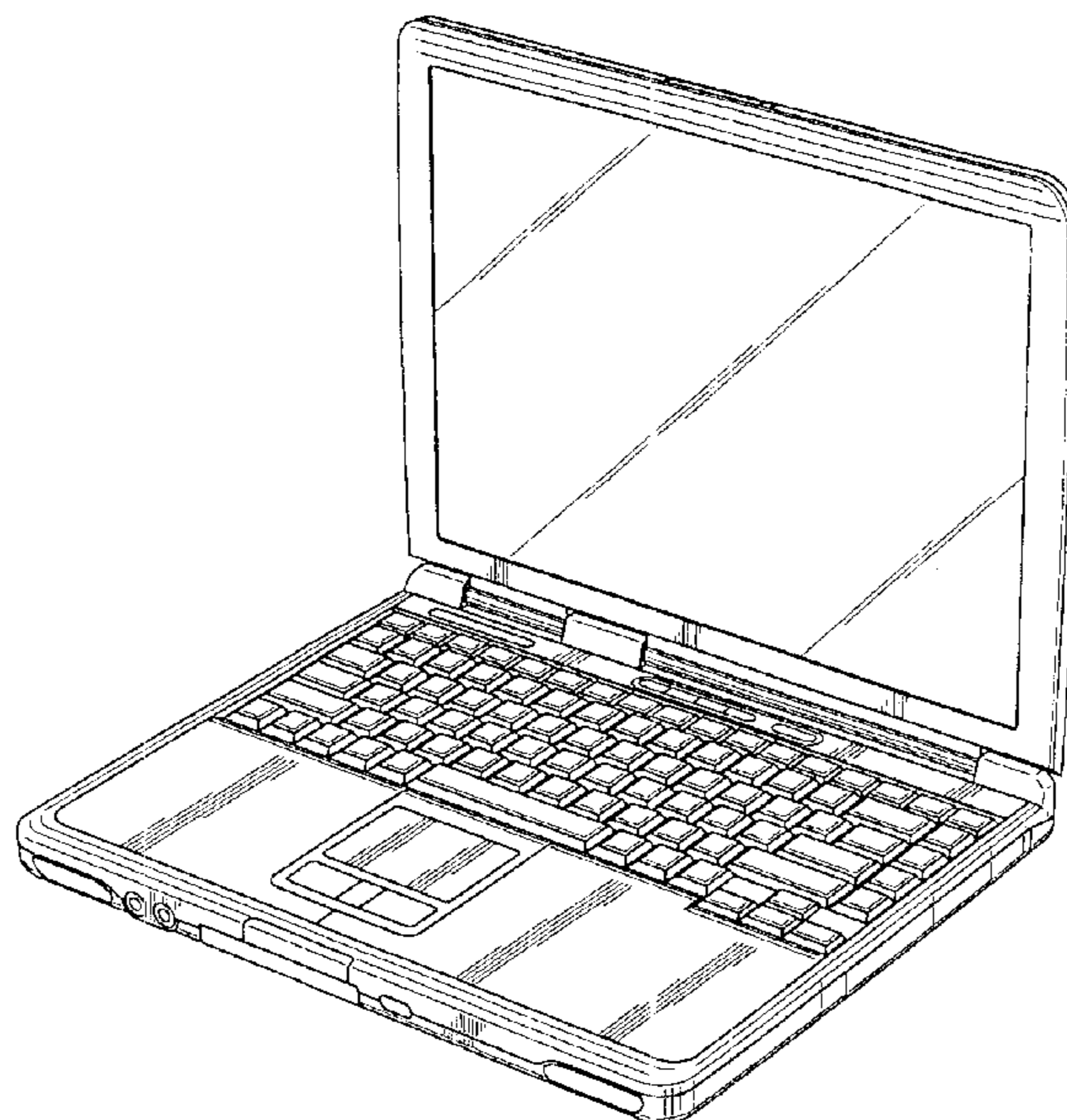


FIG. 1

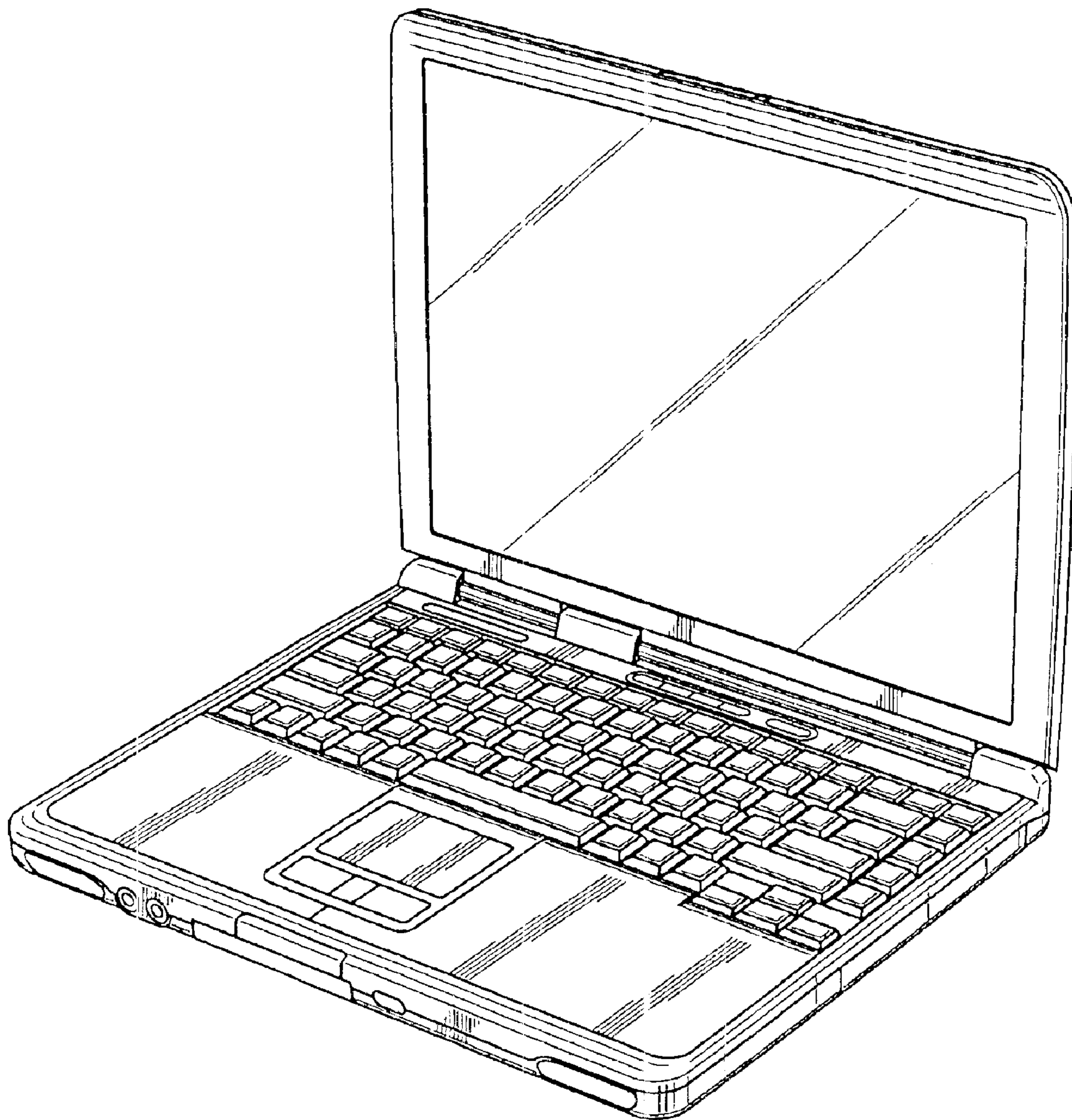


FIG. 2

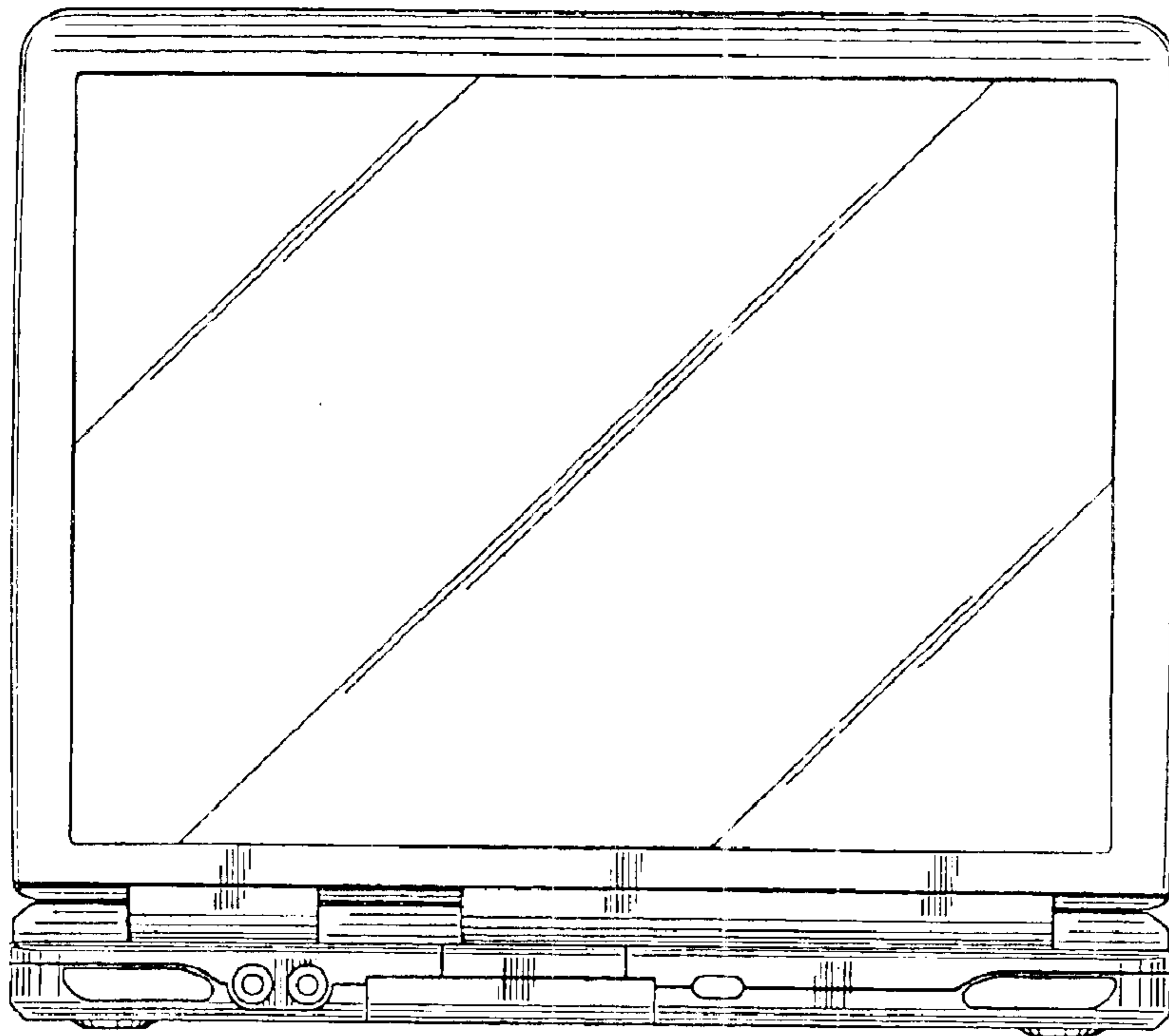


FIG. 3

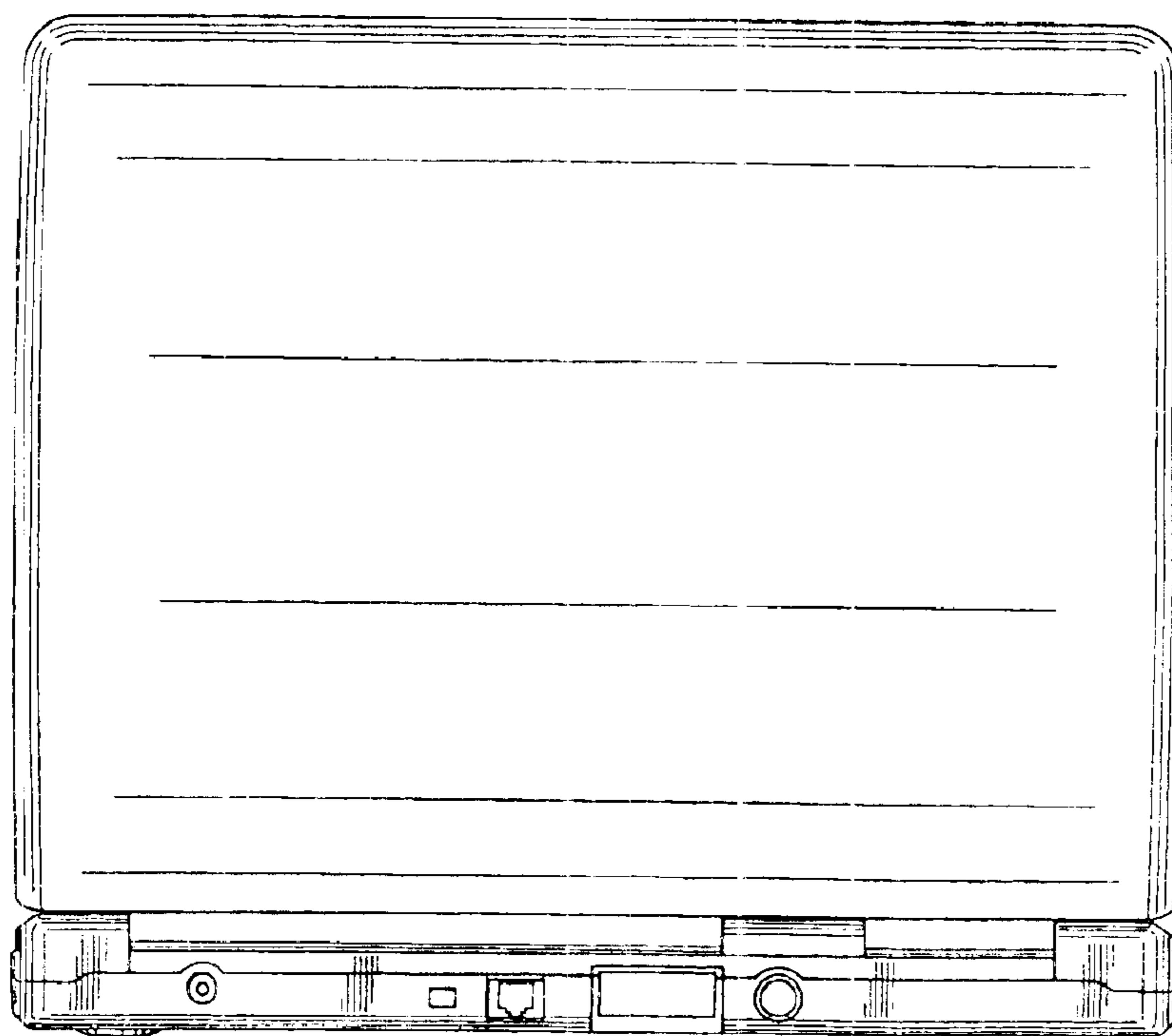


FIG. 4

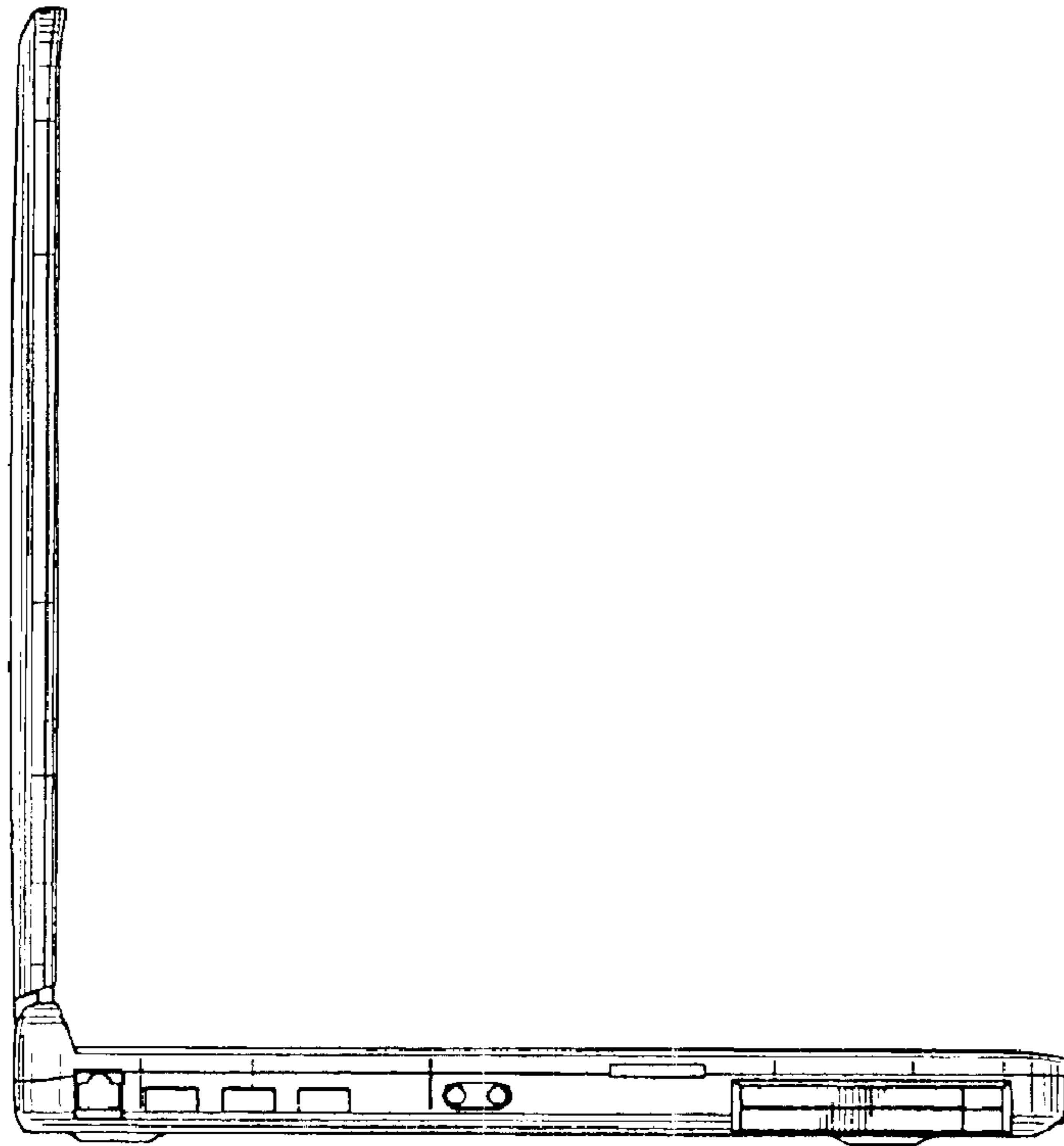


FIG. 5

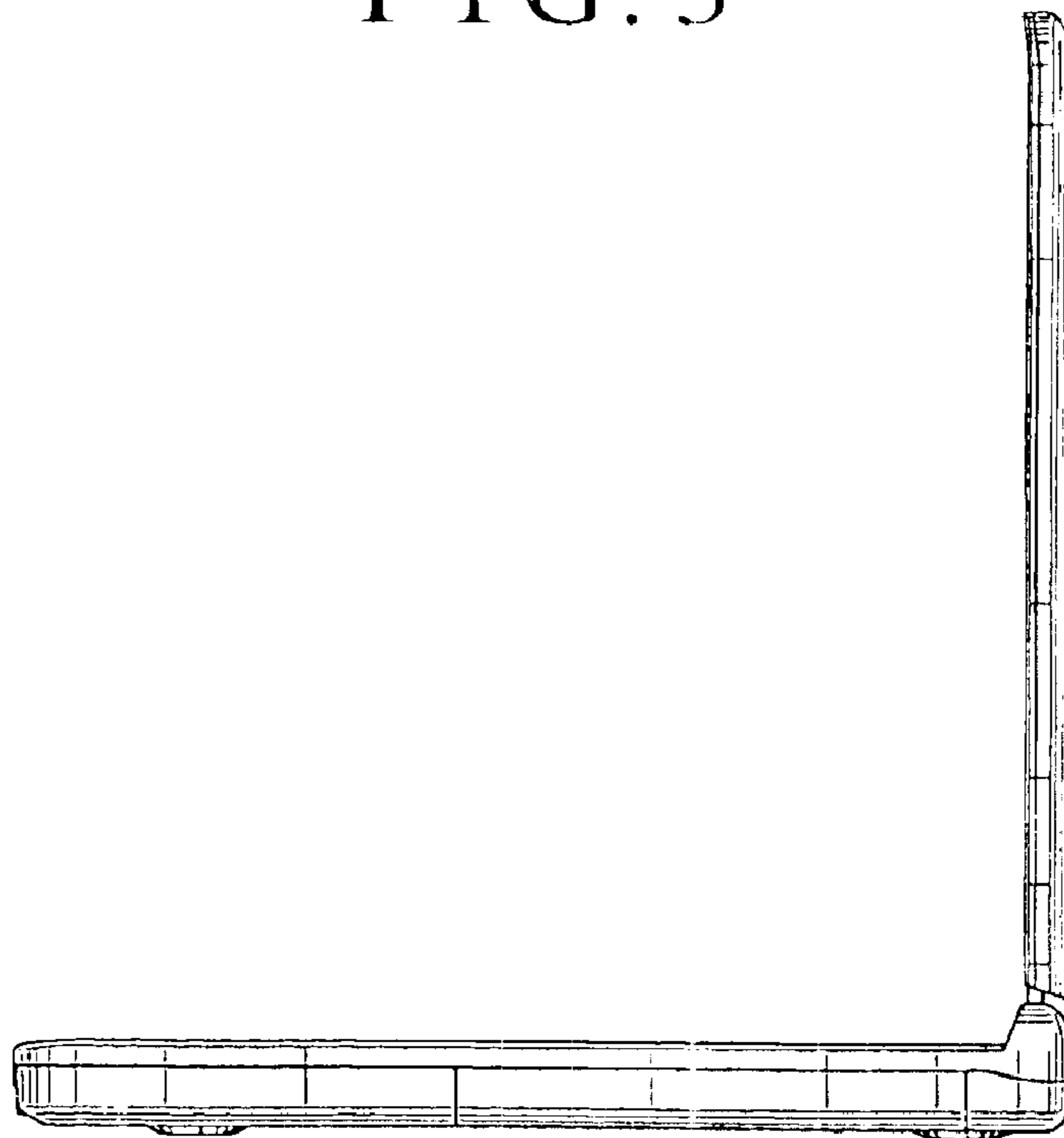


FIG. 6

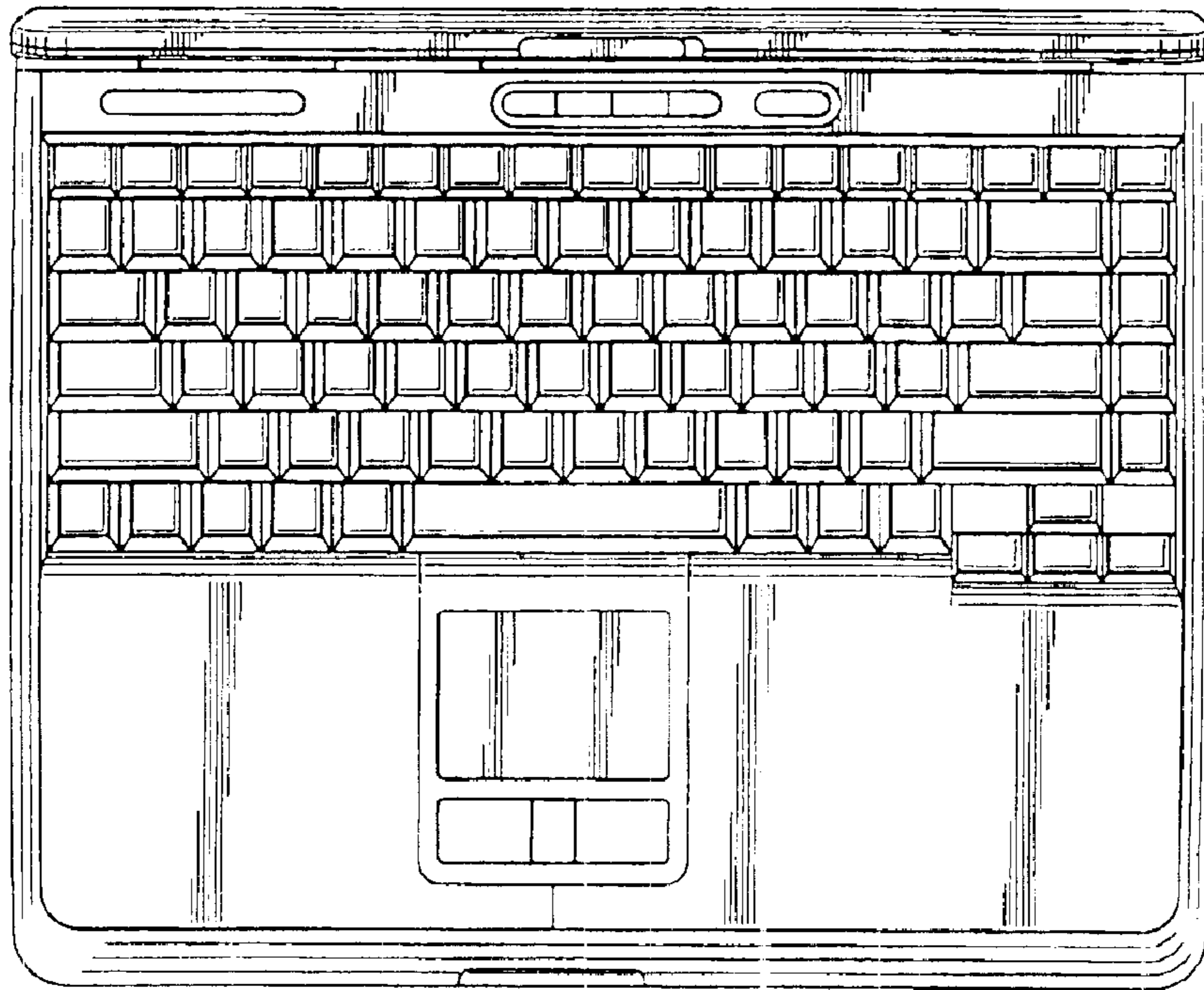


FIG. 7

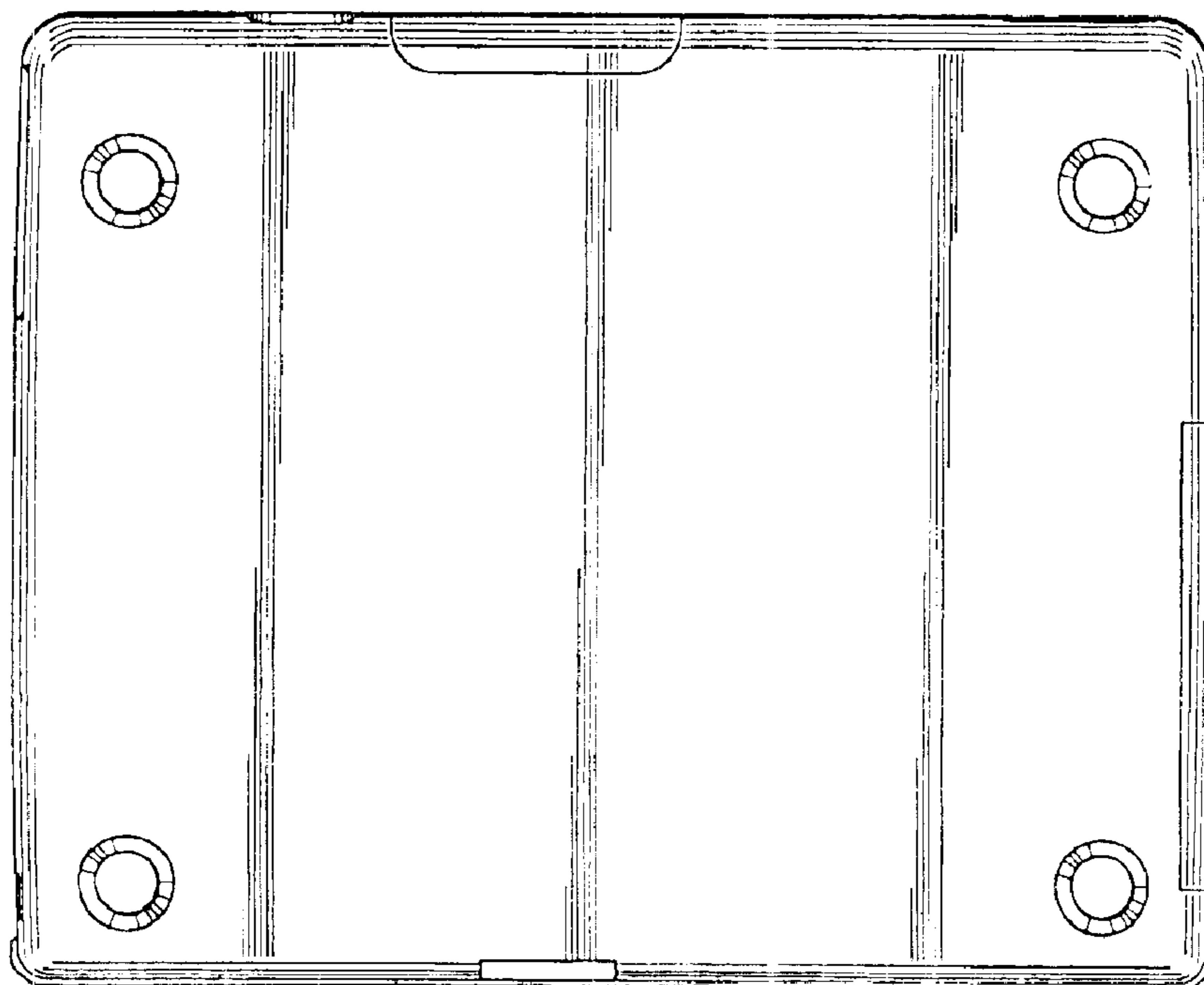


FIG. 8

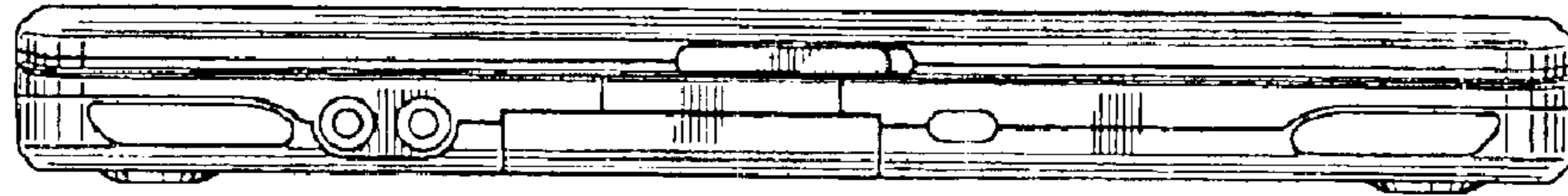


FIG. 9

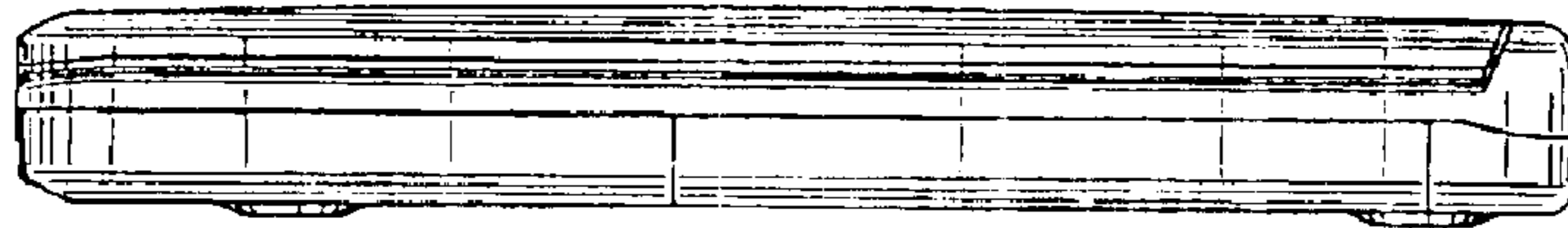


FIG. 10

