

US00D499727S

(12) **United States Design Patent** (10) **Patent No.:** **US D499,727 S**
Kondo (45) **Date of Patent:** **** Dec. 14, 2004**

(54) **ELECTRONIC COMPUTER**

JP 1145488 7/2002
JP 1163202 1/2003

(75) Inventor: **Osamu Kondo**, Nakano-ku (JP)

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(73) Assignee: **Kabushiki Kaisha Toshiba** (JP)

PC Magazine, vol. 14, No. 14, pp. 156, 167 and 212, dated Aug. 1995.

(**) Term: **14 Years**

* cited by examiner

(21) Appl. No.: **29/200,305**

Primary Examiner—Freda S. Nunn

(22) Filed: **Feb. 27, 2004**

(74) *Attorney, Agent, or Firm*—Banner & Witcoff, Ltd.

(30) **Foreign Application Priority Data**

(57) **CLAIM**

Jan. 9, 2004 (JP) 2004-000342

The ornamental design for electronic computer, as shown and described.

(51) **LOC (7) Cl.** **14-02**

(52) **U.S. Cl.** **D14/318**

(58) **Field of Search** D14/318–322;
D18/1, 2, 7; 235/145 A, 145 R; 341/22,
23; 345/104, 156, 168, 169, 173, 538; 361/680–685,
690–696; 400/486, 489

DESCRIPTION

(56) **References Cited**

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FIG. 1 is a reduced top, front and right side perspective view of electronic computer showing my new design, FIG. 2 is a front elevational view thereof, FIG. 3 is a rear elevational view thereof, FIG. 4 is a left side elevational view thereof, FIG. 5 is a right side elevational view thereof, FIG. 6 is a top plan view thereof, FIG. 7 is a bottom plan view thereof, FIG. 8 is a front elevational view showing the article in a closed position thereof, FIG. 9 is a right side elevational view showing the article in the closed position thereof; and, FIG. 10 is a top plan view showing the article in the closed position thereof.

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1 Claim, 5 Drawing Sheets

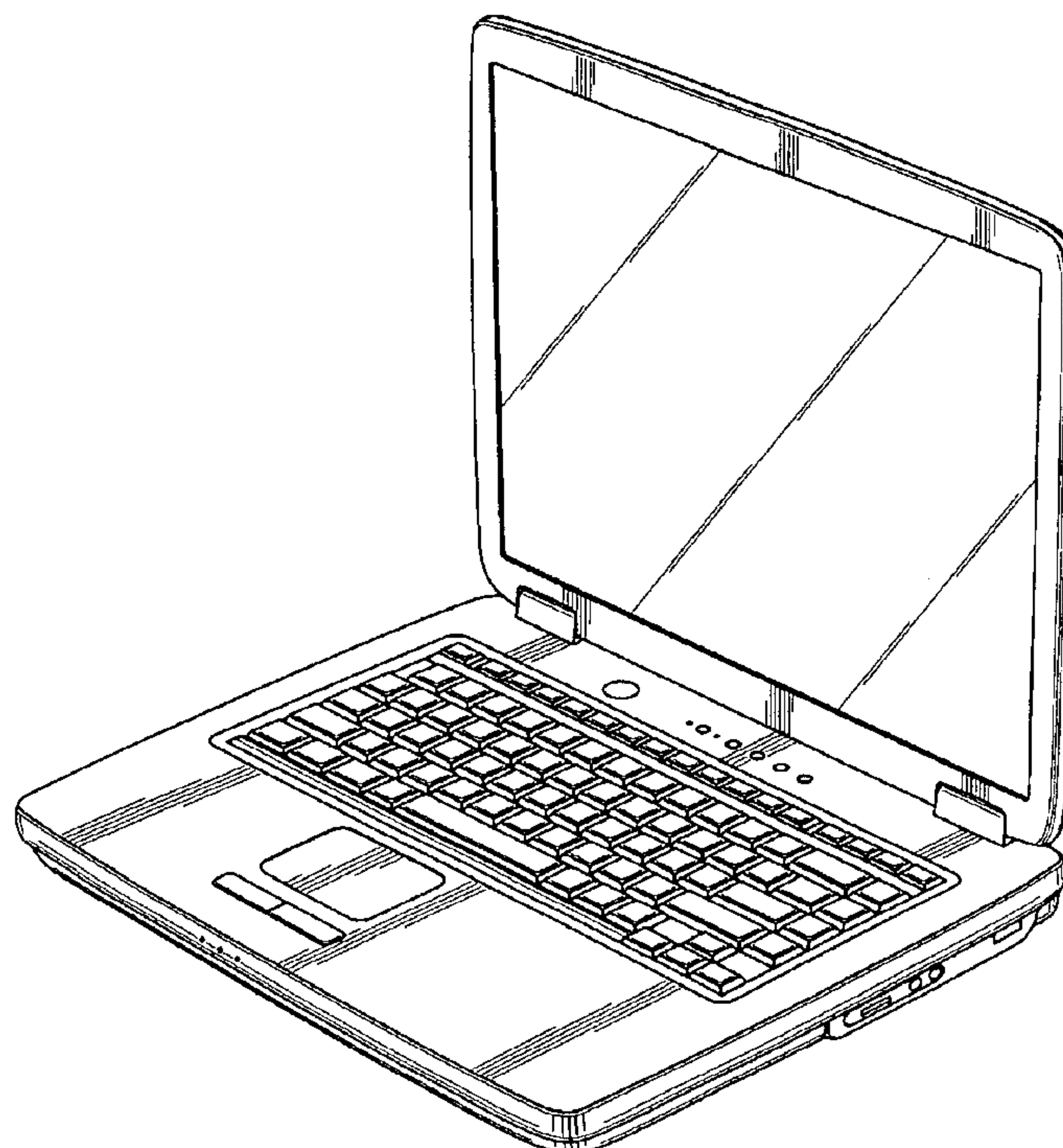


FIG. 1

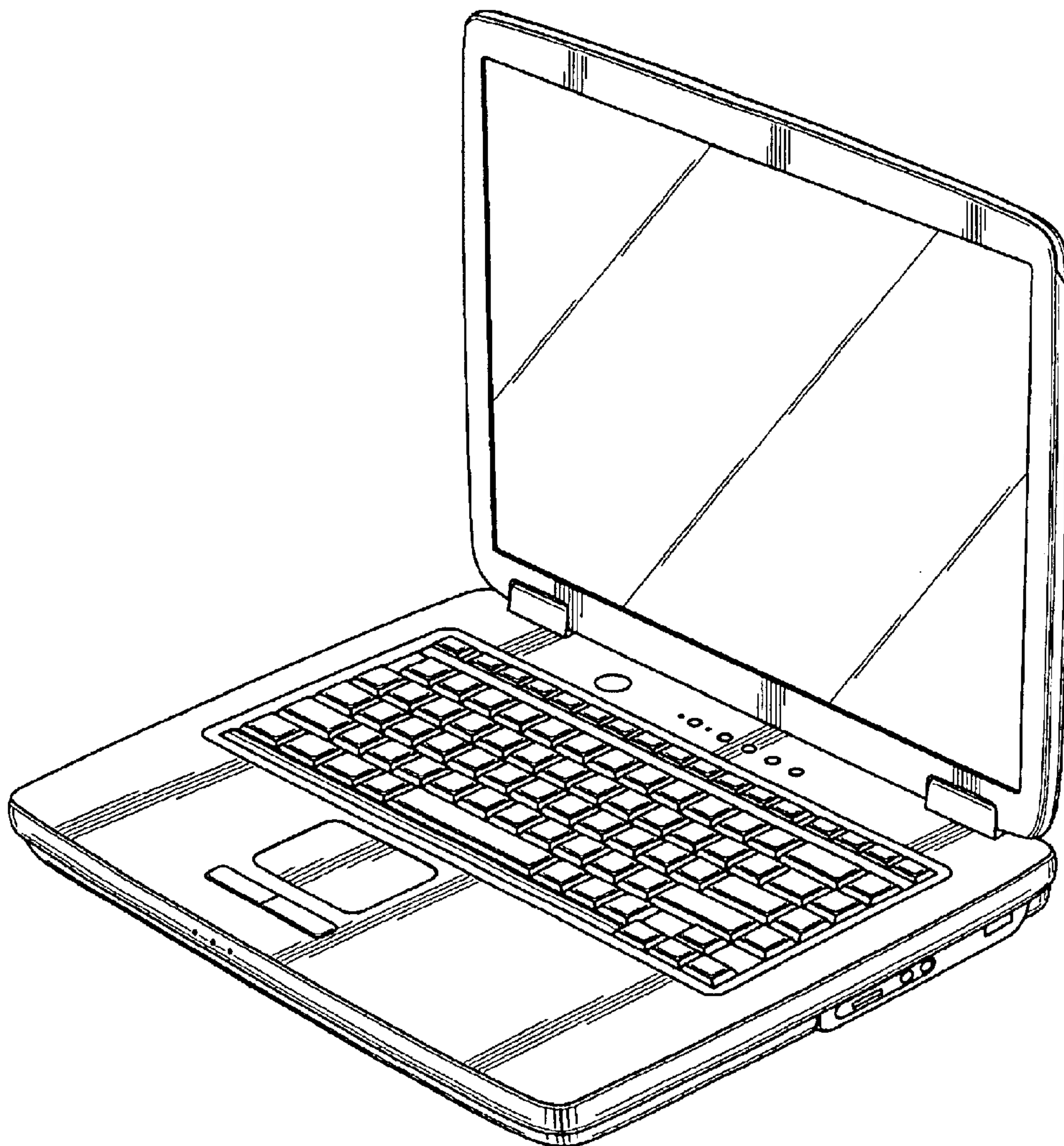


FIG. 2

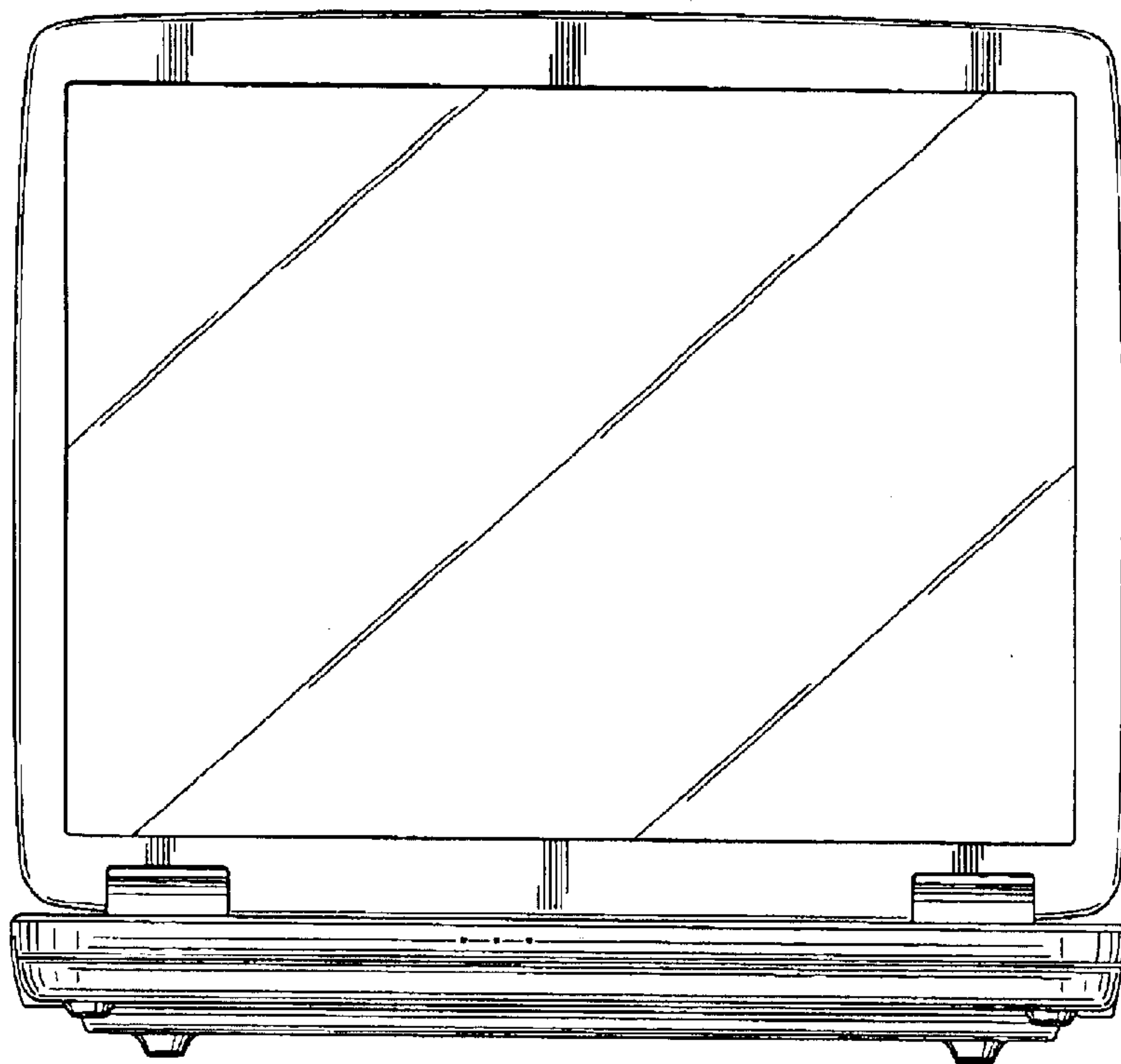


FIG. 3

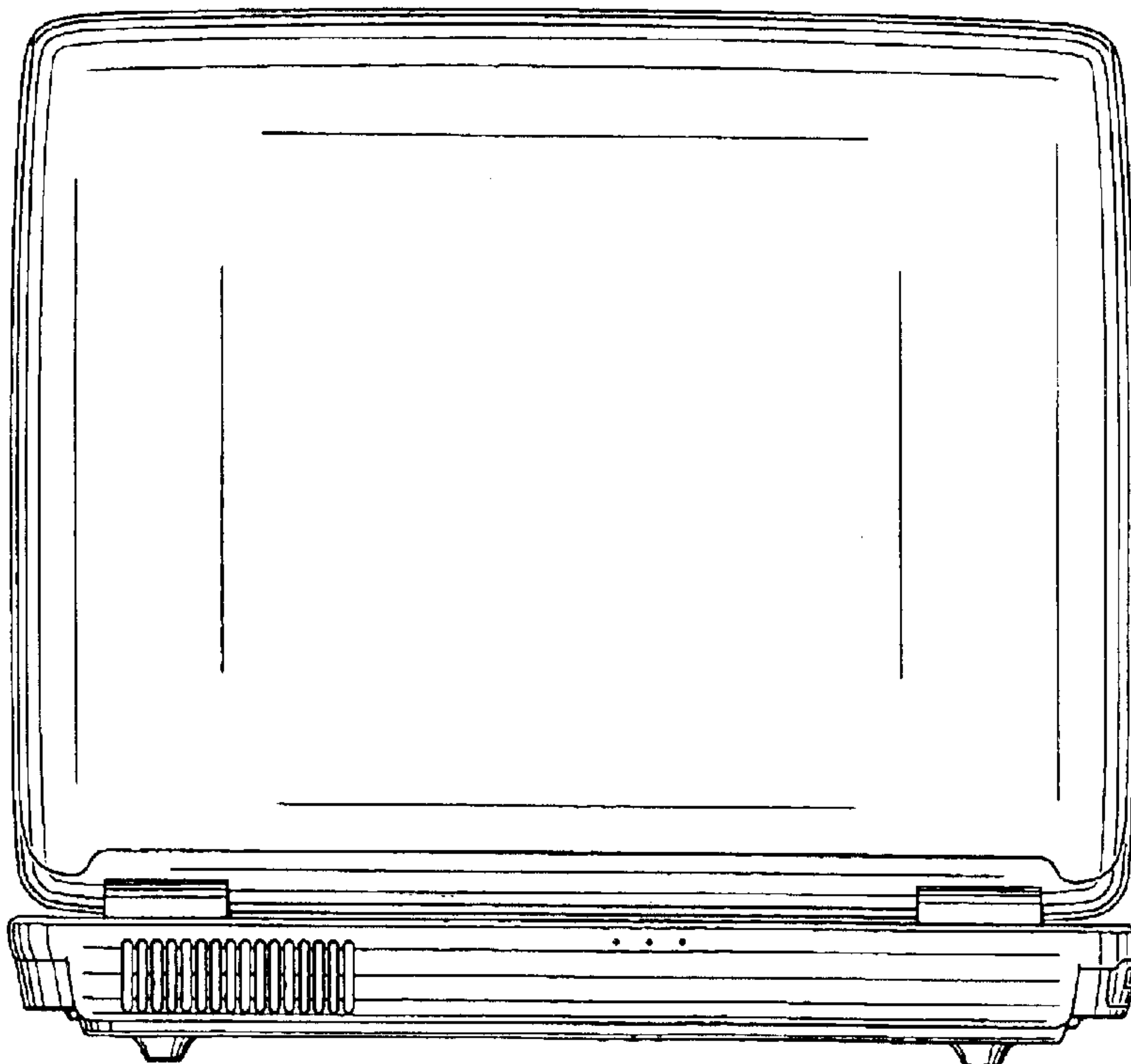


FIG. 4

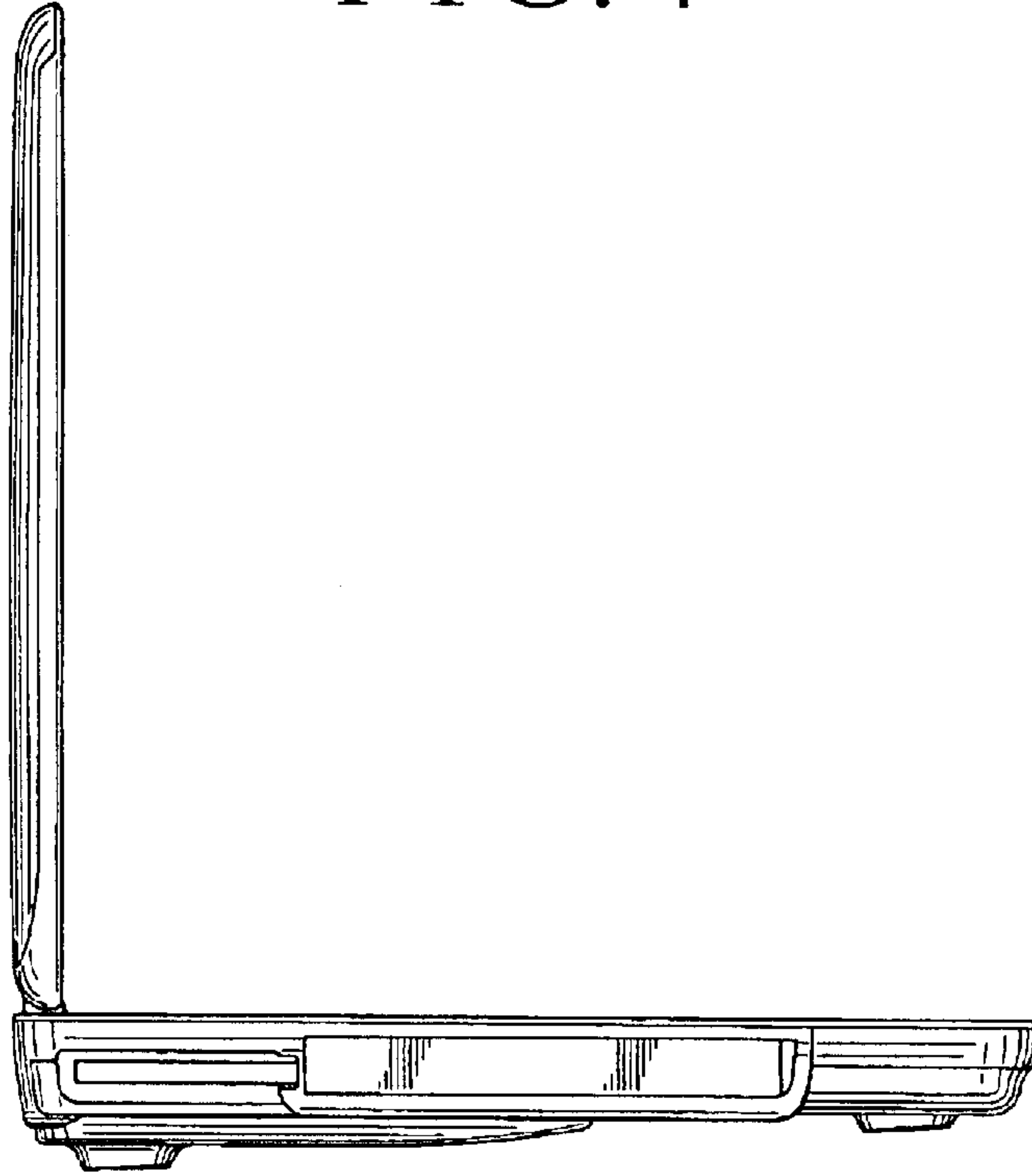


FIG. 5

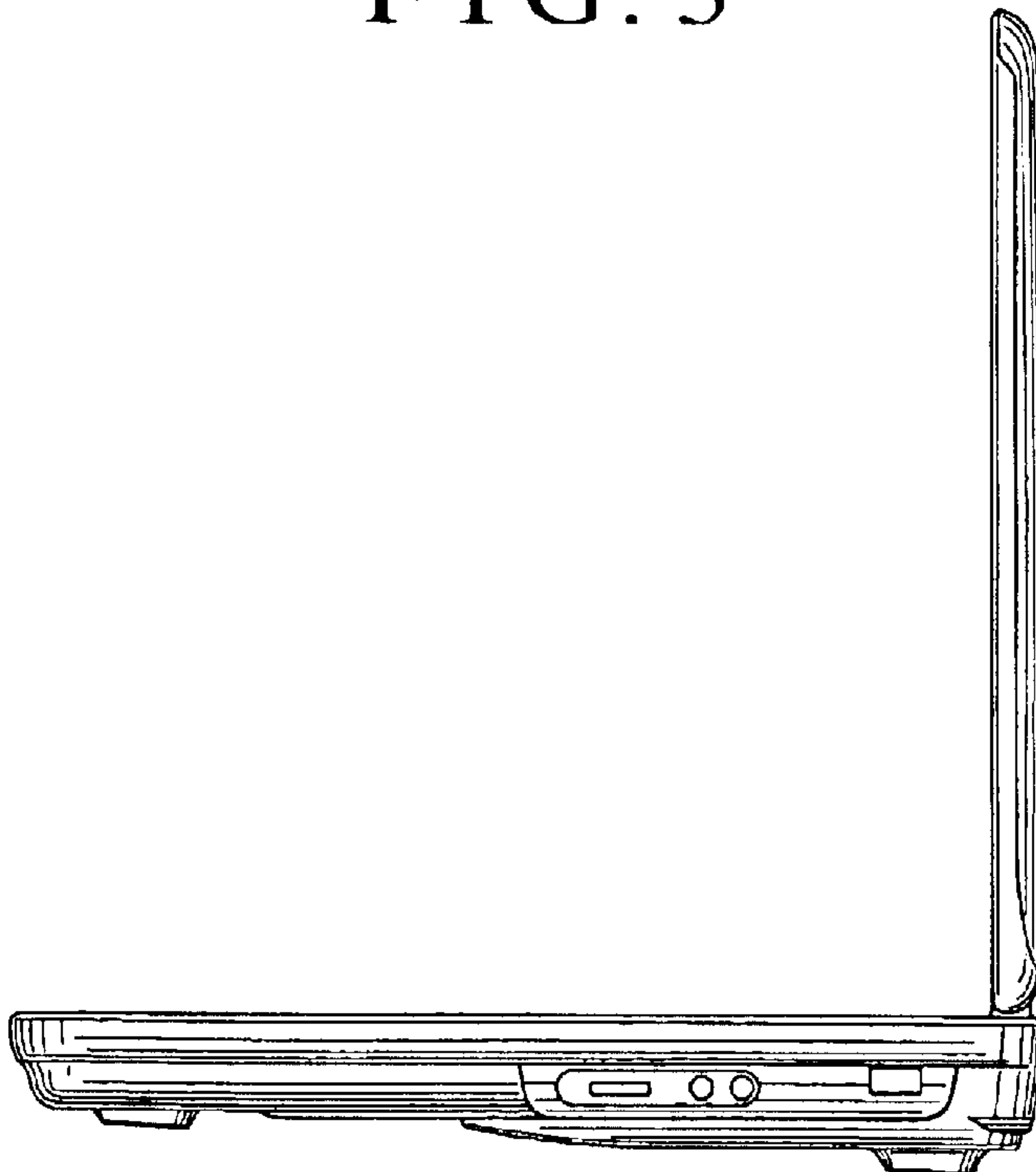


FIG. 6

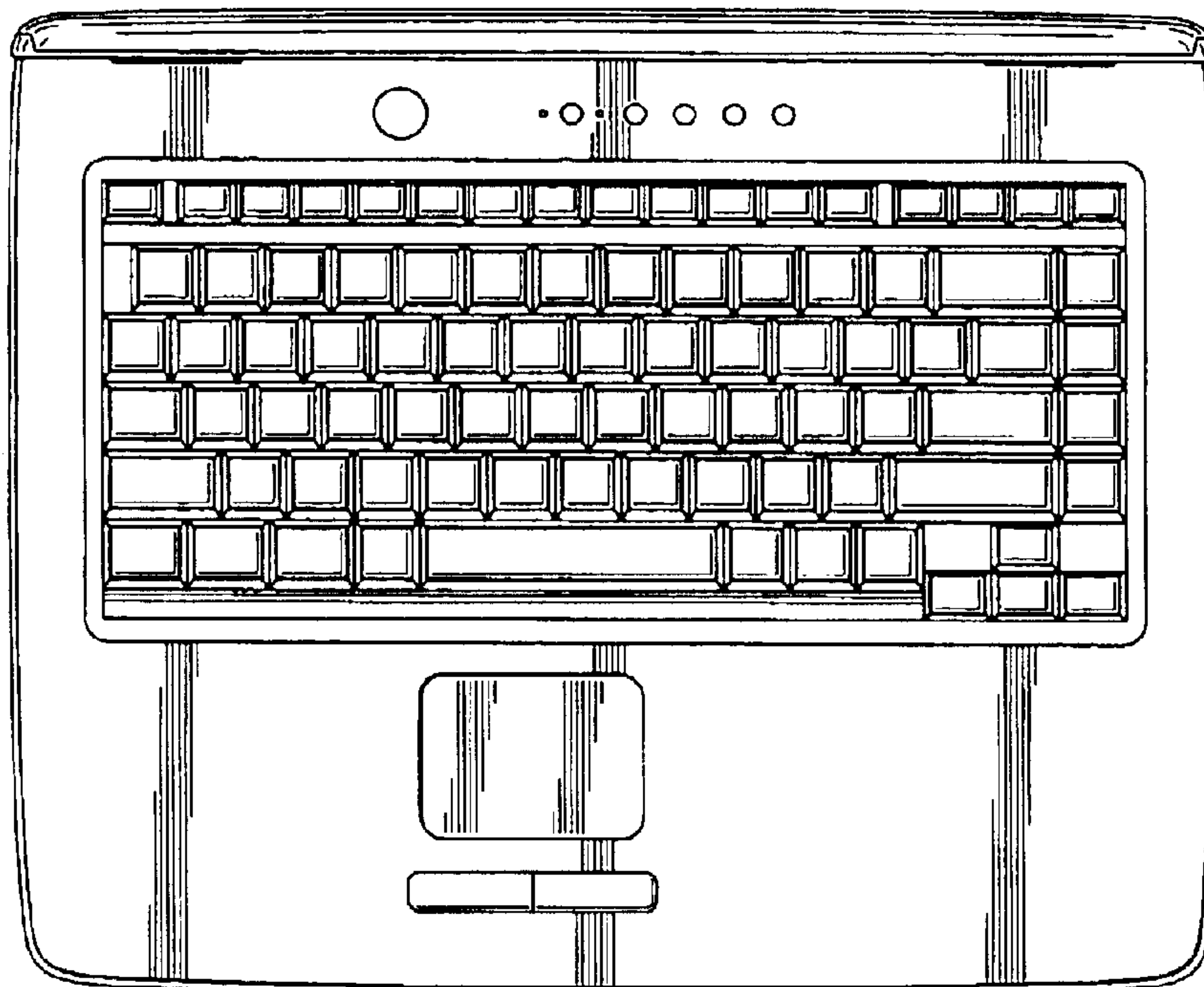


FIG. 7

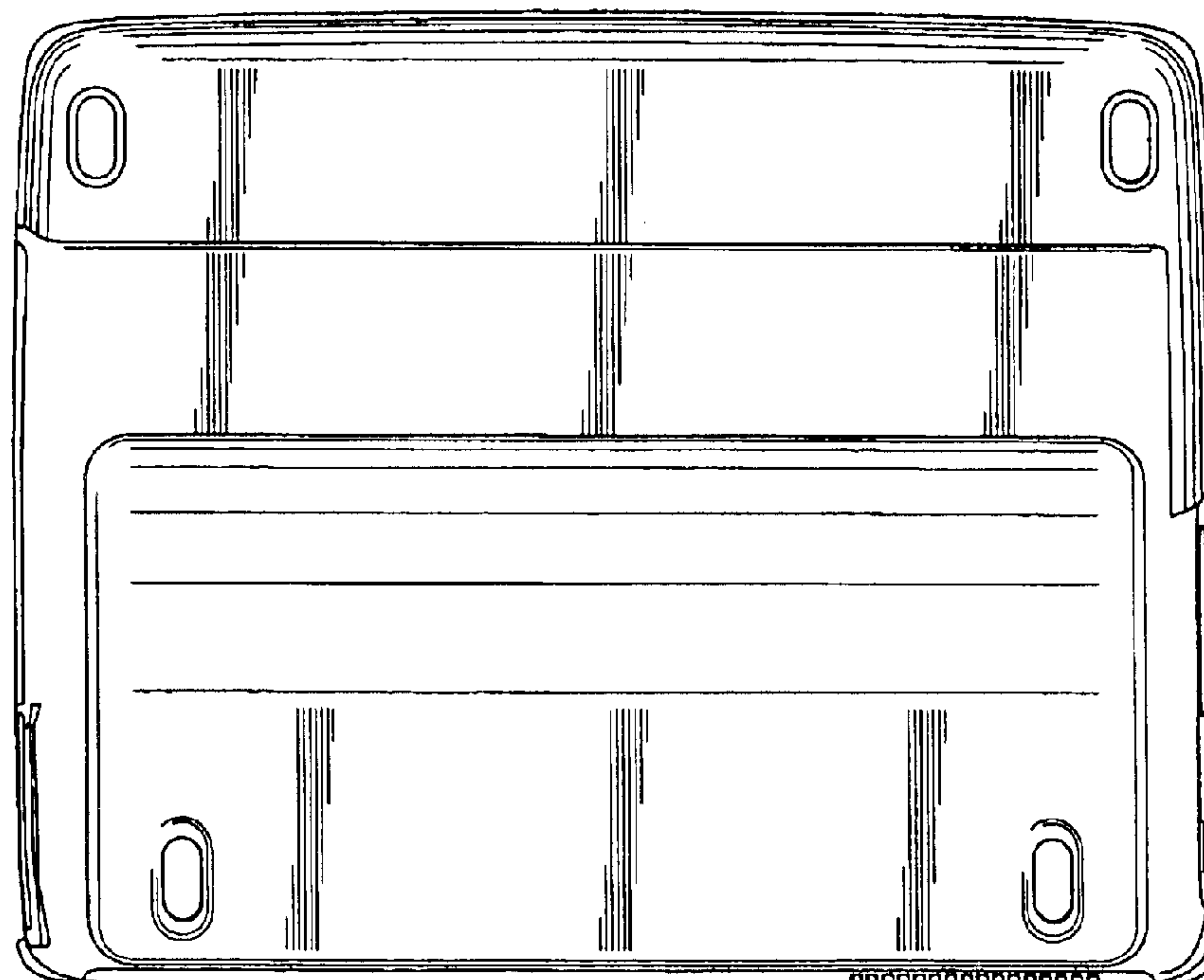


FIG. 8

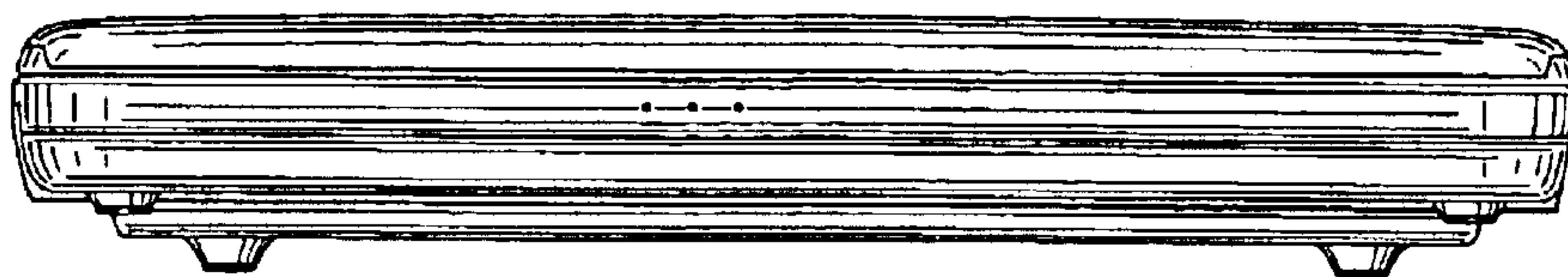


FIG. 9

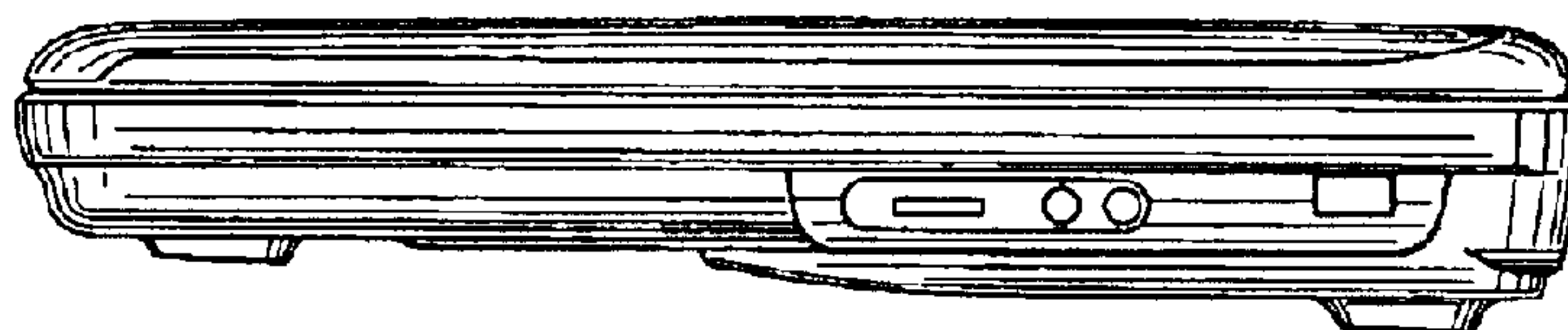


FIG. 10

