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(12) **United States Design Patent**  
**Kurokawa**

(10) **Patent No.:** **US D499,726 S**  
(45) **Date of Patent:** **\*\* Dec. 14, 2004**

- (54) **ELECTRONIC COMPUTER**
- (75) Inventor: **Tadashi Kurokawa**, Setagaya-ku (JP)
- (73) Assignee: **Kabushiki Kaisha Toshiba** (JP)
- (\*\*) Term: **14 Years**
- (21) Appl. No.: **29/200,304**
- (22) Filed: **Feb. 27, 2004**
- (30) **Foreign Application Priority Data**  
Dec. 26, 2003 (JP) ..... 2003-038547
- (51) **LOC (7) Cl.** ..... **14-02**
- (52) **U.S. Cl.** ..... **D14/318**
- (58) **Field of Search** ..... D14/318-322;  
D18/1, 2, 7; 235/145 A, 145 R; 341/22,  
23; 345/104, 156, 168, 169, 173, 538; 361/680-686,  
690-696; 400/486, 489

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*Primary Examiner*—Freda S. Nunn

(74) *Attorney, Agent, or Firm*—Banner & Witcoff, Ltd.

(57) **CLAIM**

The ornamental design for an electronic computer, as shown and described.

**DESCRIPTION**

FIG. 1 is a reduced top, front and right side perspective view of an electronic computer showing my new design, FIG. 2 is a front elevational view thereof, FIG. 3 is a rear elevational view thereof, FIG. 4 is a left side elevational view thereof, FIG. 5 is a right side elevational view thereof, FIG. 6 is a top plan view thereof, FIG. 7 is a bottom plan view thereof, FIG. 8 is a front elevational view showing the article in a closed position thereof, FIG. 9 is a right side elevational view showing the article in the closed position thereof; and, FIG. 10 is a top plan view showing the article in the closed position thereof.

**1 Claim, 5 Drawing Sheets**

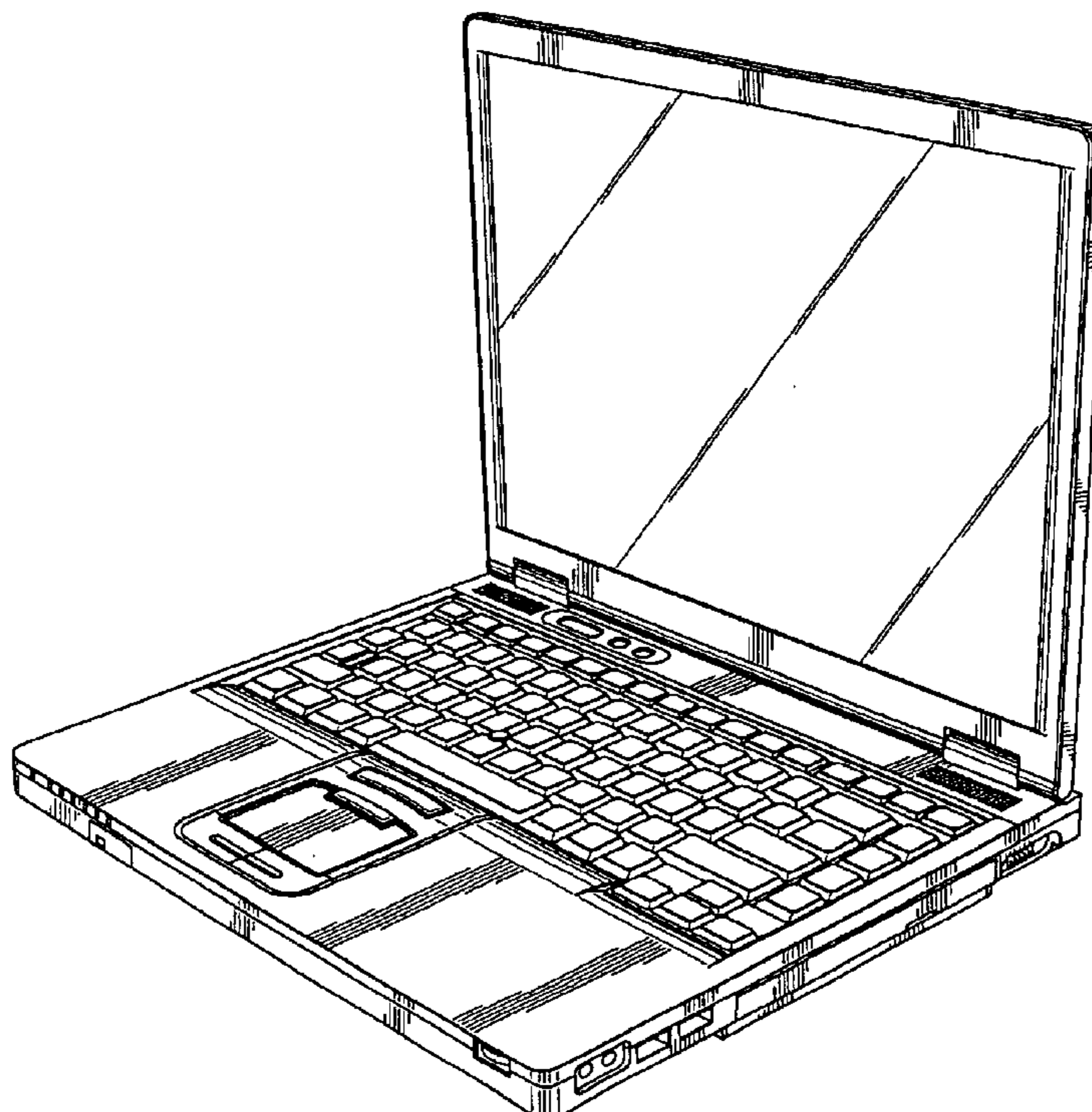


FIG. 1



FIG. 2

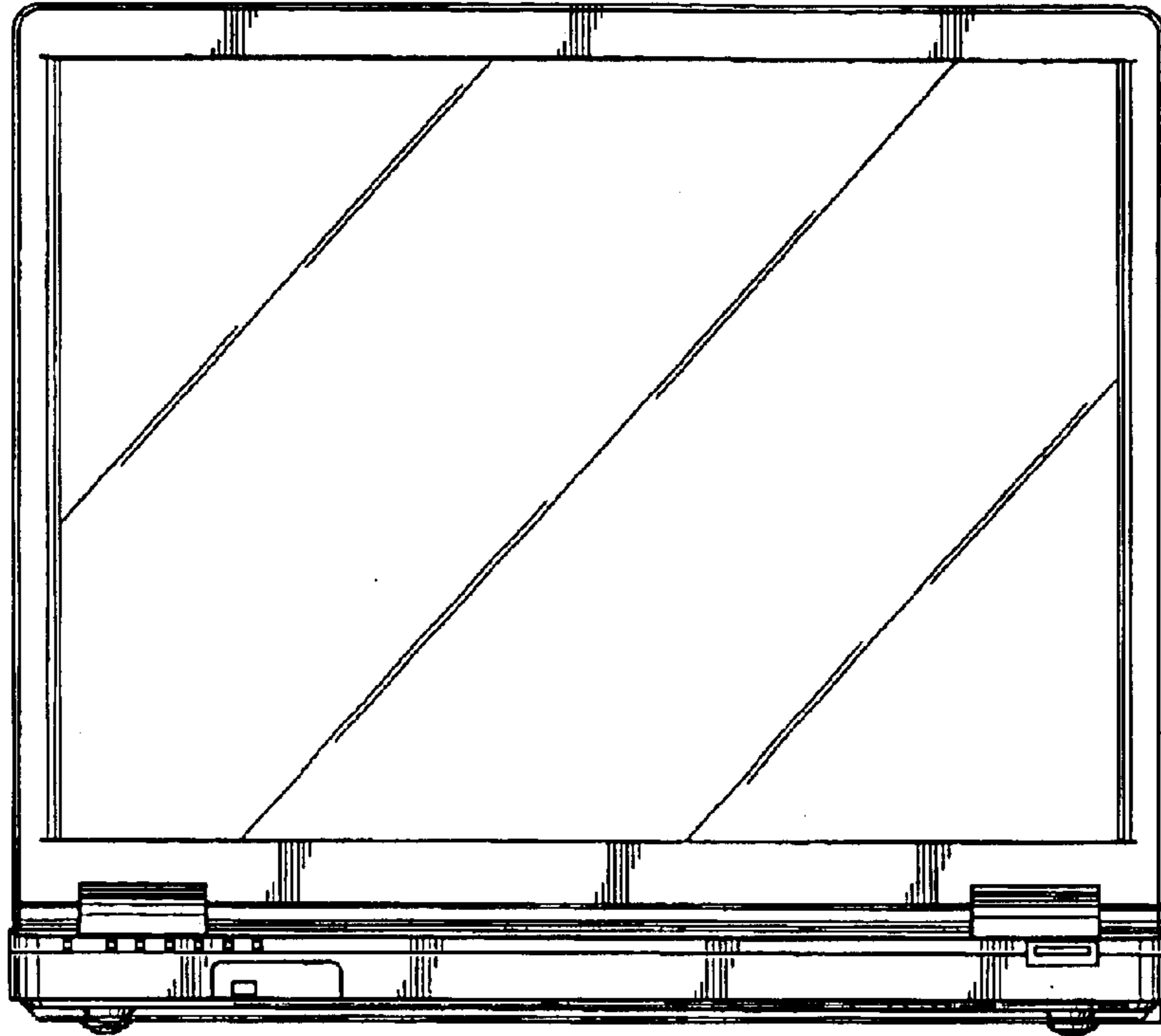


FIG. 3

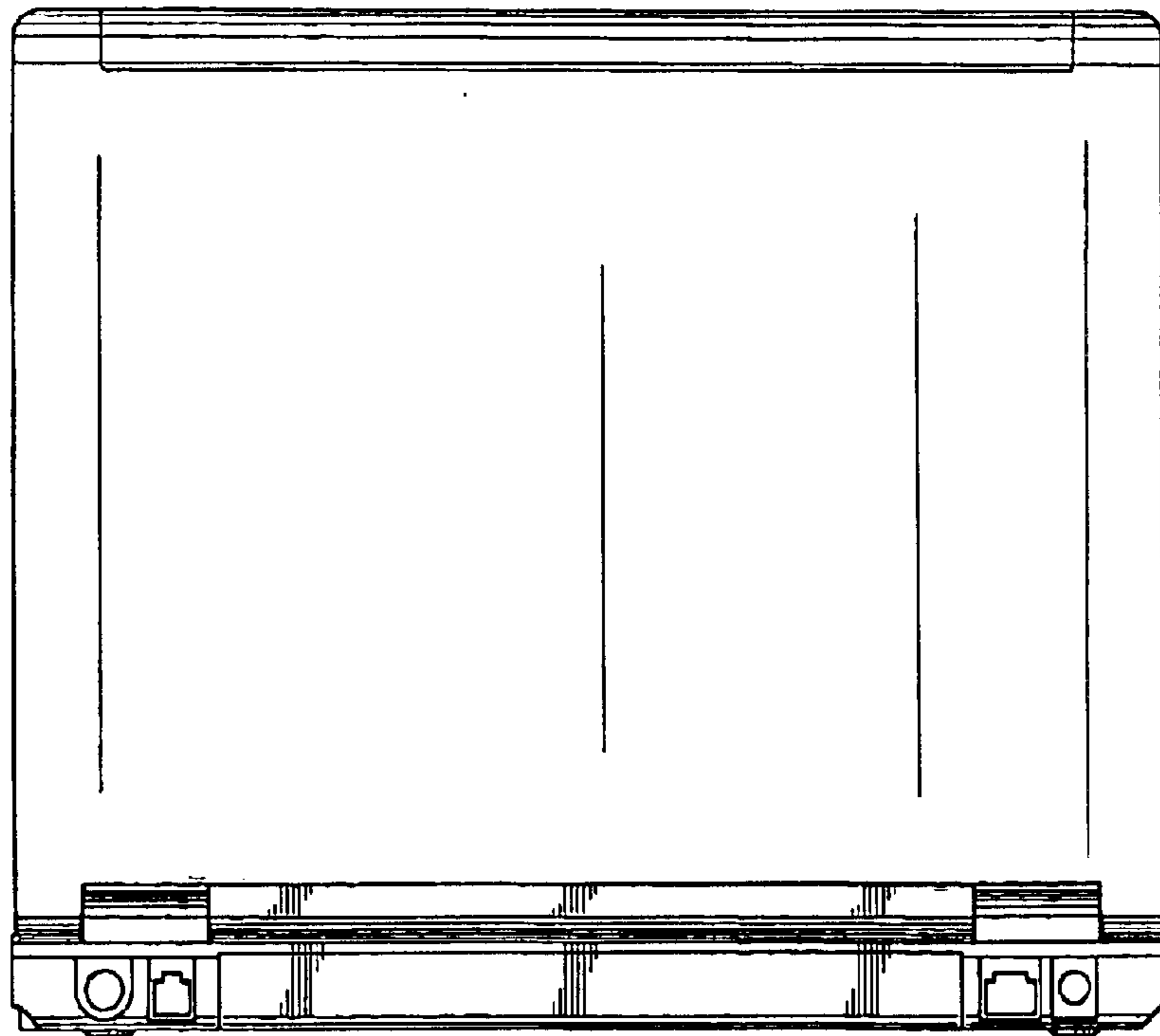


FIG. 4

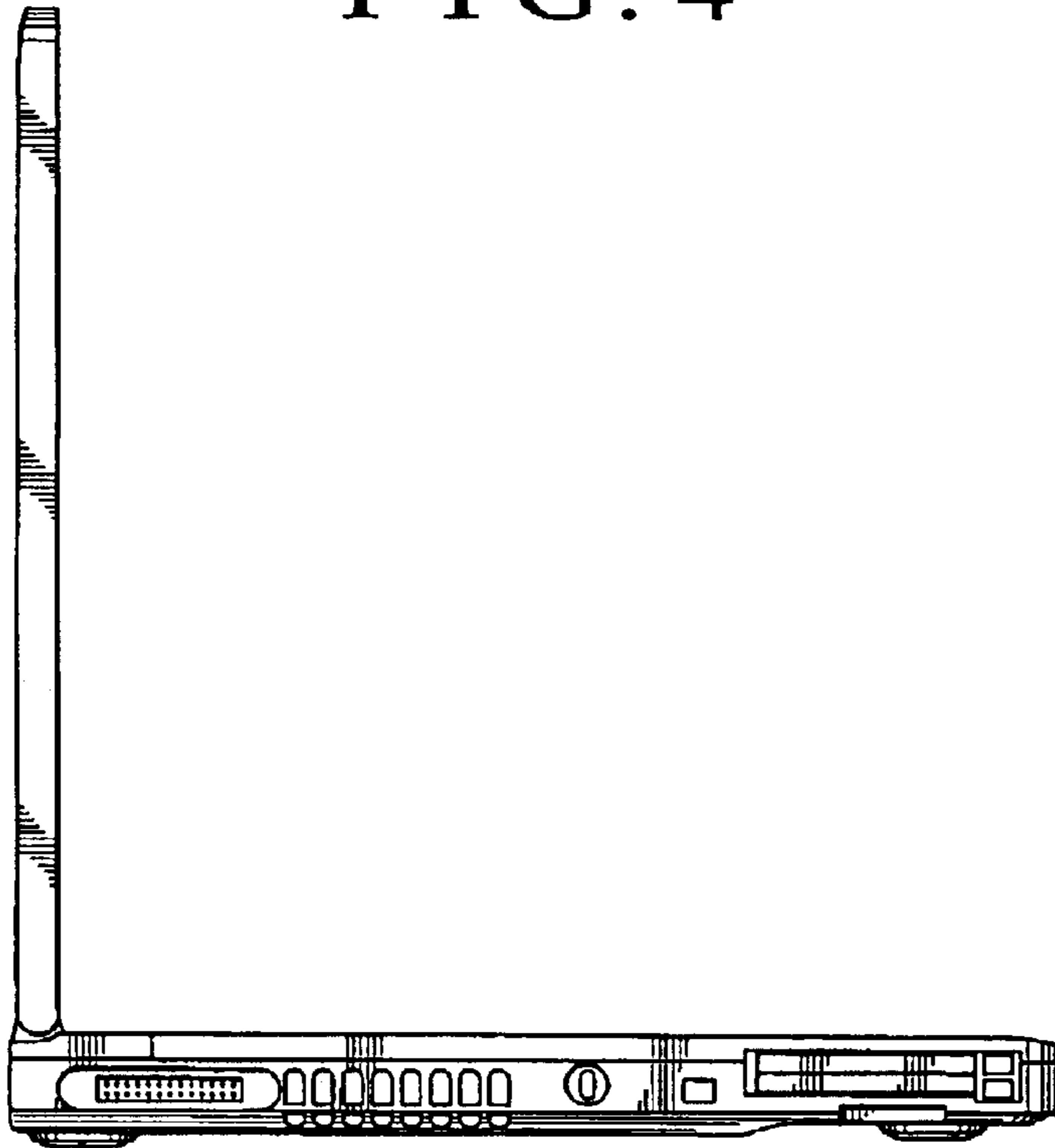


FIG. 5

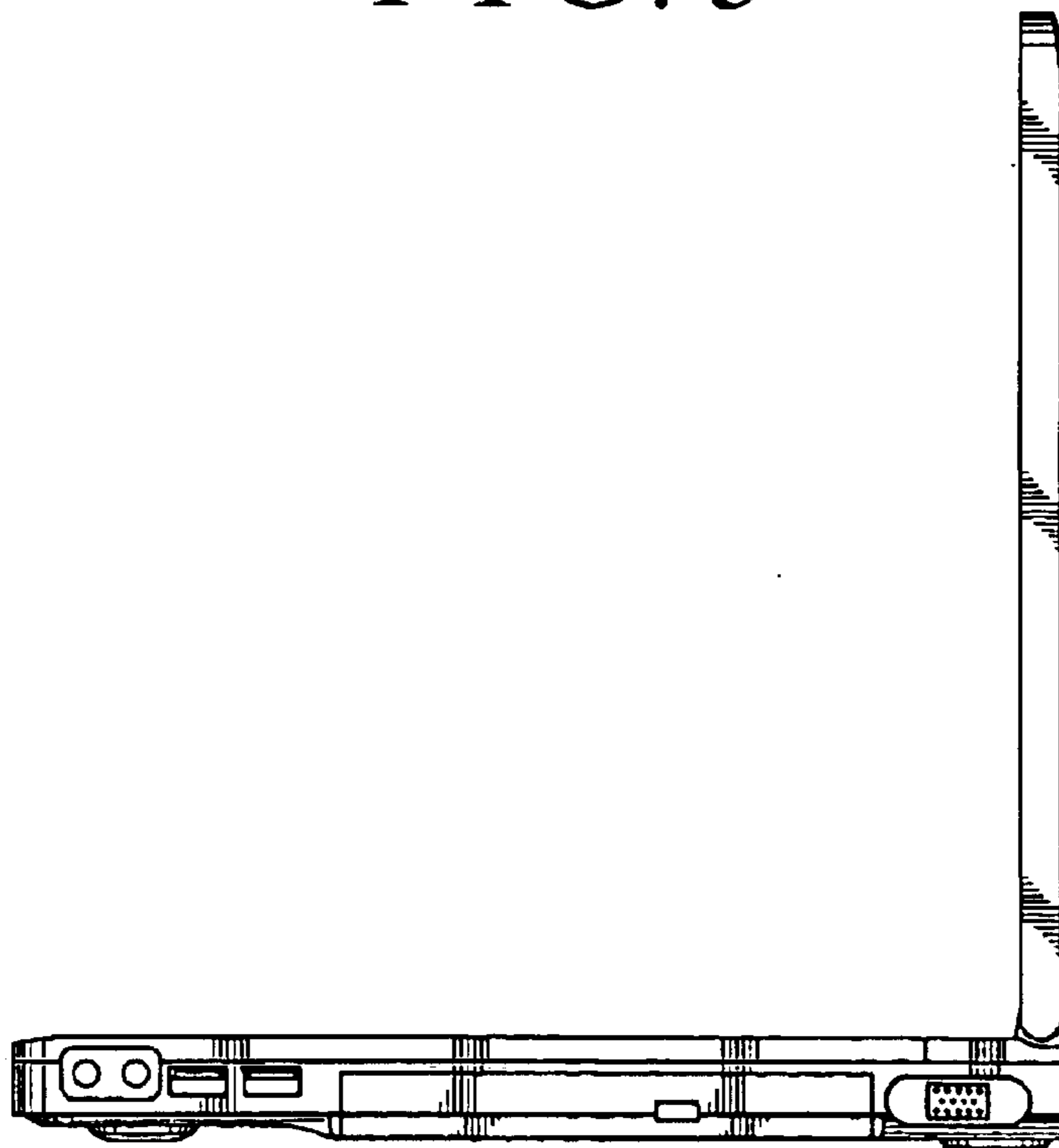


FIG. 6

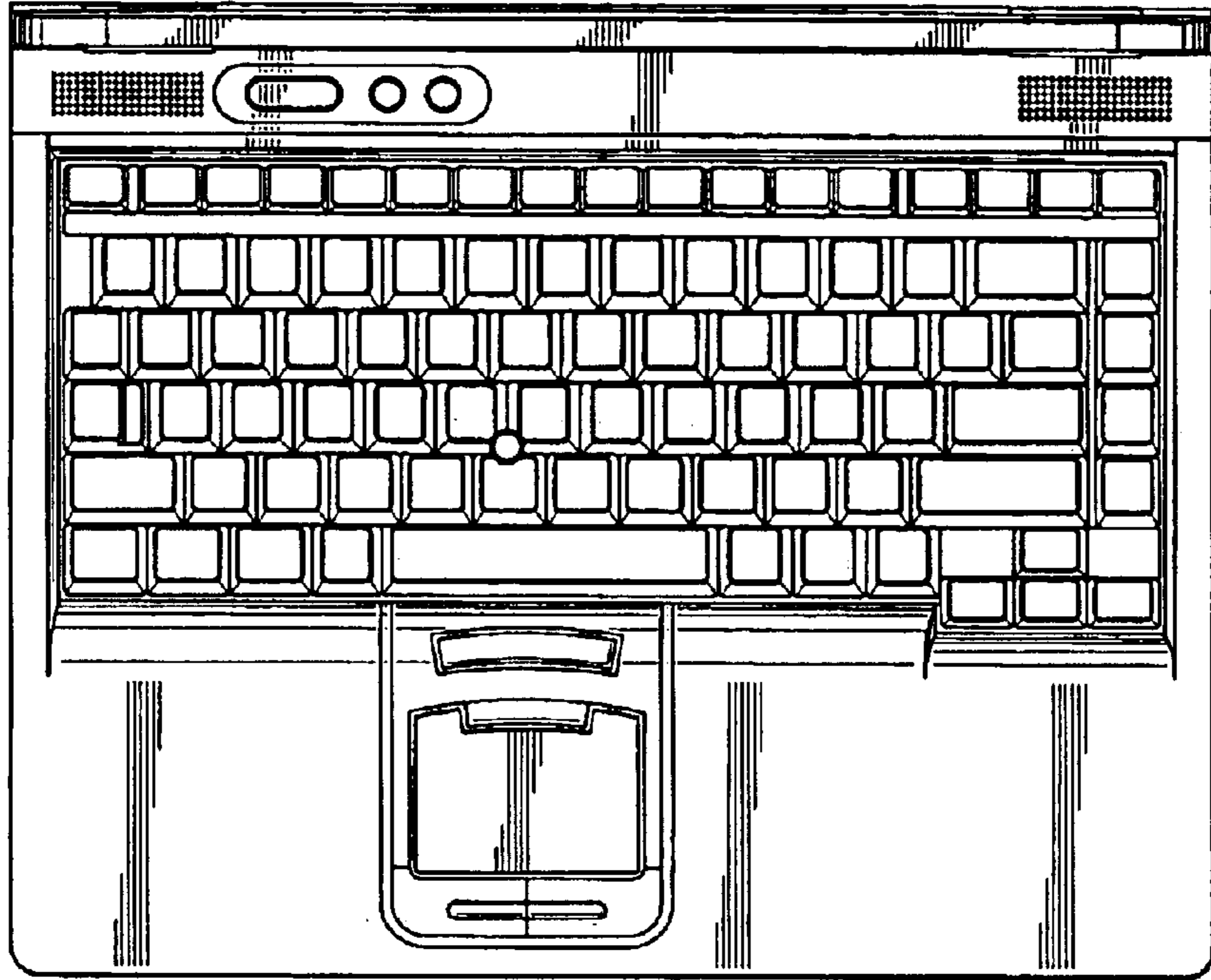


FIG. 7

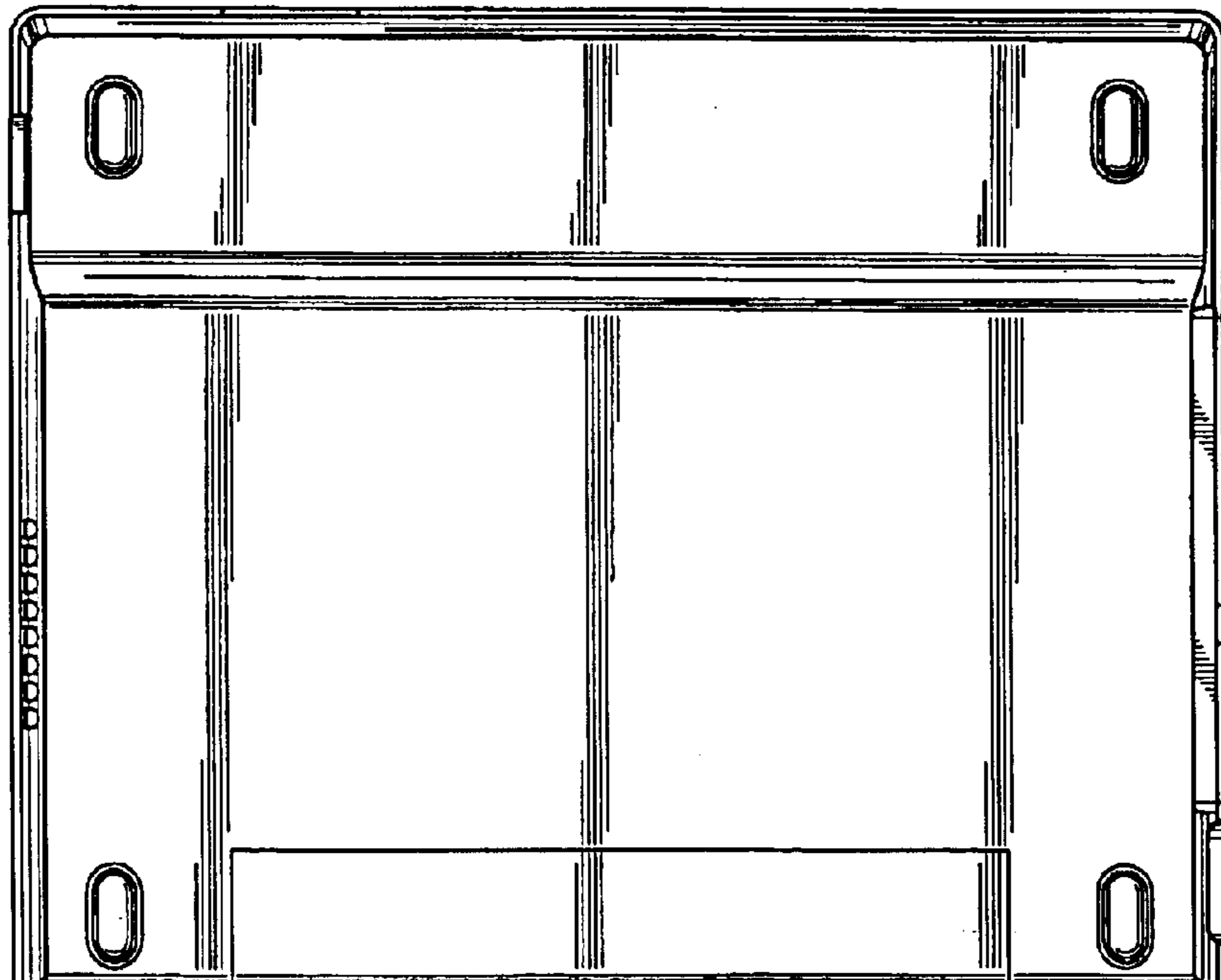


FIG. 8

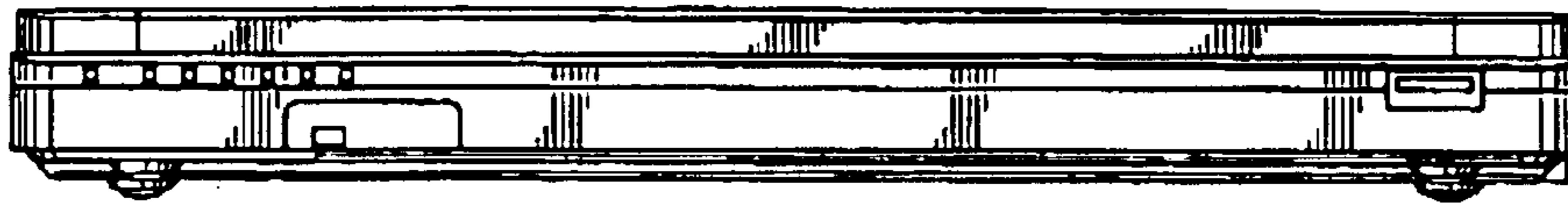


FIG. 9

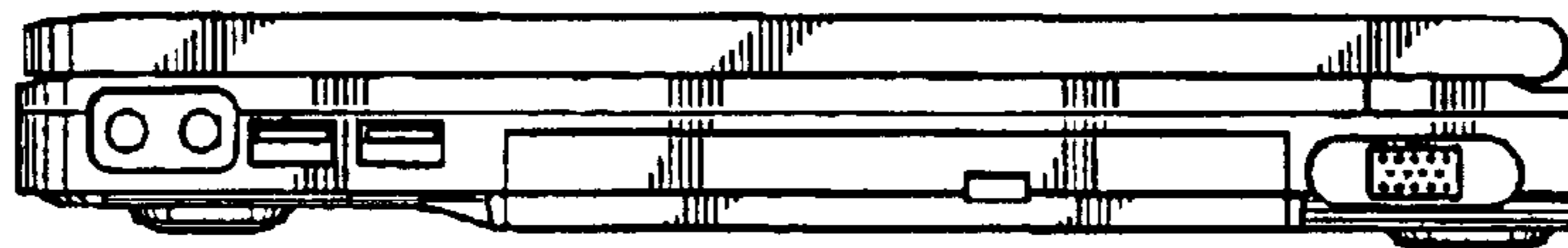


FIG. 10

