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(12) **United States Design Patent** (10) **Patent No.:** **US D496,037 S**
Ooura et al. (45) **Date of Patent:** **** Sep. 14, 2004**

(54) **MEMORY CONTROL EQUIPMENT FOR COMPUTERS**

(75) Inventors: **Norihiro Ooura**, Tokyo (JP); **Shunji Ishida**, Tokyo (JP); **Tetsuya Nagata**, Tokyo (JP)

(73) Assignee: **NEC Corporation**, Tokyo (JP)

(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (7) Cl.** **14-02**

(52) **U.S. Cl.** **D14/356**

(58) **Field of Search** D14/356, 358;
361/684, 685

(56) **References Cited**

U.S. PATENT DOCUMENTS

D314,189 S * 1/1991 Horie D14/308
D376,138 S * 12/1996 Moon D14/358
D389,809 S * 1/1998 Tirrell D13/184

D418,492 S * 1/2000 Nagase D14/311
D436,352 S * 1/2001 Smith D14/240
D438,205 S * 2/2001 Tanaka D14/356
6,285,563 B1 * 9/2001 Nelson et al. 361/784
6,297,950 B1 * 10/2001 Erwin 361/685
D474,925 S * 5/2003 Macor et al. D6/492
D480,728 S * 10/2003 Tanaka D14/444

* cited by examiner

Primary Examiner—Joel Sincavage

(74) *Attorney, Agent, or Firm*—Sughrue Mion, PLLC

(57) **CLAIM**

The ornamental design for a memory control equipment for computers, as shown.

DESCRIPTION

FIG. 1 is a perspective view of the top, front and right side of a memory control equipment for computers showing our new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a left side elevational view thereof;

FIG. 5 is a right side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

1 Claim, 4 Drawing Sheets

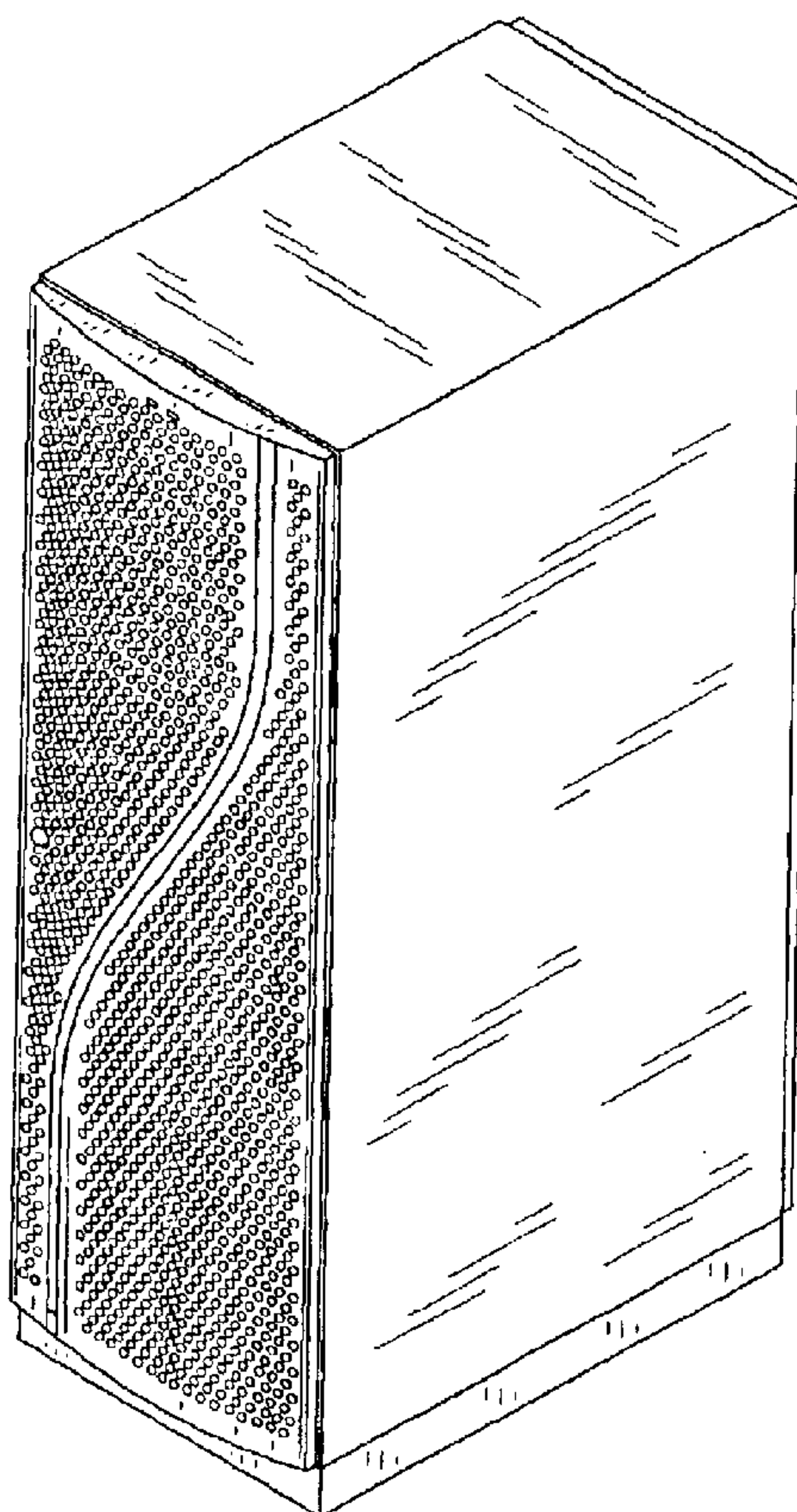


FIG. 1

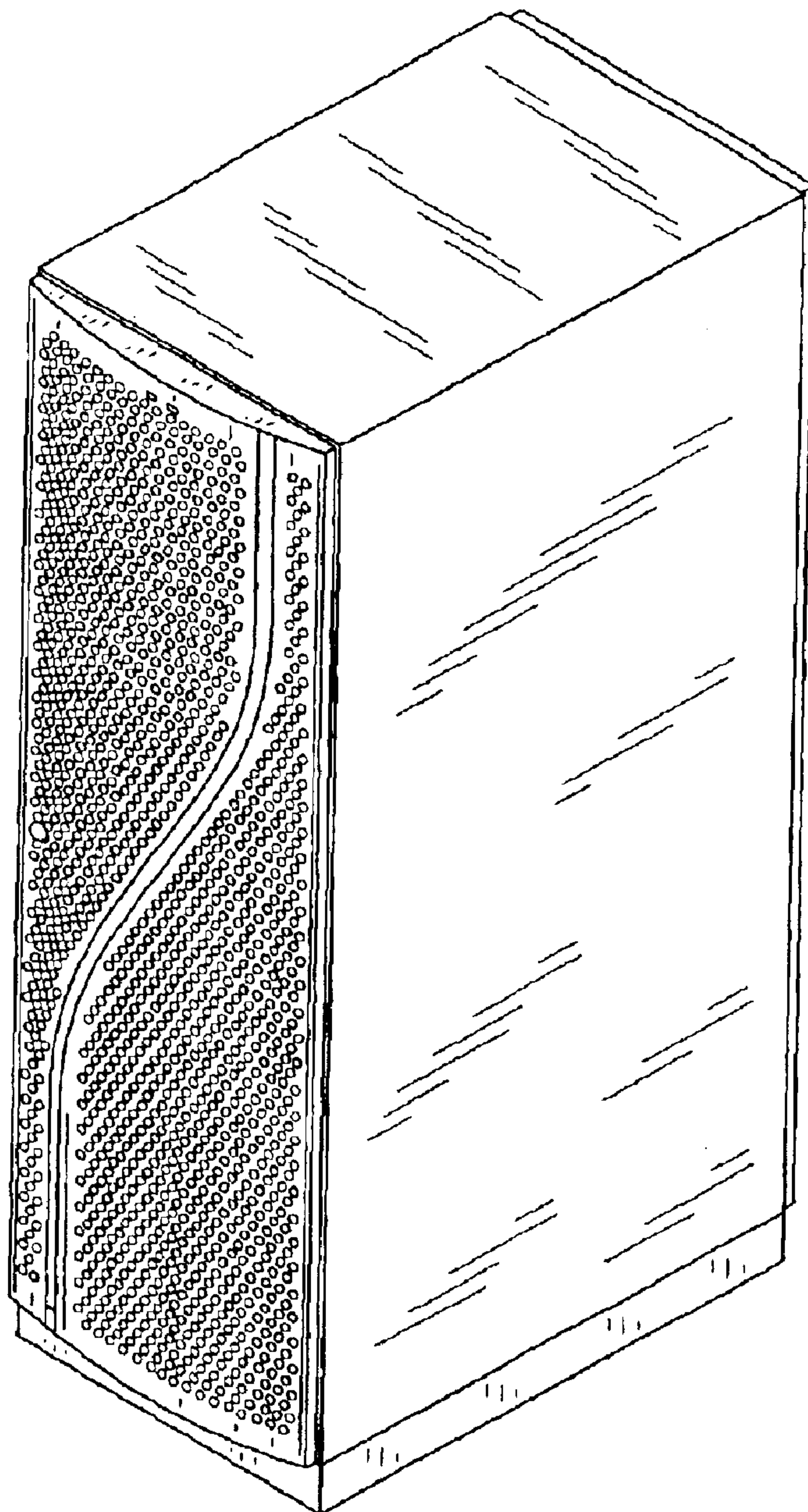


FIG. 3

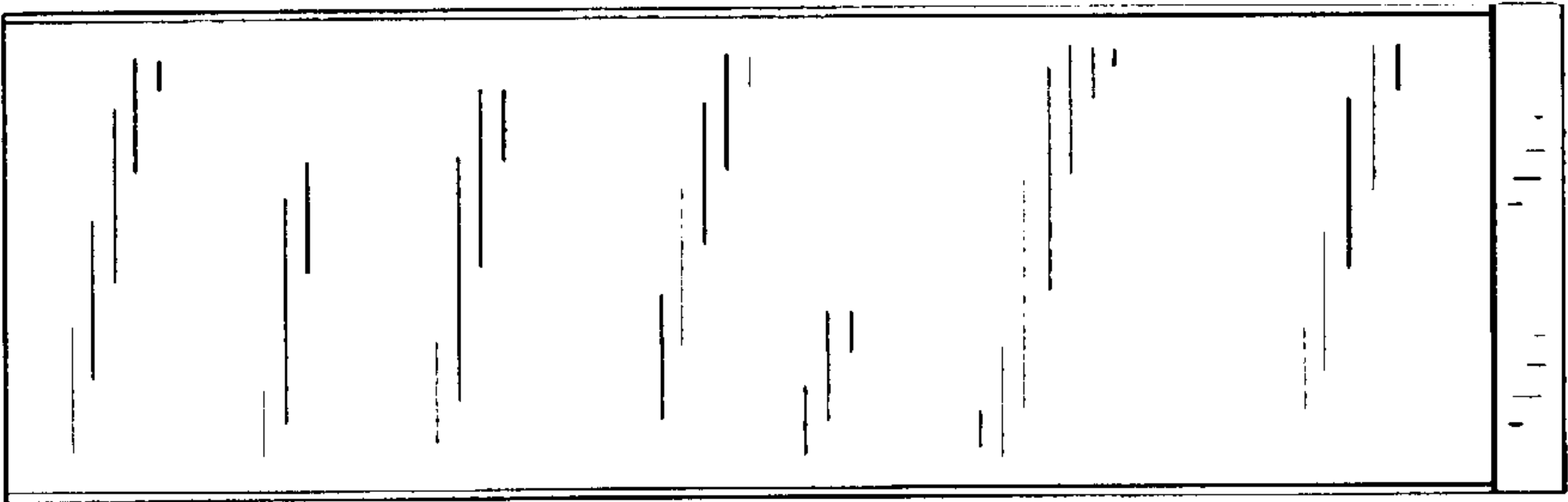


FIG. 2

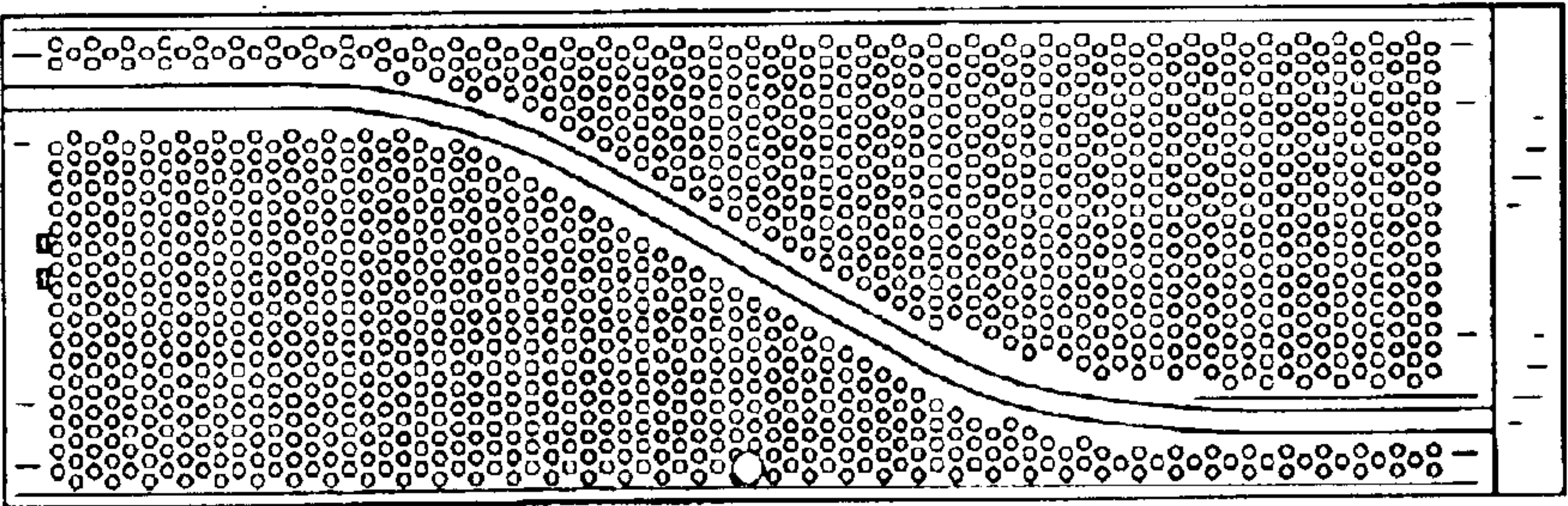


FIG. 5

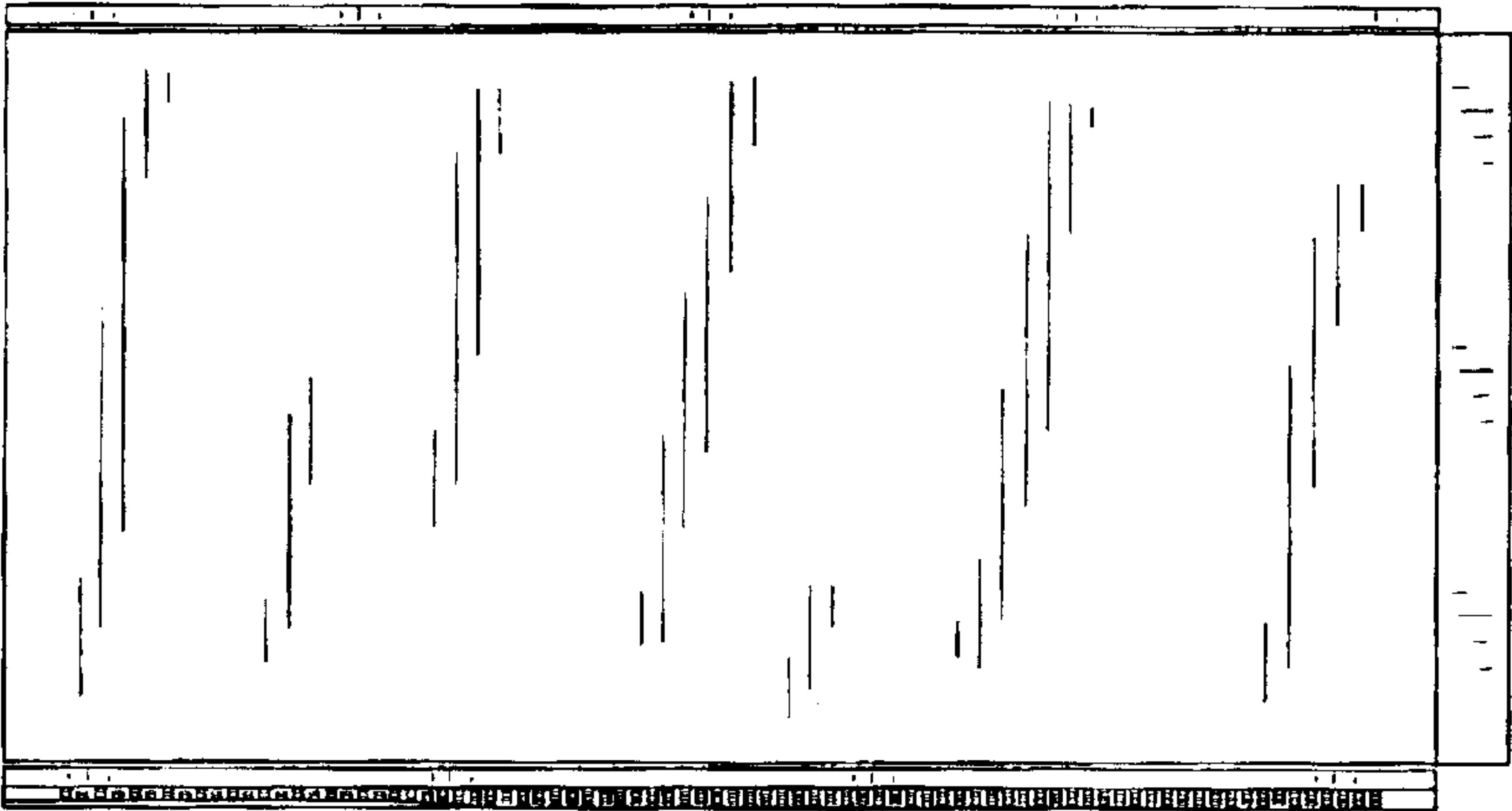


FIG. 4

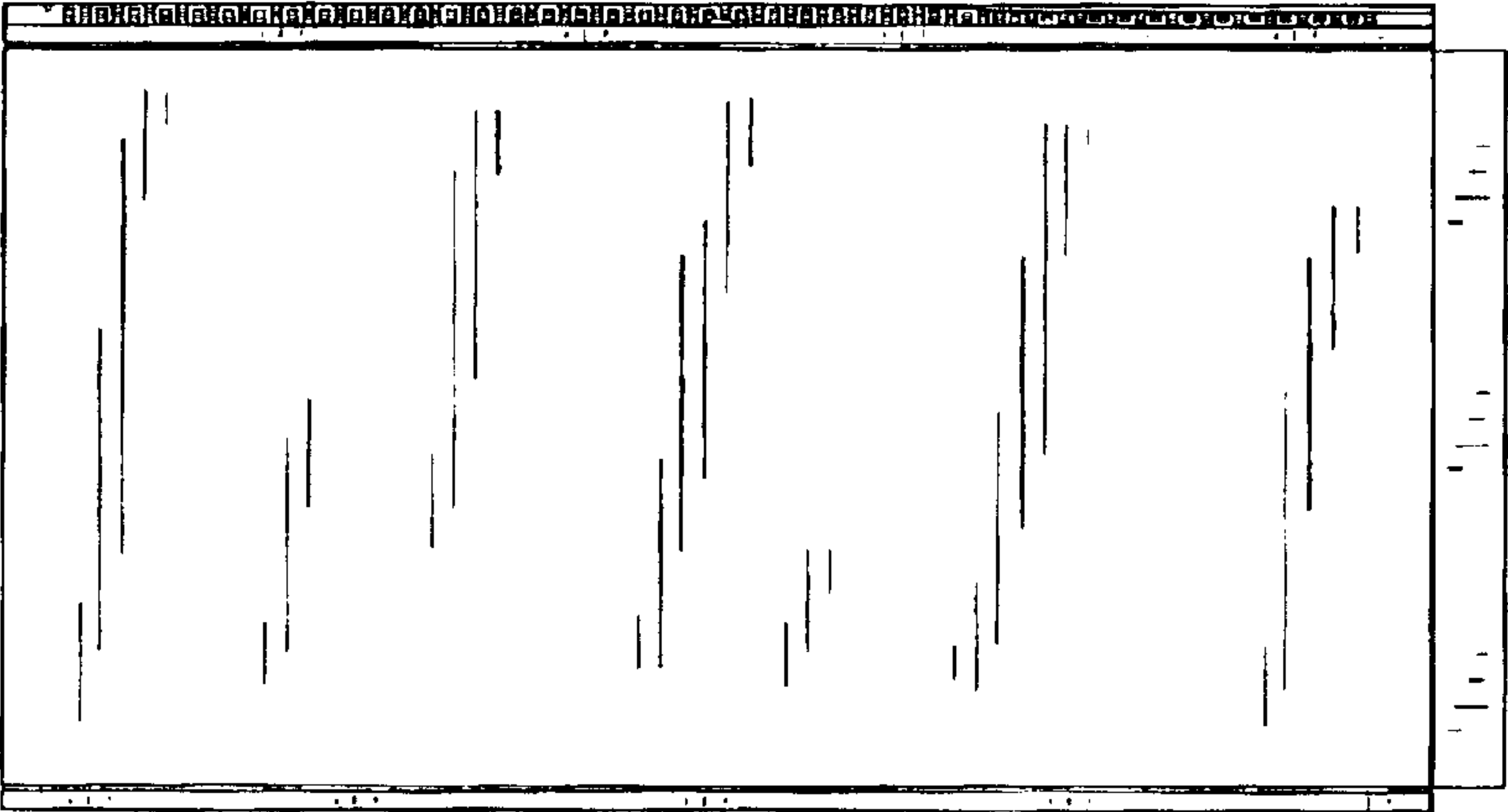


FIG. 6

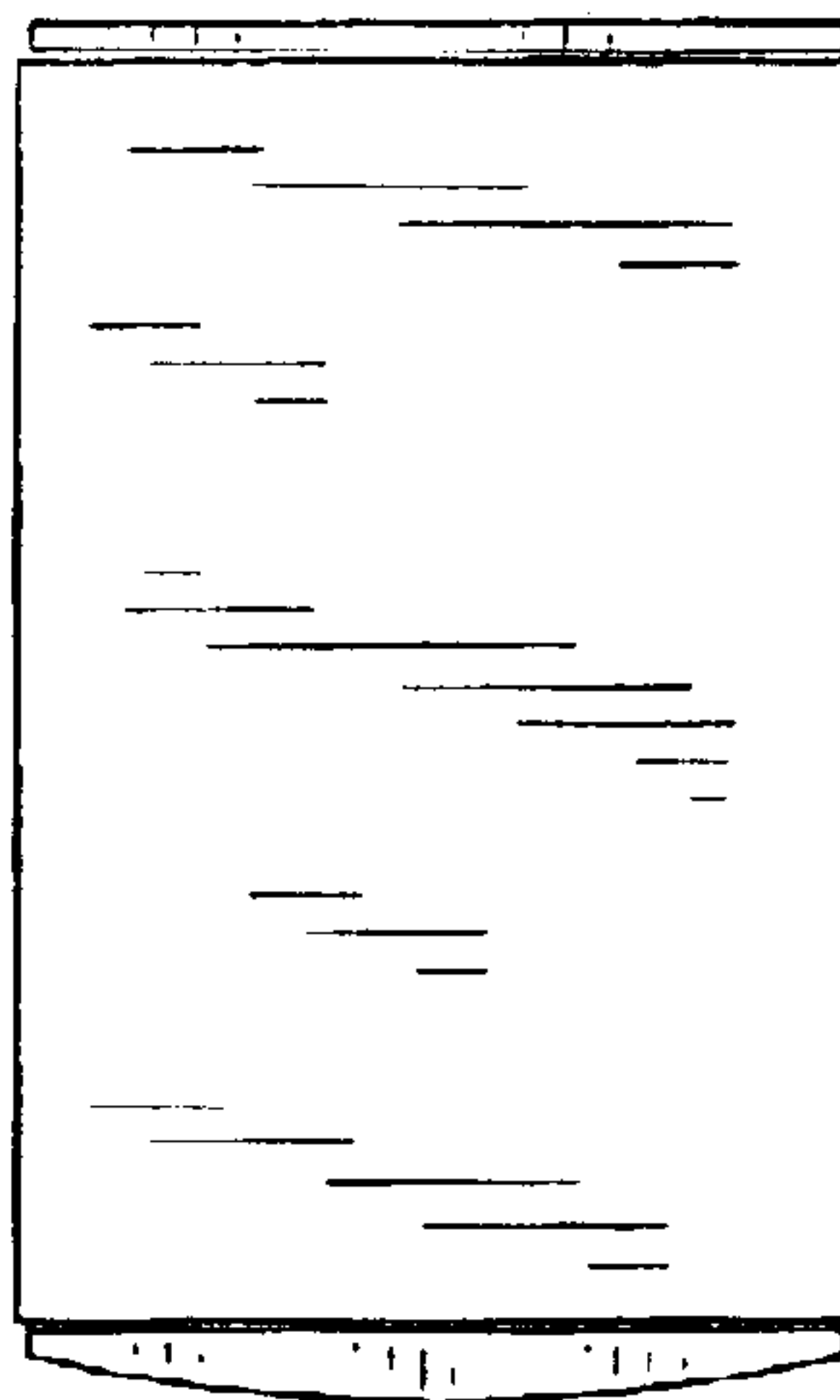


FIG. 7

