

US00D495658S1

(12) **United States Design Patent**
Cho

(10) **Patent No.:** **US D495,658 S**

(45) **Date of Patent:** **** Sep. 7, 2004**

(54) **VOLTAGE REGULATOR**

(76) **Inventor:** **Albert Cho**, 6F, No. 41, Lane 221,
Gangchi Rd., Neihu Chiu, Taipei (TW)

(**) **Term:** **14 Years**

(21) **Appl. No.:** **29/181,039**

(22) **Filed:** **May 5, 2003**

(51) **LOC (7) Cl.** **13-03**

(52) **U.S. Cl.** **D13/137.3; D13/137.2**

(58) **Field of Search** **D13/137.2, 137.3,**
D13/124, 160, 178; 439/650, 651, 652,
653; 361/56, 111

(56) **References Cited**

U.S. PATENT DOCUMENTS

D284,758 S	*	7/1986	Maloney	D13/137.3
4,867,701 A	*	9/1989	Wiand	439/501
D312,615 S	*	12/1990	Schwartz	D13/137.2
D361,315 S	*	8/1995	Wedell et al.	D13/160
D364,379 S	*	11/1995	Pogue et al.	D13/160
D369,784 S	*	5/1996	Pogue et al.	D13/160
D370,458 S	*	6/1996	Fladung et al.	D13/139.8

D390,190 S	*	2/1998	Stekelenburg	D13/139.7
D397,086 S	*	8/1998	Lin	D13/137.2
D433,386 S	*	11/2000	Veino et al.	D13/160
6,406,308 B1	*	6/2002	Wang	439/145

* cited by examiner

Primary Examiner—Philip S. Hyder

(74) *Attorney, Agent, or Firm*—Birch, Stewart, Kolasch & Birch, LLP

(57) **CLAIM**

The ornamental design for a voltage regulator, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a voltage regulator showing my new design;

FIG. 2 is a right side elevational view thereof;

FIG. 3 is a top plan view thereof;

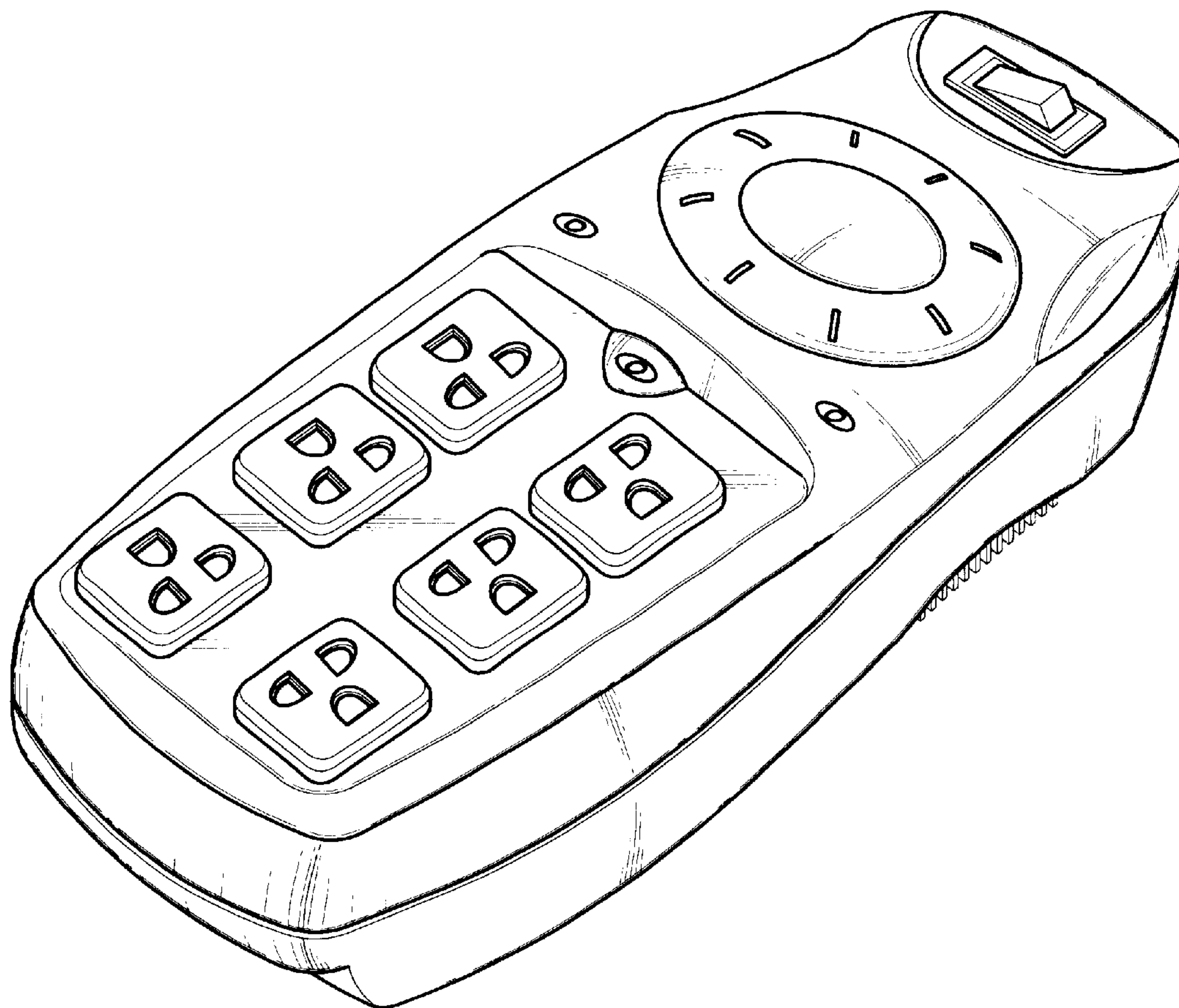
FIG. 4 is a front side elevational view thereof;

FIG. 5 is a bottom plan view thereof;

FIG. 6 is a left side elevational view thereof; and,

FIG. 7 is a rear side elevational view thereof.

1 Claim, 4 Drawing Sheets



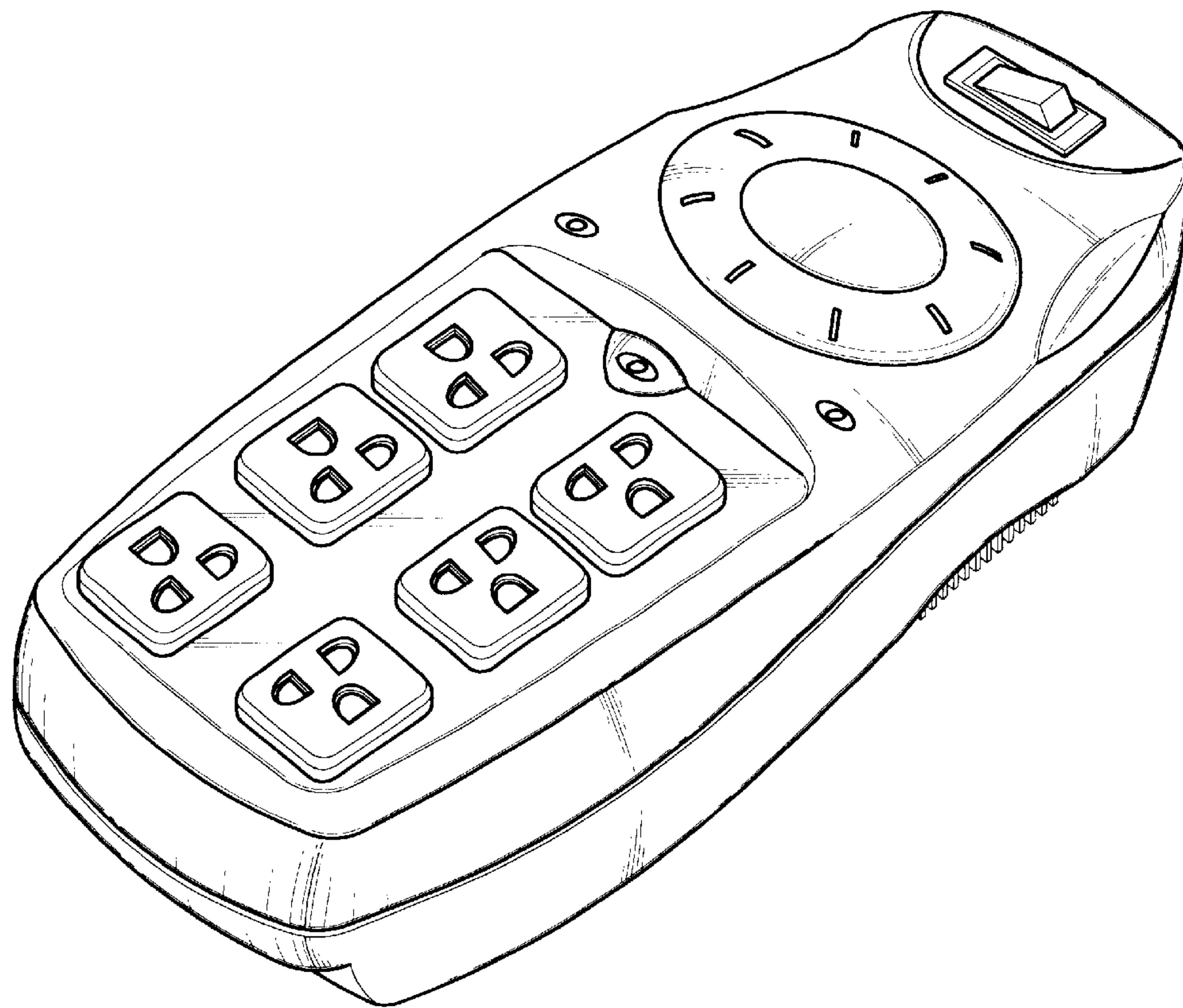


Fig. 1

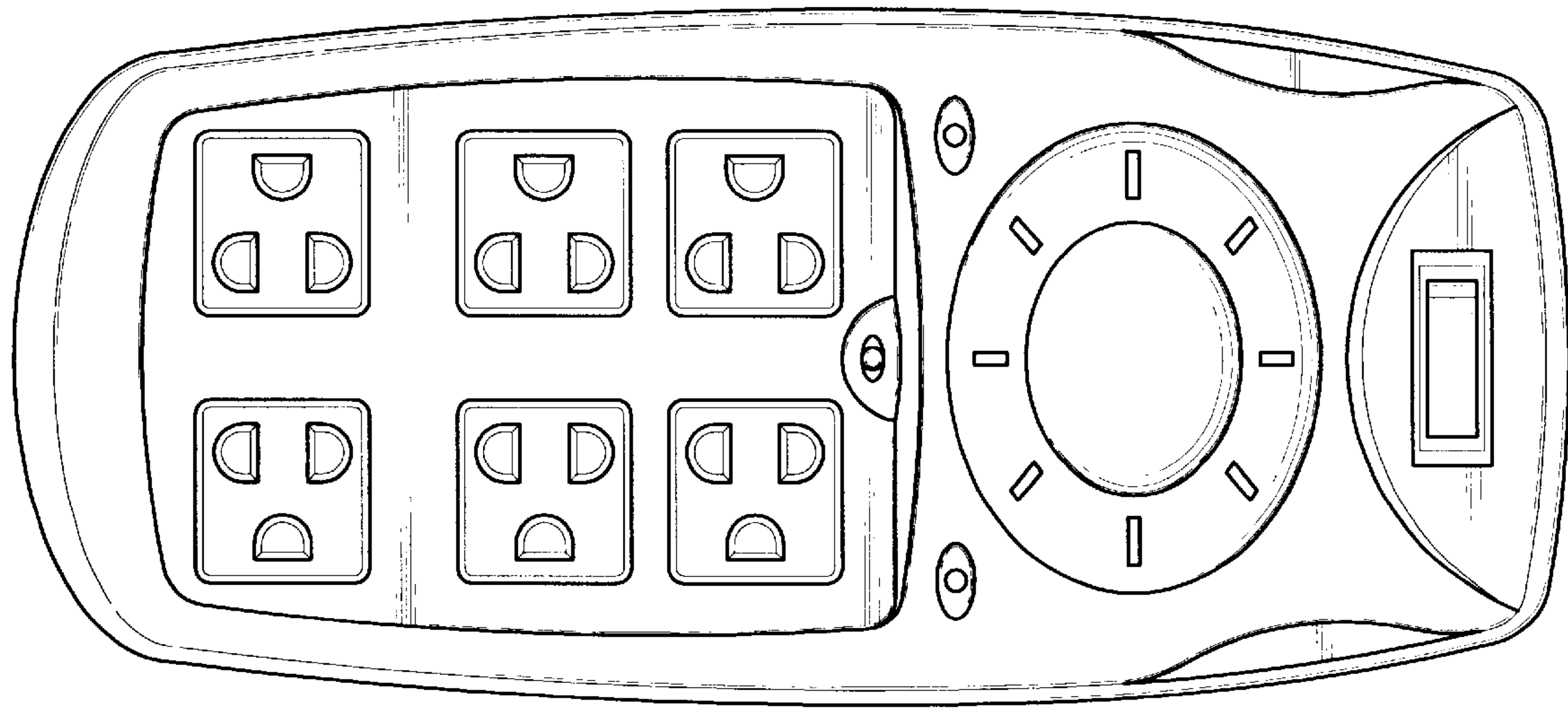


Fig.3

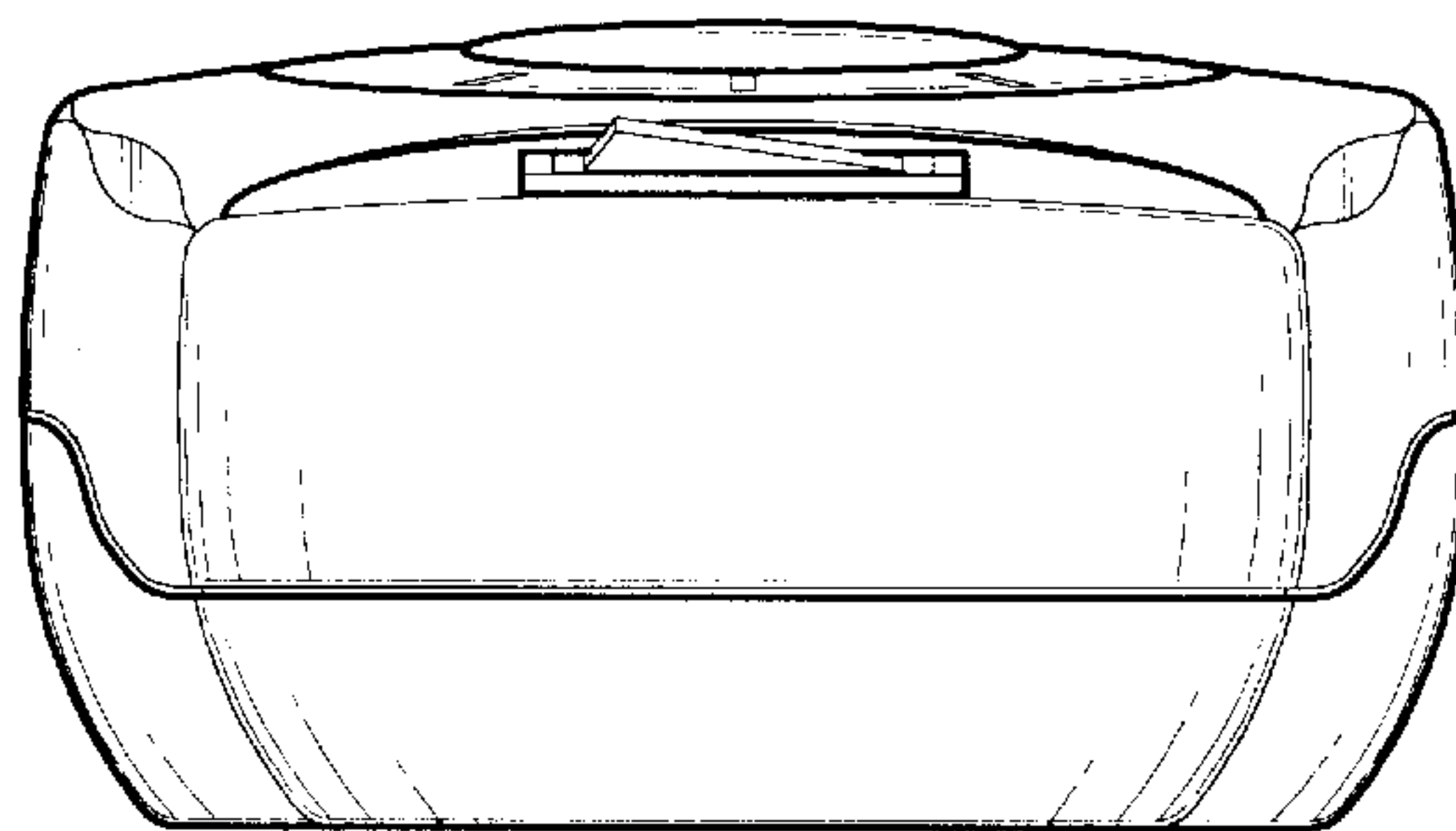


Fig.2

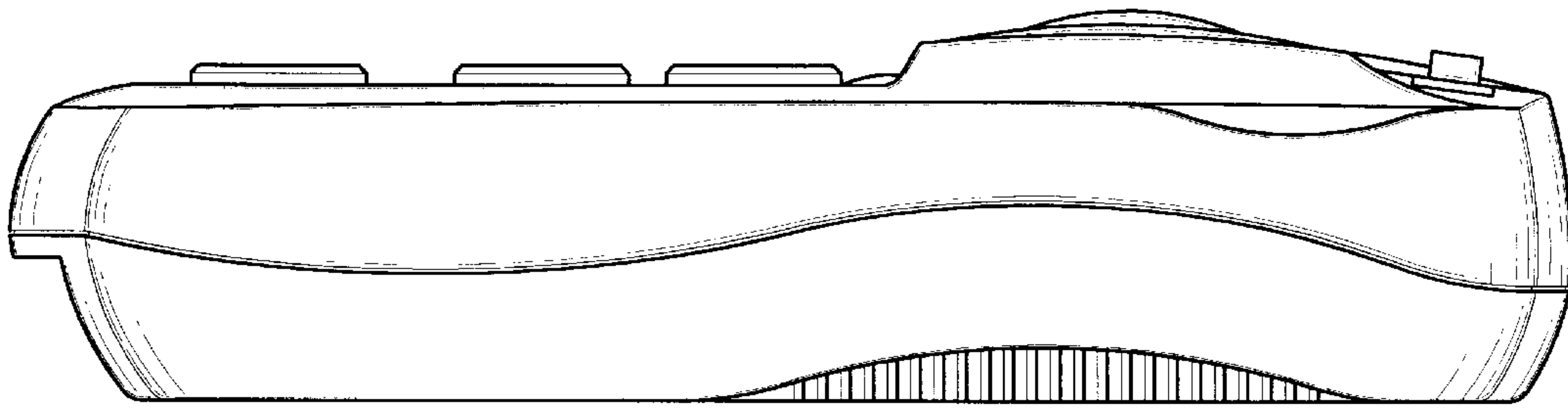


Fig.4

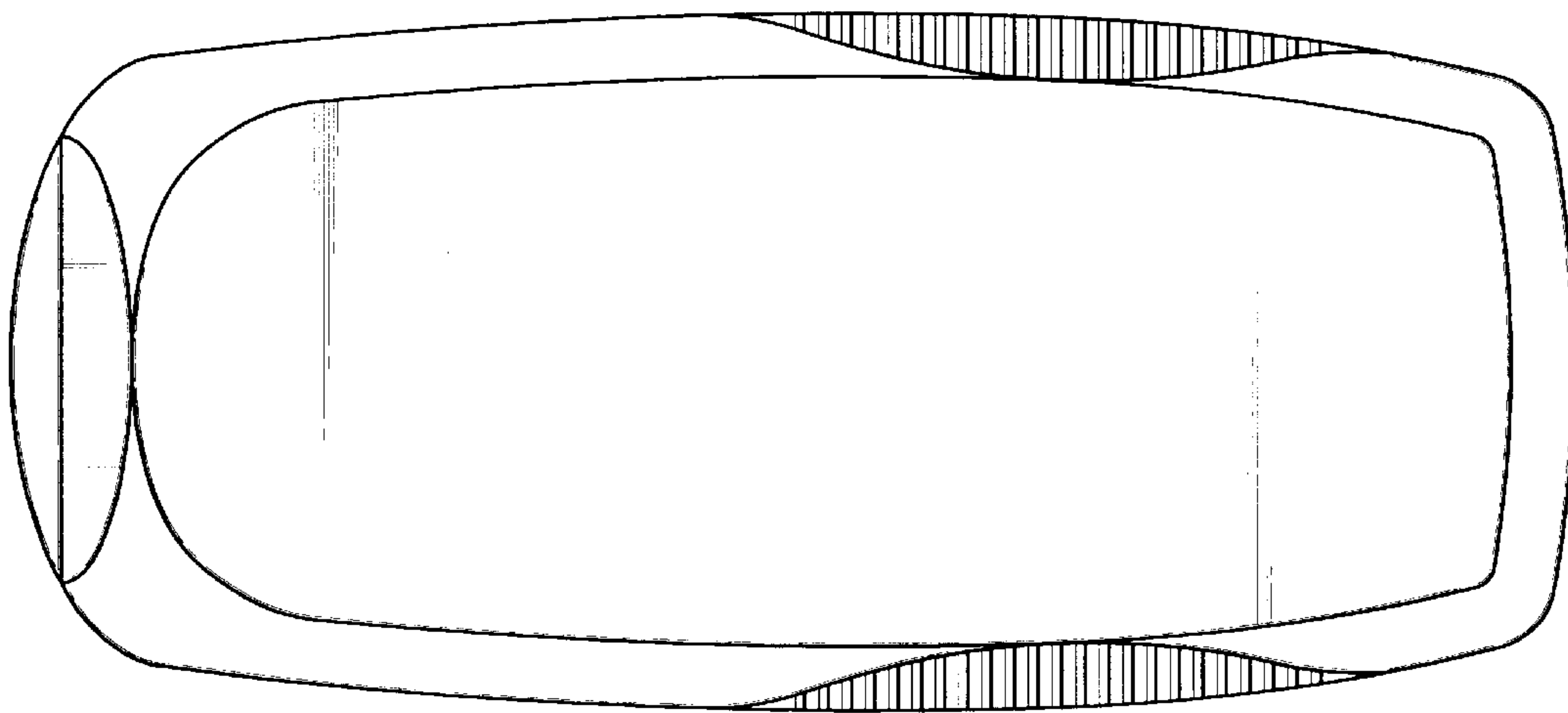


Fig.5

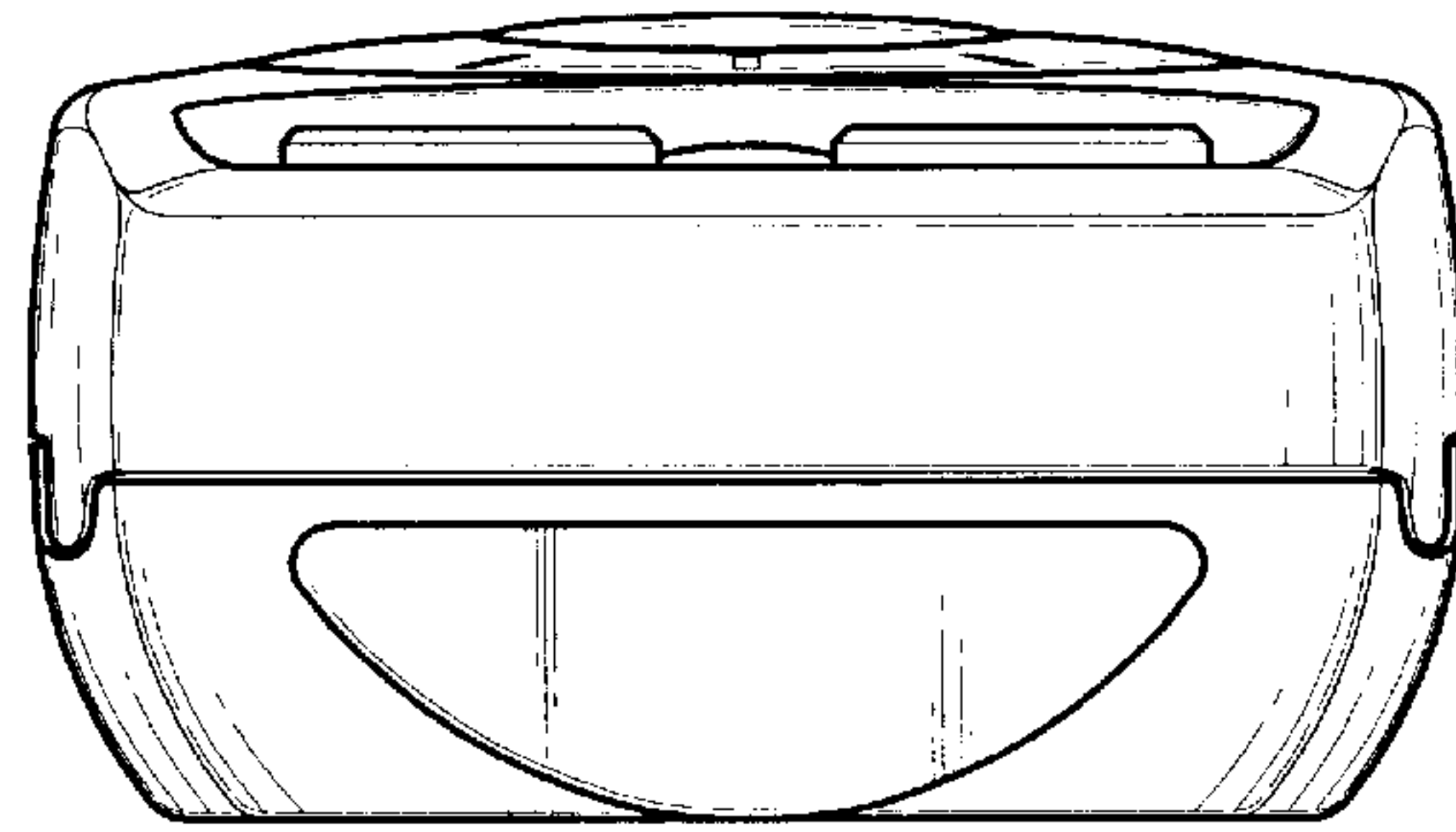


Fig.6

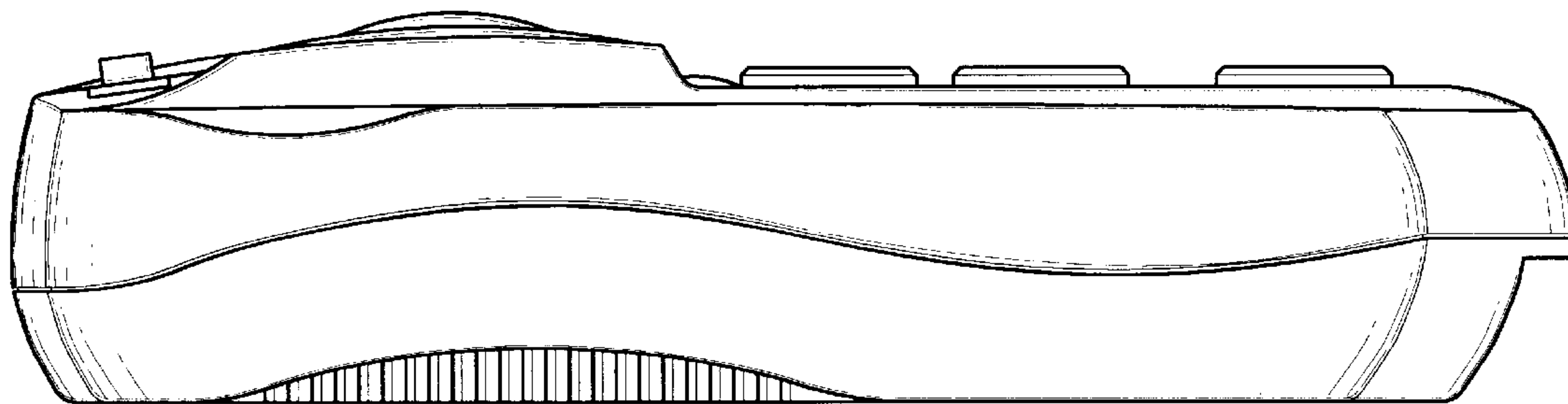


Fig.7