

US00D495586S1

(12) **United States Design Patent** (10) **Patent No.:** **US D495,586 S**  
**Carroll et al.** (45) **Date of Patent:** **\*\* Sep. 7, 2004**

(54) **PRINTED CIRCUIT BOARD DAUGHTER BOARD SUPPORT**

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\* cited by examiner

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(\*\*) Term: **14 Years**

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(21) Appl. No.: **29/198,687**

(57) **CLAIM**

(22) Filed: **Feb. 3, 2004**

We claim the ornamental design for a printed circuit board daughter board support, as shown and described.

**Related U.S. Application Data**

**DESCRIPTION**

(62) Division of application No. 29/172,653, filed on Dec. 16, 2002.

FIG. 1 is a side elevational perspective view of a printed circuit board daughter board support;

(51) **LOC (7) Cl.** ..... **08-05**

FIG. 2 is a front elevational view of a printed circuit board daughter board support;

(52) **U.S. Cl.** ..... **D8/354**

FIG. 3 is a rear elevational view of a printed circuit board daughter board support;

(58) **Field of Search** ..... D8/349, 354, 373,  
D8/380, 382; D13/182, 199; 174/138 D,  
138 G, 260, 261; 211/41.17; 361/719, 760,  
784, 792, 807, 808, 809, 825; 439/377

FIG. 4 is a side elevational view of one side of a printed circuit board daughter board support;

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FIG. 5 is a side elevational view of the opposite side of a printed circuit board daughter board support;

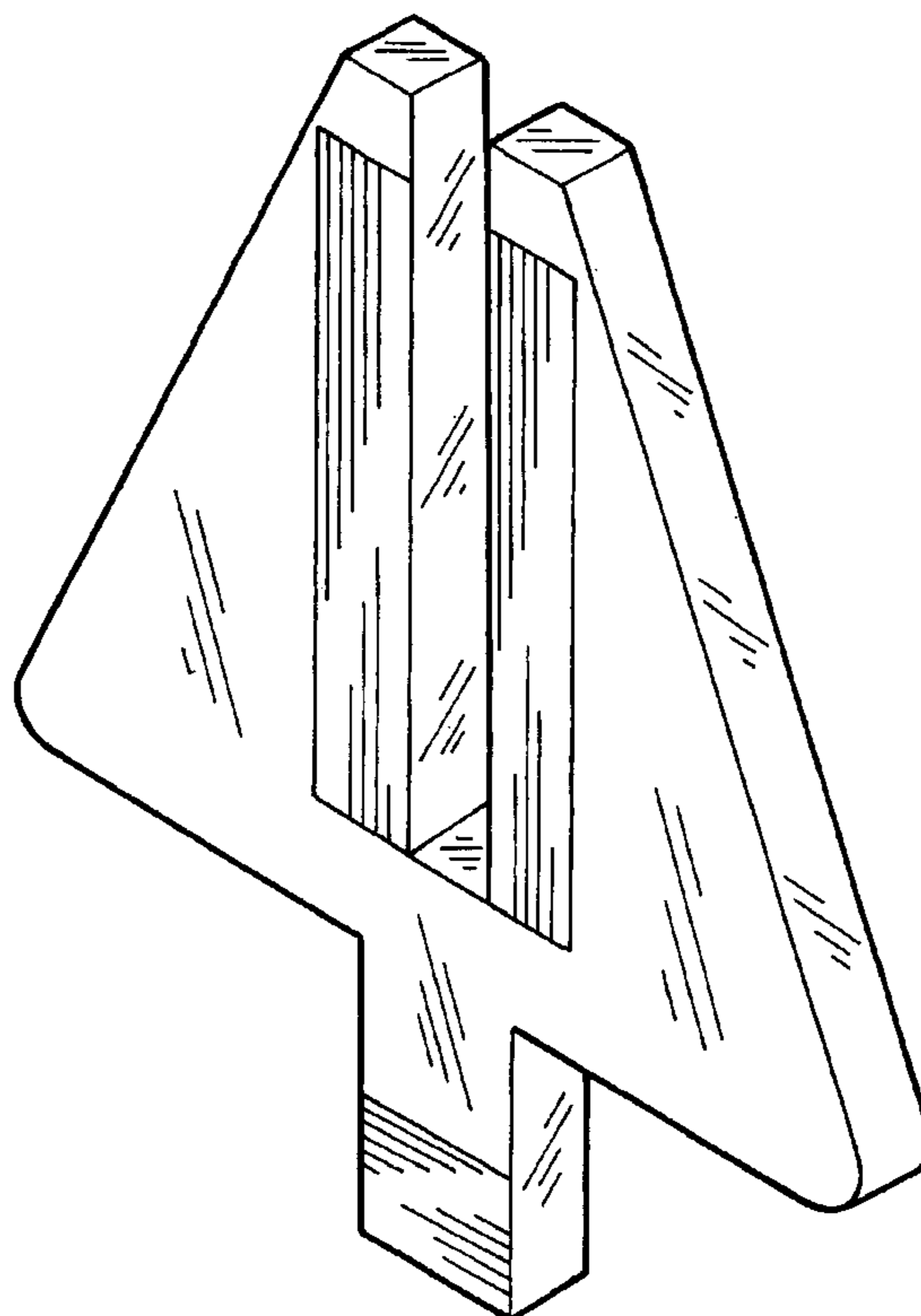
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FIG. 6 is a top view of a printed circuit board daughter board support; and,

FIG. 7 is a bottom view of a of a printed circuit board daughter board support.

**1 Claim, 3 Drawing Sheets**



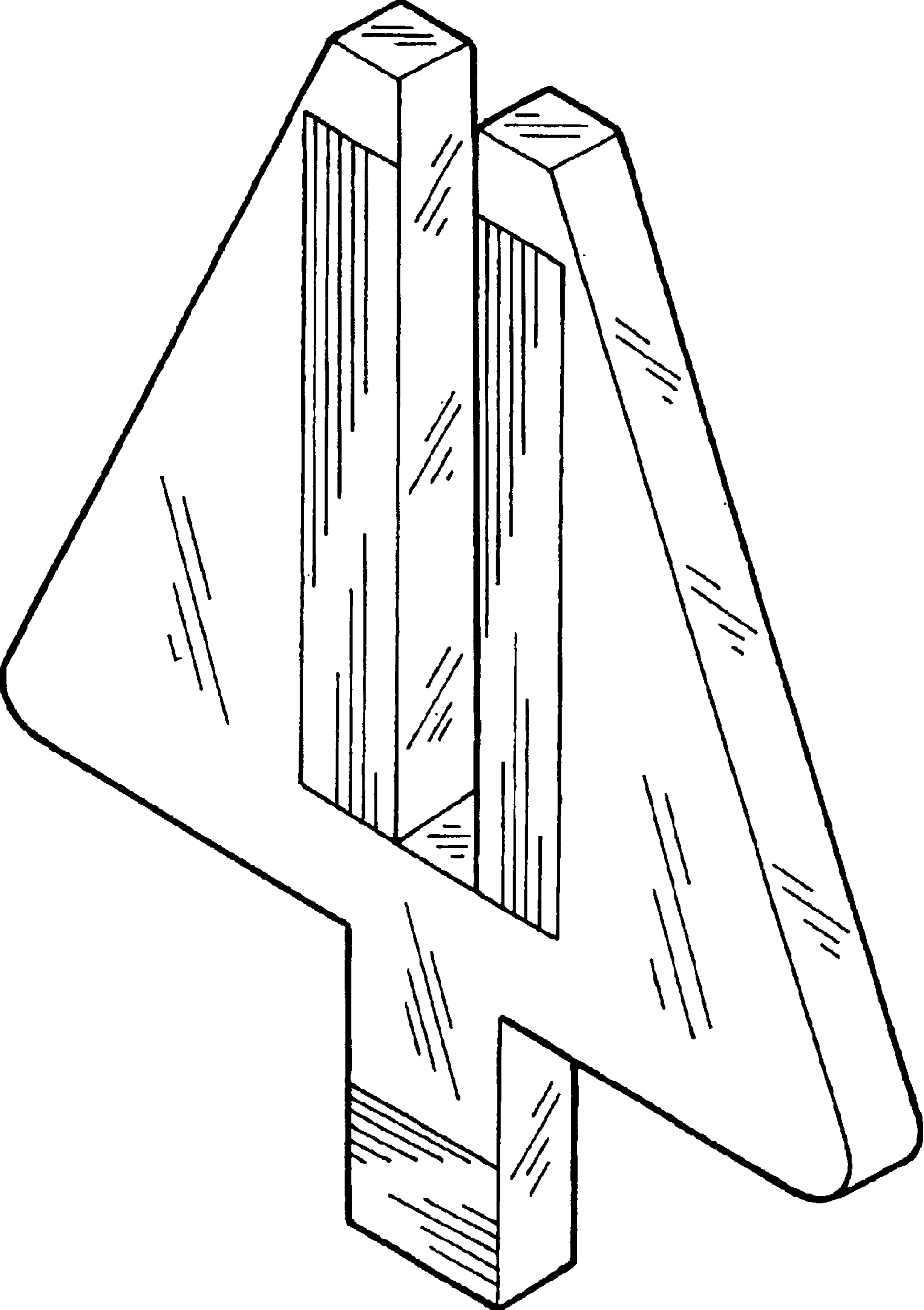


FIG. 1

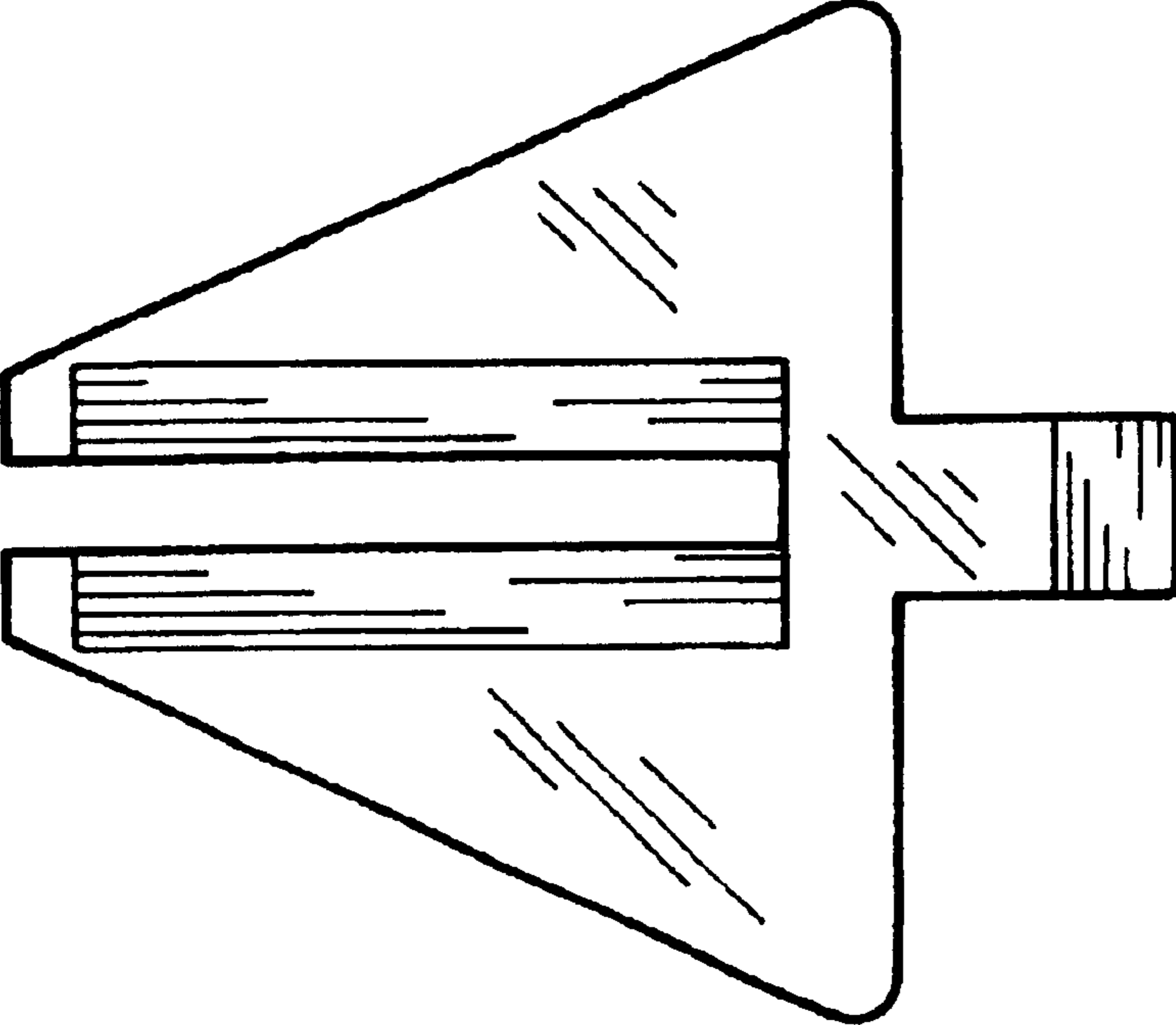


FIG. 2

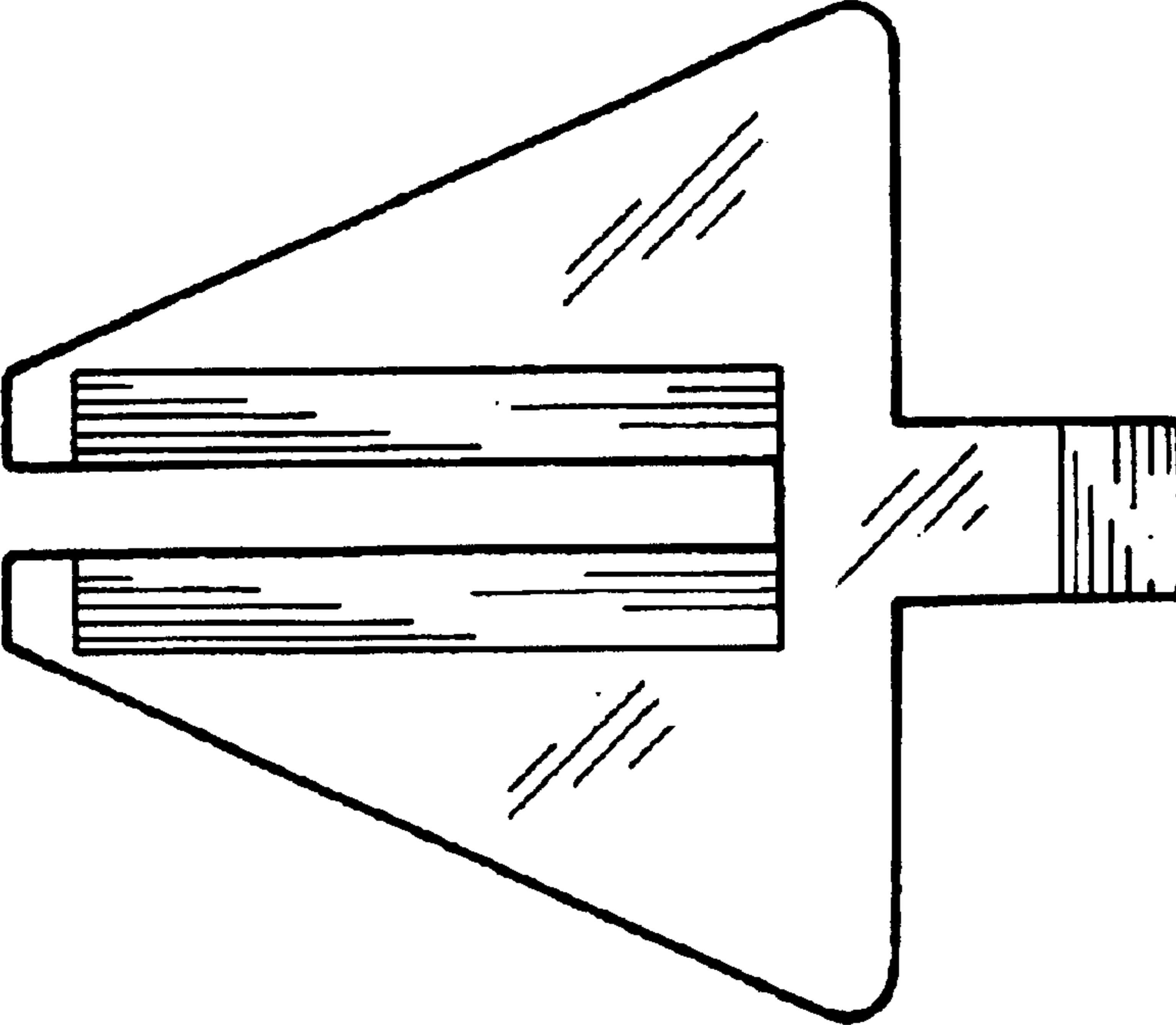


FIG. 3

FIG. 4



FIG. 5



FIG. 6



FIG. 7