

US00D495327S1

(12) **United States Design Patent**  
**Okuley**

(10) **Patent No.:** **US D495,327 S**

(45) **Date of Patent:** **\*\* Aug. 31, 2004**

(54) **PROCESSOR-BASED SYSTEM**

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(73) **Assignee:** **Intel Corporation, Santa Clara, CA (US)**

(\*\*) **Term:** **14 Years**

(21) **Appl. No.:** **29/190,283**

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(51) **LOC (7) Cl.** ..... **14-02**

(52) **U.S. Cl.** ..... **D14/336; D14/374**

(58) **Field of Search** ..... D14/336, 337, D14/371, 374, 375, 125-129; 345/104, 156, 168, 173; 348/180, 184, 325, 739; 248/917-924; 349/1, 2, 11, 62; 341/12

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

D394,048 S	*	5/1998	Lee	.....	D14/375
D397,094 S	*	8/1998	Kishi et al.	.....	D14/337
D416,549 S	*	11/1999	Coster et al.	.....	D14/375
D416,876 S	*	11/1999	Yin	.....	D14/375

D442,581 S	*	5/2001	Su	.....	D14/337
D465,490 S	*	11/2002	Wei	.....	D14/374
D470,489 S	*	2/2003	Lin	.....	D14/375
D471,549 S	*	3/2003	Tsuji	.....	D14/375

\* cited by examiner

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(57) **CLAIM**

The ornamental design for the processor-based system, as shown and described.

**DESCRIPTION**

FIG. 1 is a front perspective view of the processor-based system;

FIG. 2 is a back perspective view thereof;

FIG. 3 is a top plan view thereof;

FIG. 4 is a bottom plan view thereof;

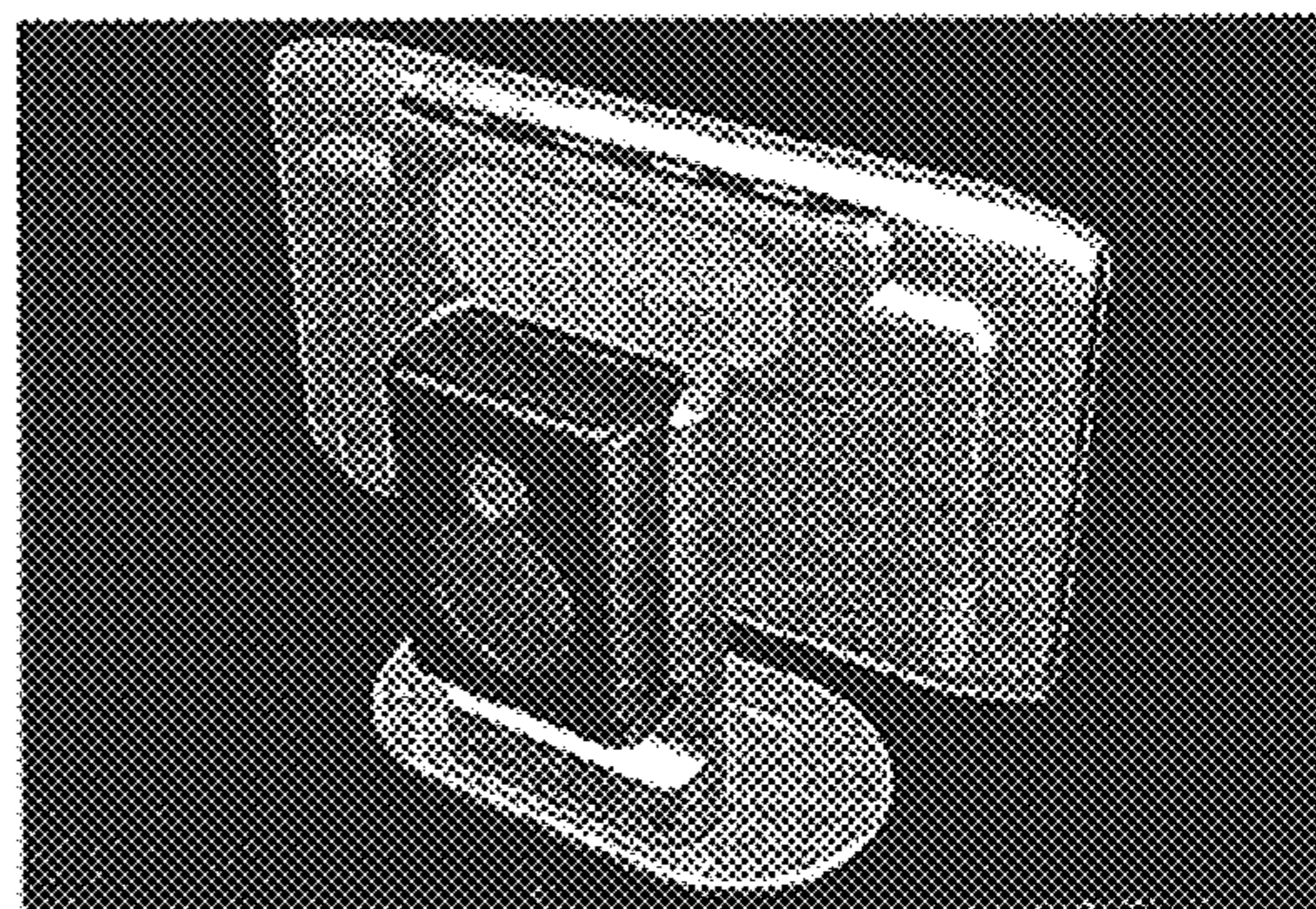
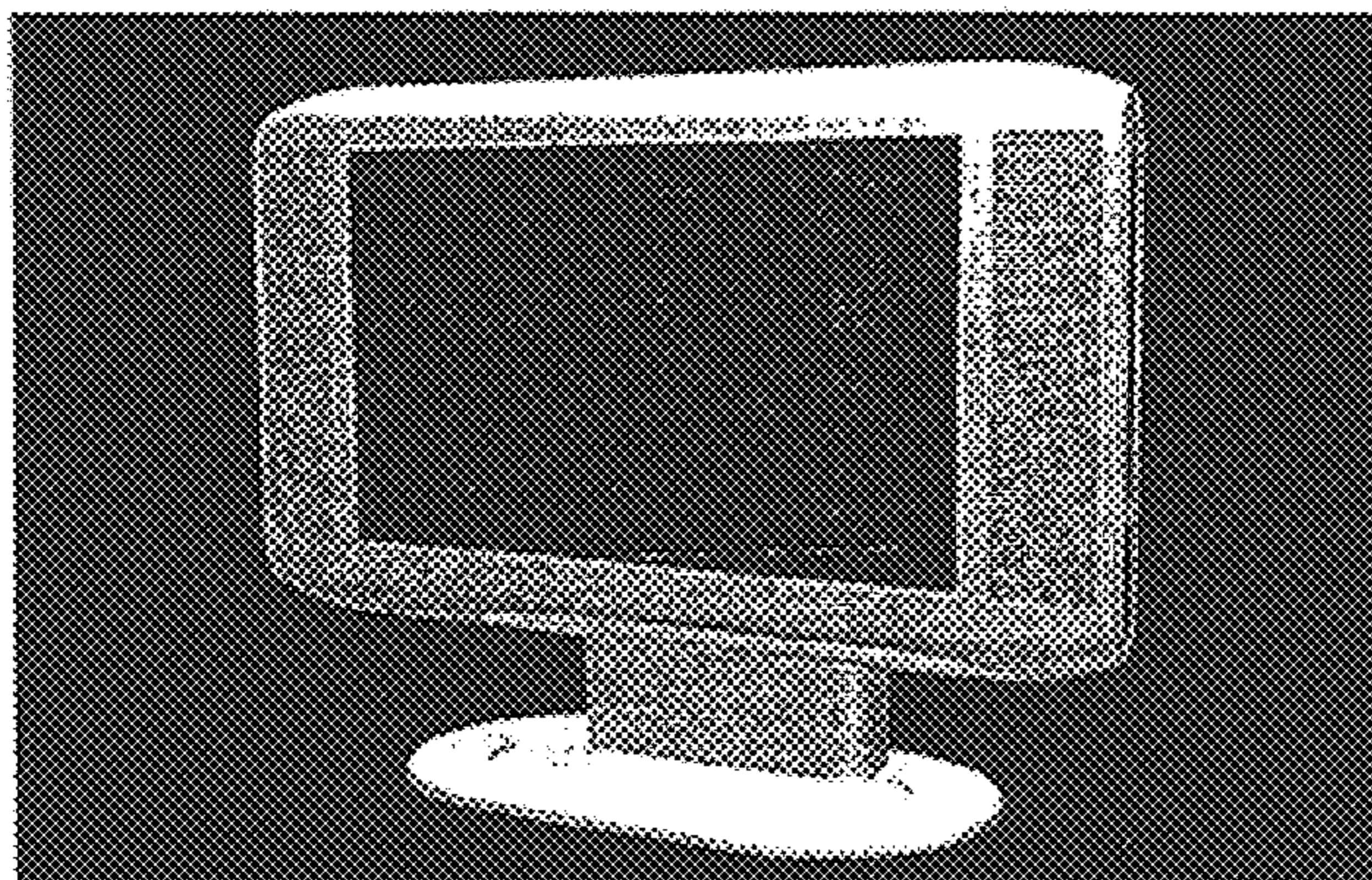
FIG. 5 is a left side plan view thereof;

FIG. 6 is a right side plan view thereof;

FIG. 7 is a back plan view thereof; and,

FIG. 8 is a front plan view thereof.

**1 Claim, 6 Drawing Sheets**





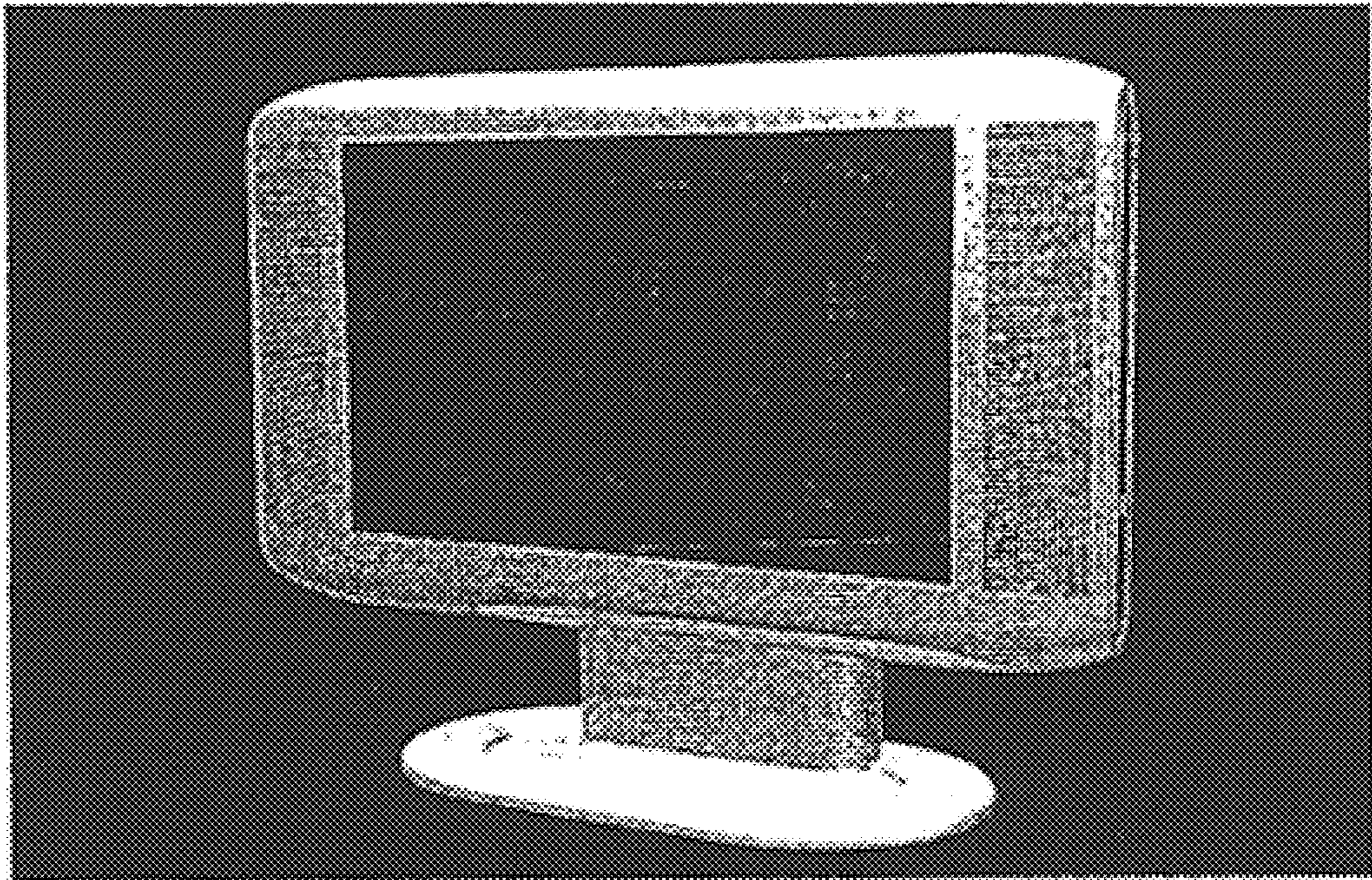


Fig. 1

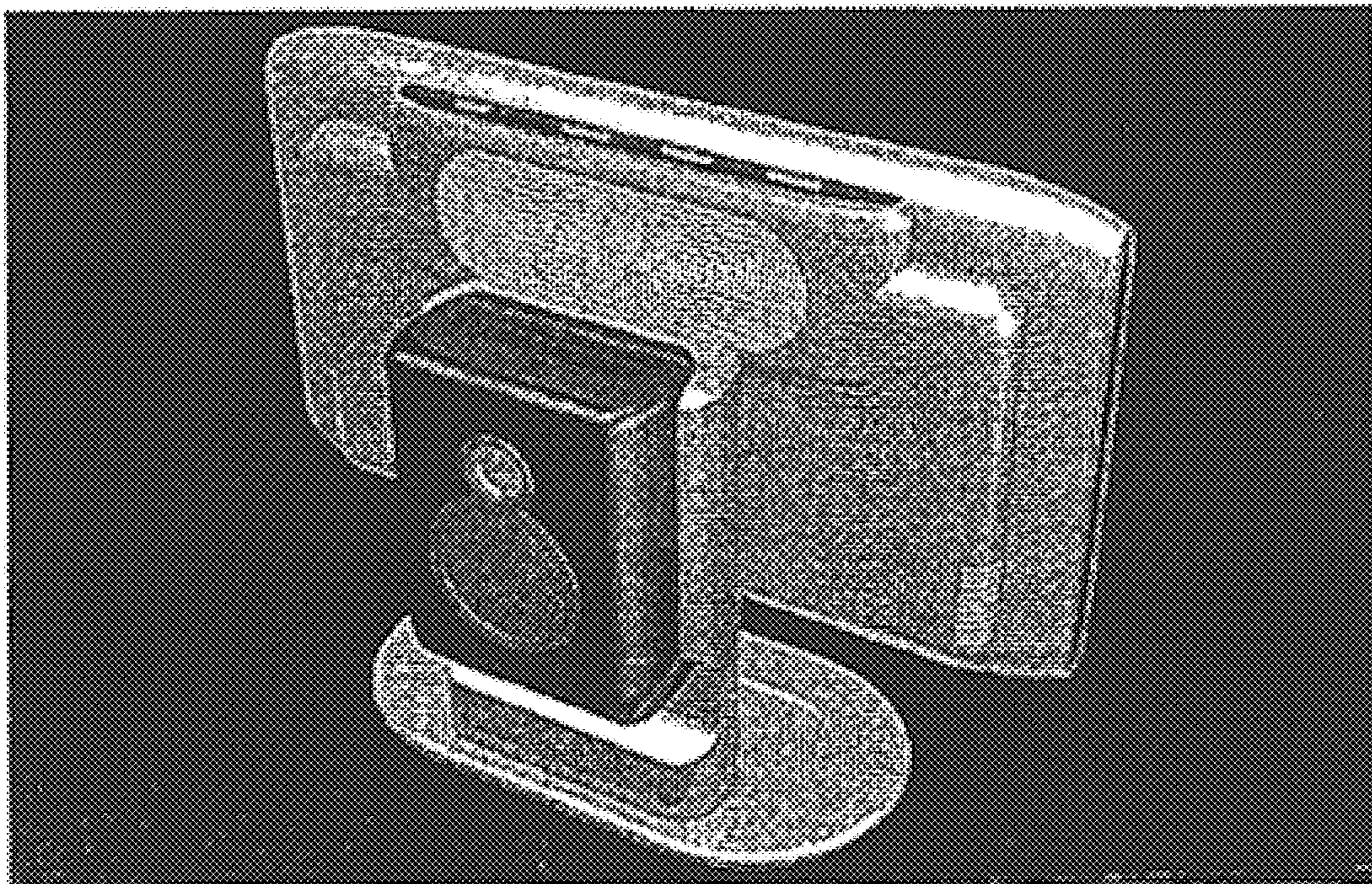


Fig. 2



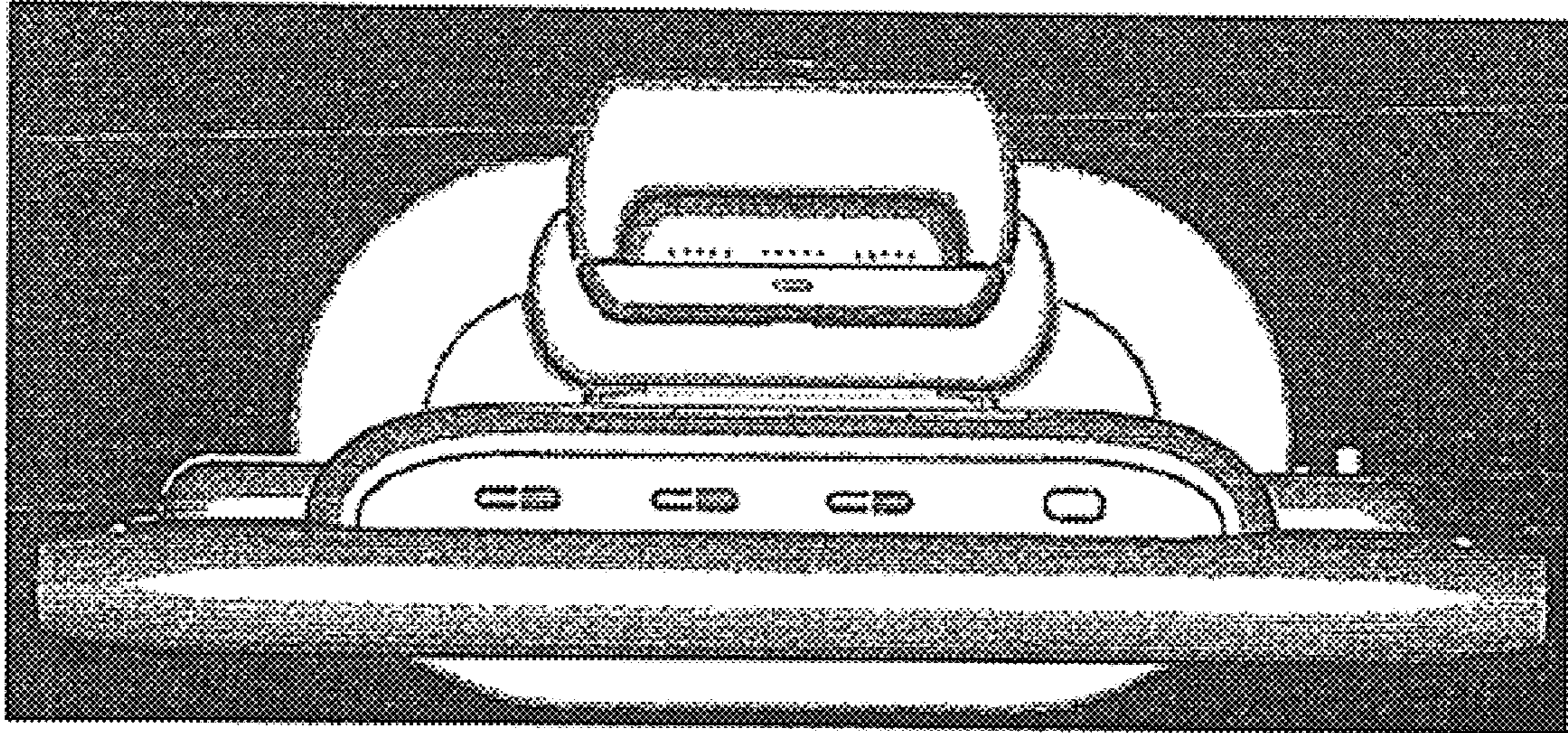


Fig. 3

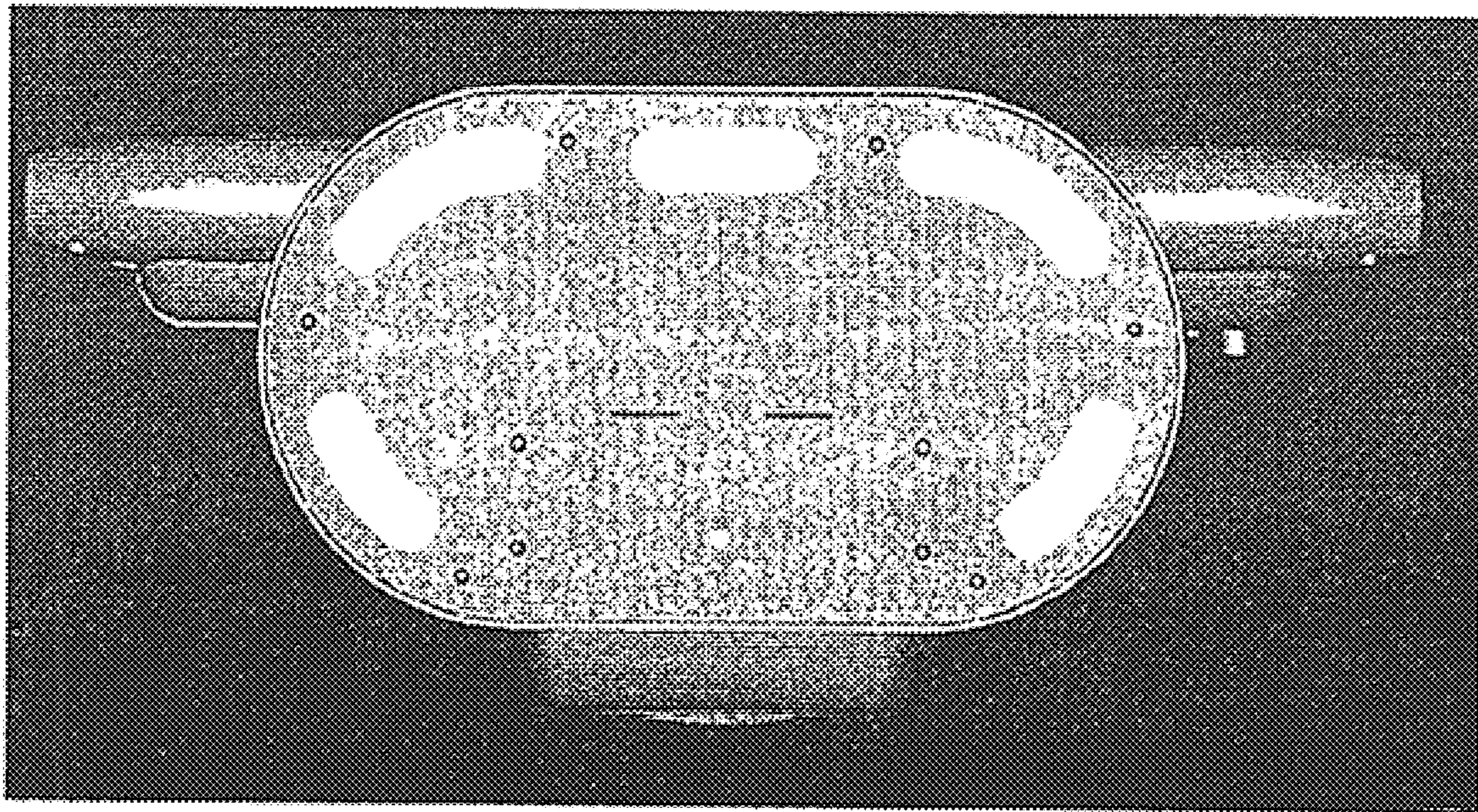


Fig. 4



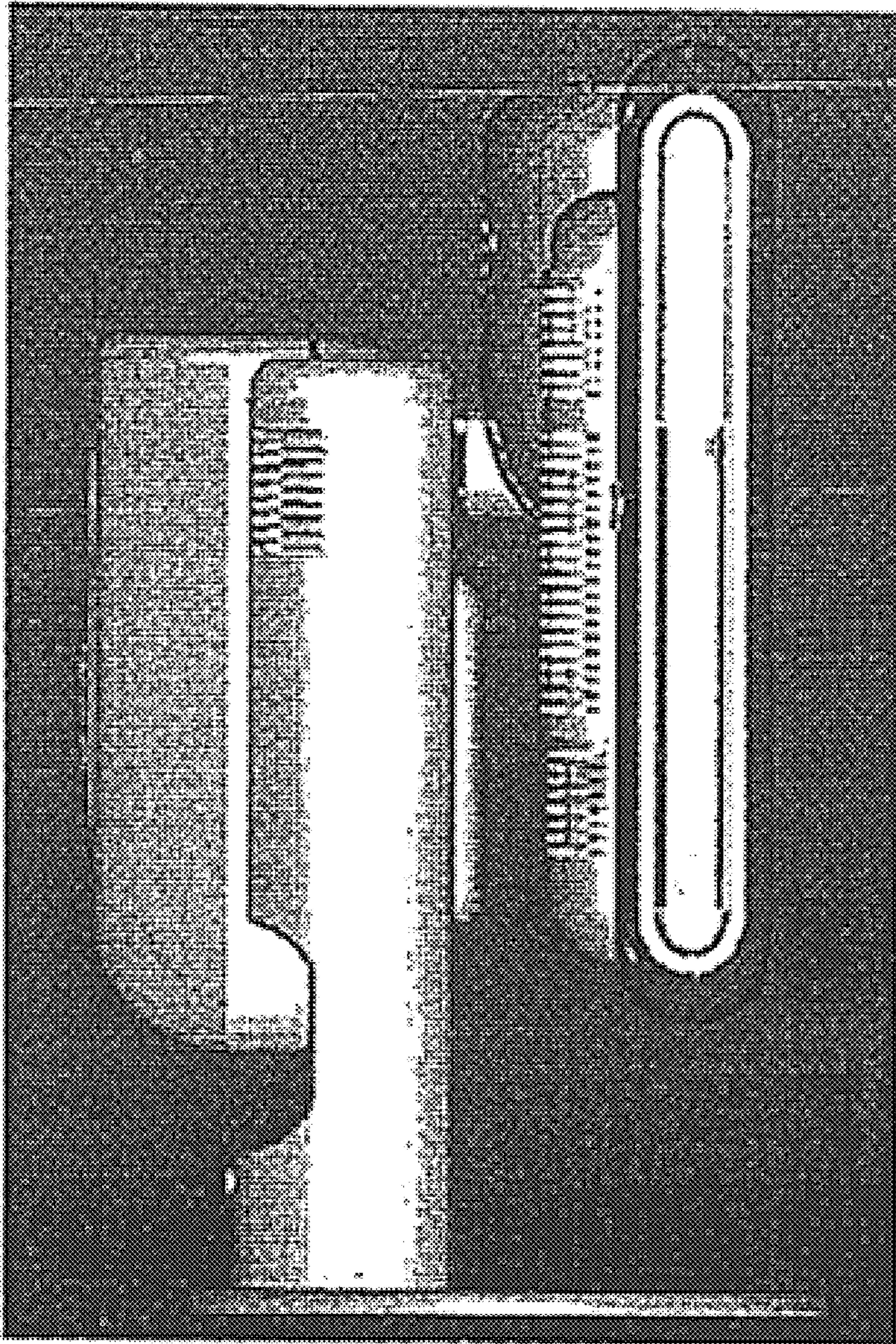


Fig. 5



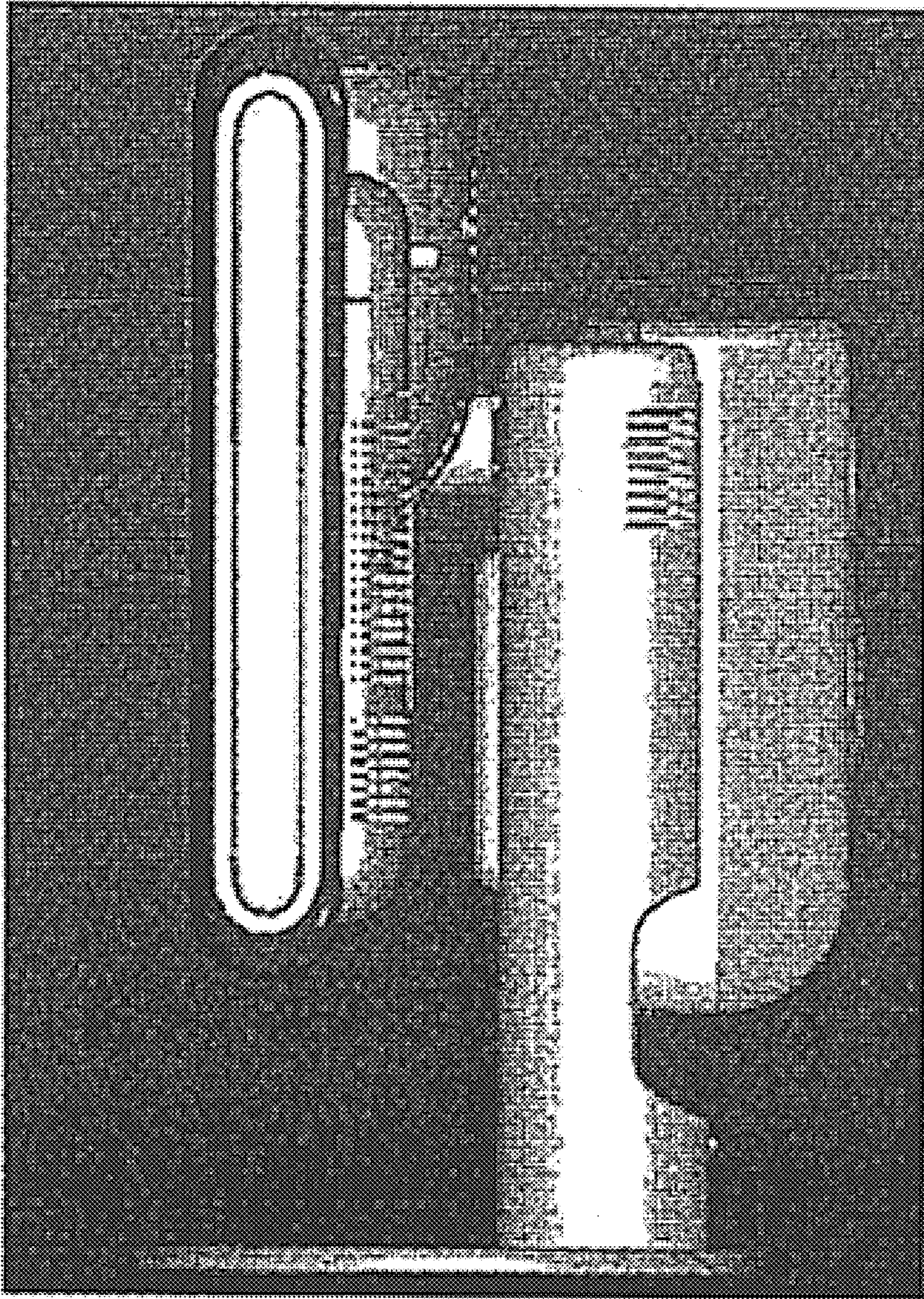


Fig. 6



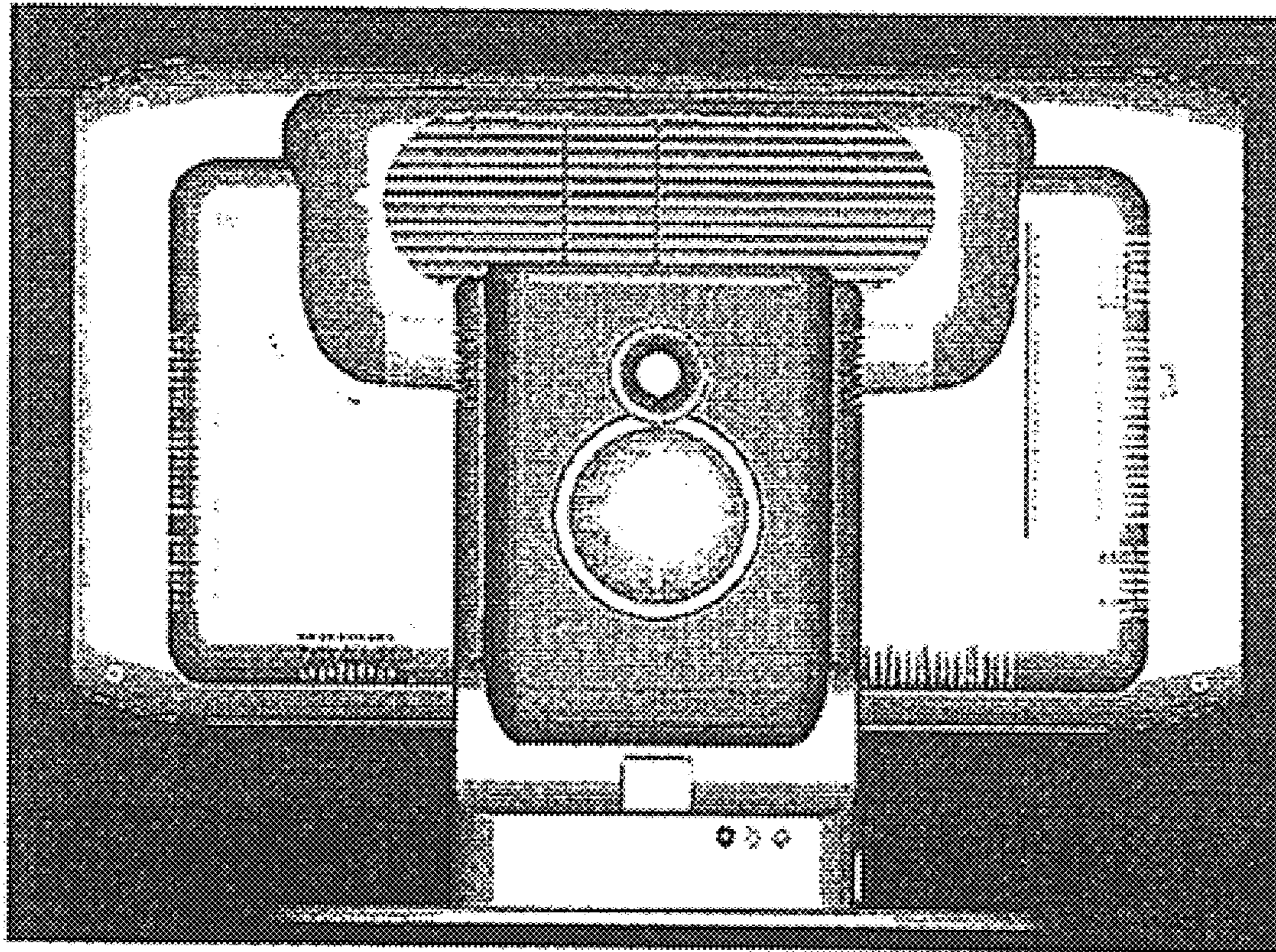


Fig. 7



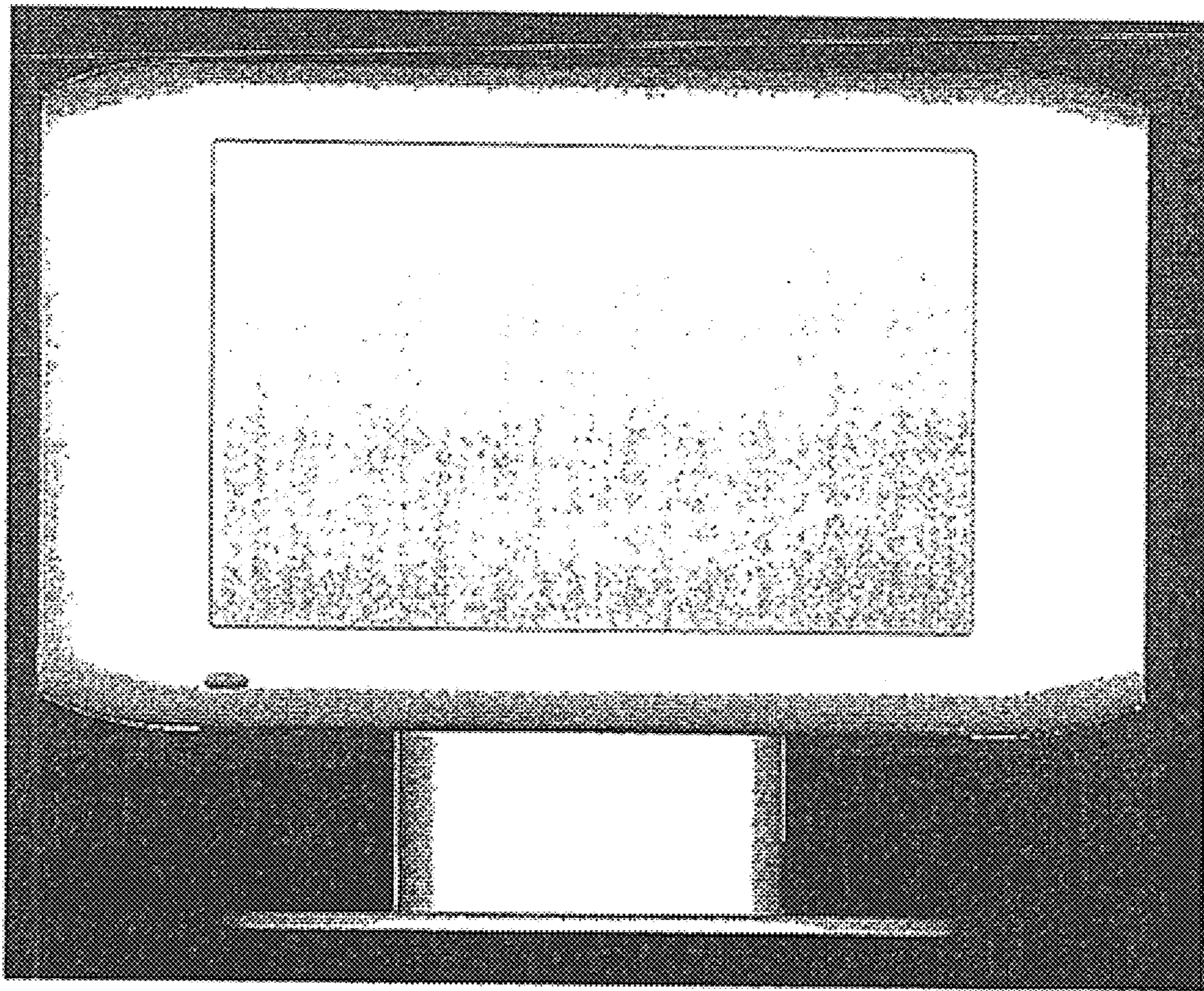


Fig. 8