

US00D494973S1

(12) **United States Design Patent** (10) **Patent No.:** **US D494,973 S**
Imazeki (45) **Date of Patent:** **** Aug. 24, 2004**

(54) **SCANNER**

(75) **Inventor:** **Kazuyoshi Imazeki, Tokyo (JP)**

(73) **Assignee:** **General Research of Electronics, Inc. (JP)**

(**) **Term:** **14 Years**

(21) **Appl. No.:** **29/175,446**

(22) **Filed:** **Feb. 5, 2003**

(51) **LOC (7) Cl.** **14-02**

(52) **U.S. Cl.** **D14/426**

(58) **Field of Search** D14/420, 426-431,
D14/399; 361/683, 379, 383; 235/472.02,
462.15, 375, 378, 385, 454; D19/41-51;
D26/37, 45, 50

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Primary Examiner—Kay H. Chin

(74) *Attorney, Agent, or Firm*—Trexler, Bushnell,
Giangiorgi, Blackstone & Marr, Ltd

(57) **CLAIM**

The ornamental design for a scanner, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of the scanner according to my design;

FIG. 2 is a front elevational view of the scanner;

FIG. 3 is a bottom plan view of the scanner;

FIG. 4 is a top plan view of the scanner;

FIG. 5 is a right side elevational view of the scanner;

FIG. 6 is a rear elevational view of the scanner;

FIG. 7 is a left side elevational view of the scanner; and,

FIG. 8 is an enlarged, partial front elevational view of the scanner showing that the holes in the center form a circular pattern and extend through the housing of the scanner, while depressions surround the holes and do not extend through the housing of the scanner.

In FIGS. 1, 2, 4, 6 and 7, broken line representations of an antenna connector is shown, which is for illustrative purposes only and forms no portion of the claimed invention.

In FIG. 4, a broken line representation of a headphone jack is shown, which is for illustrative purposes only and forms no portion of the claimed invention.

1 Claim, 3 Drawing Sheets

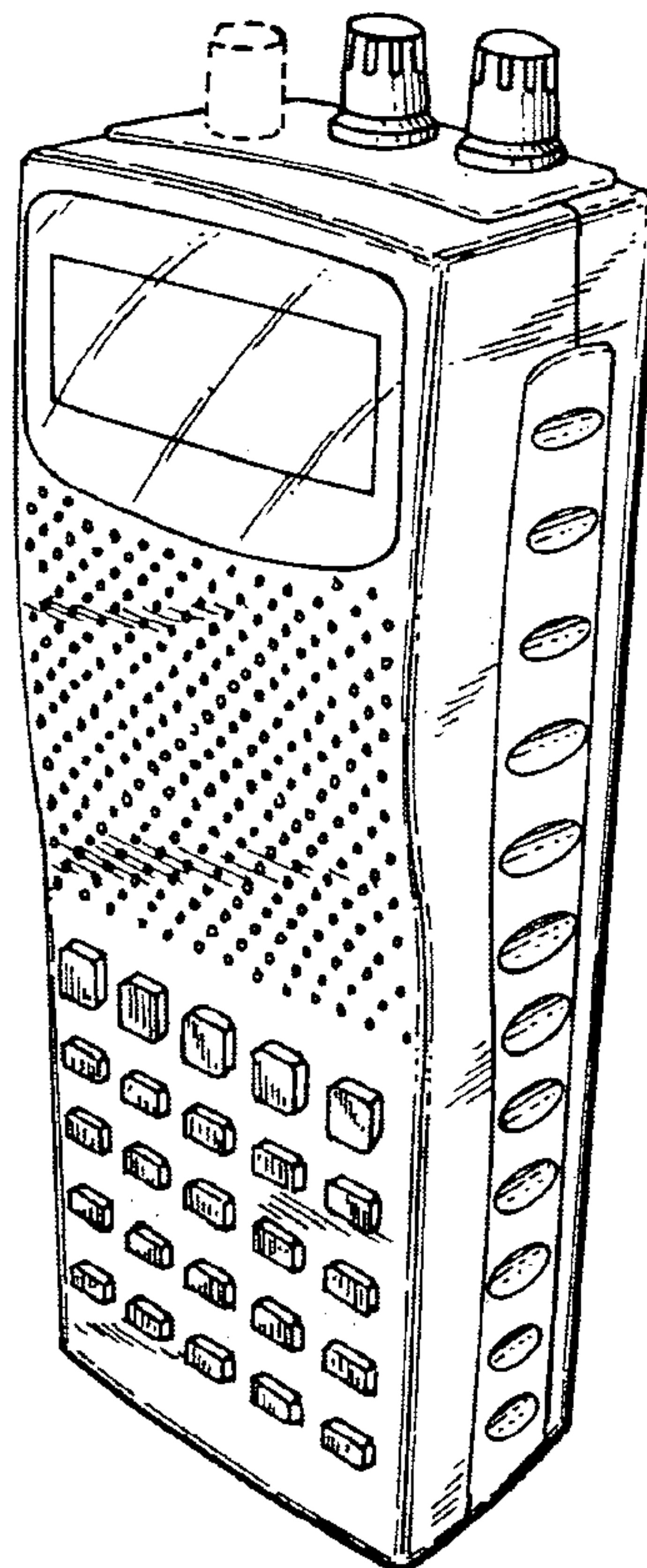


FIG.1

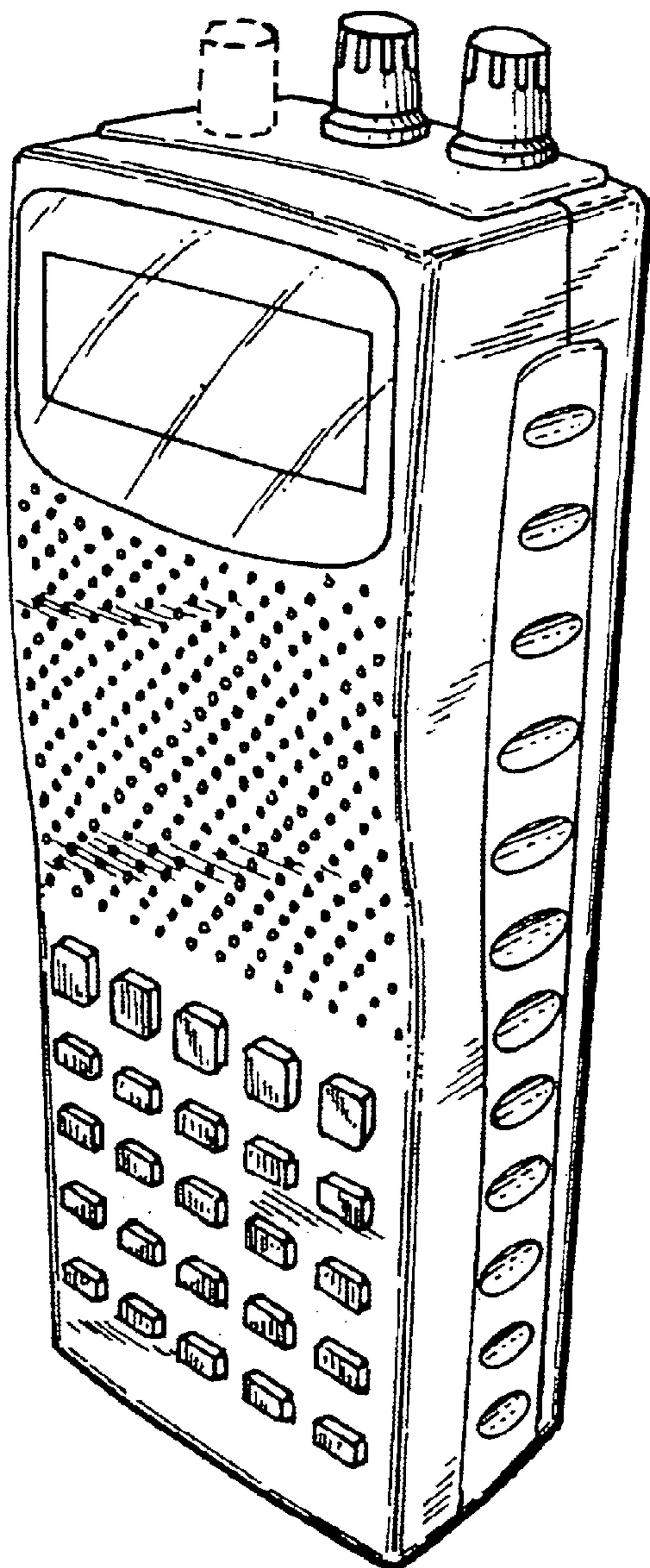


FIG.2

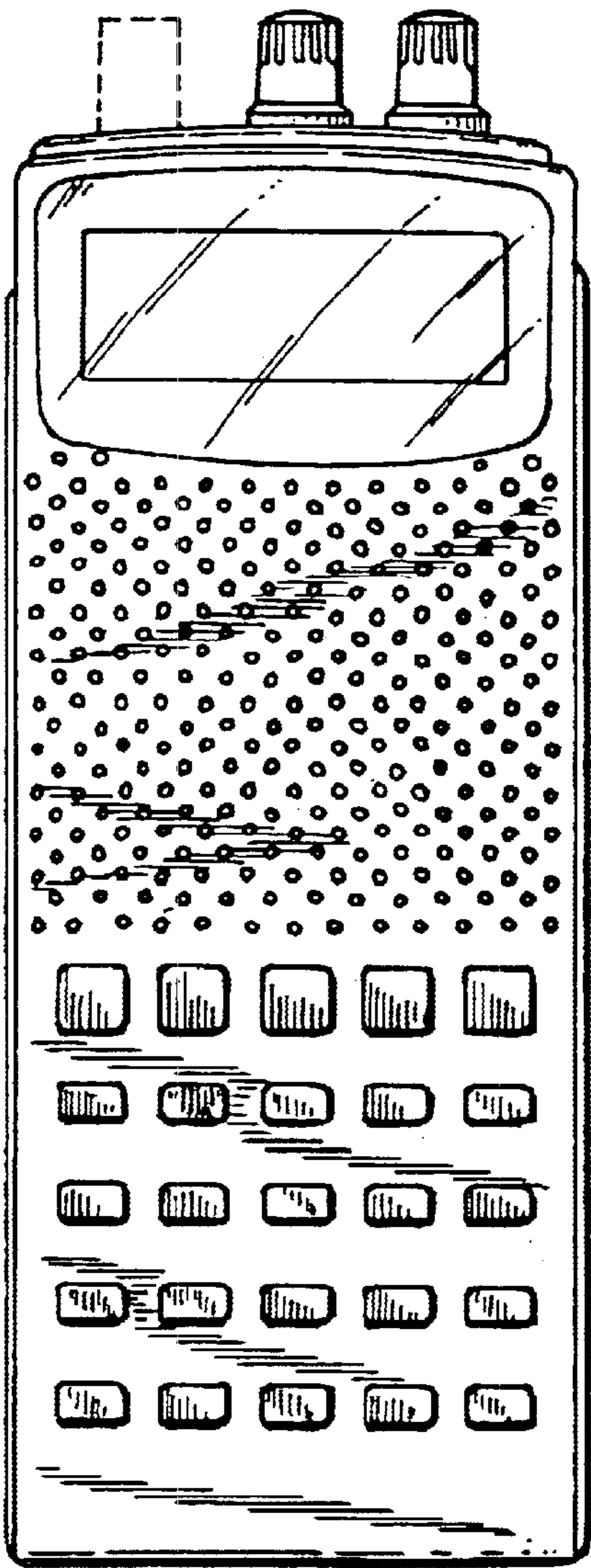


FIG.3

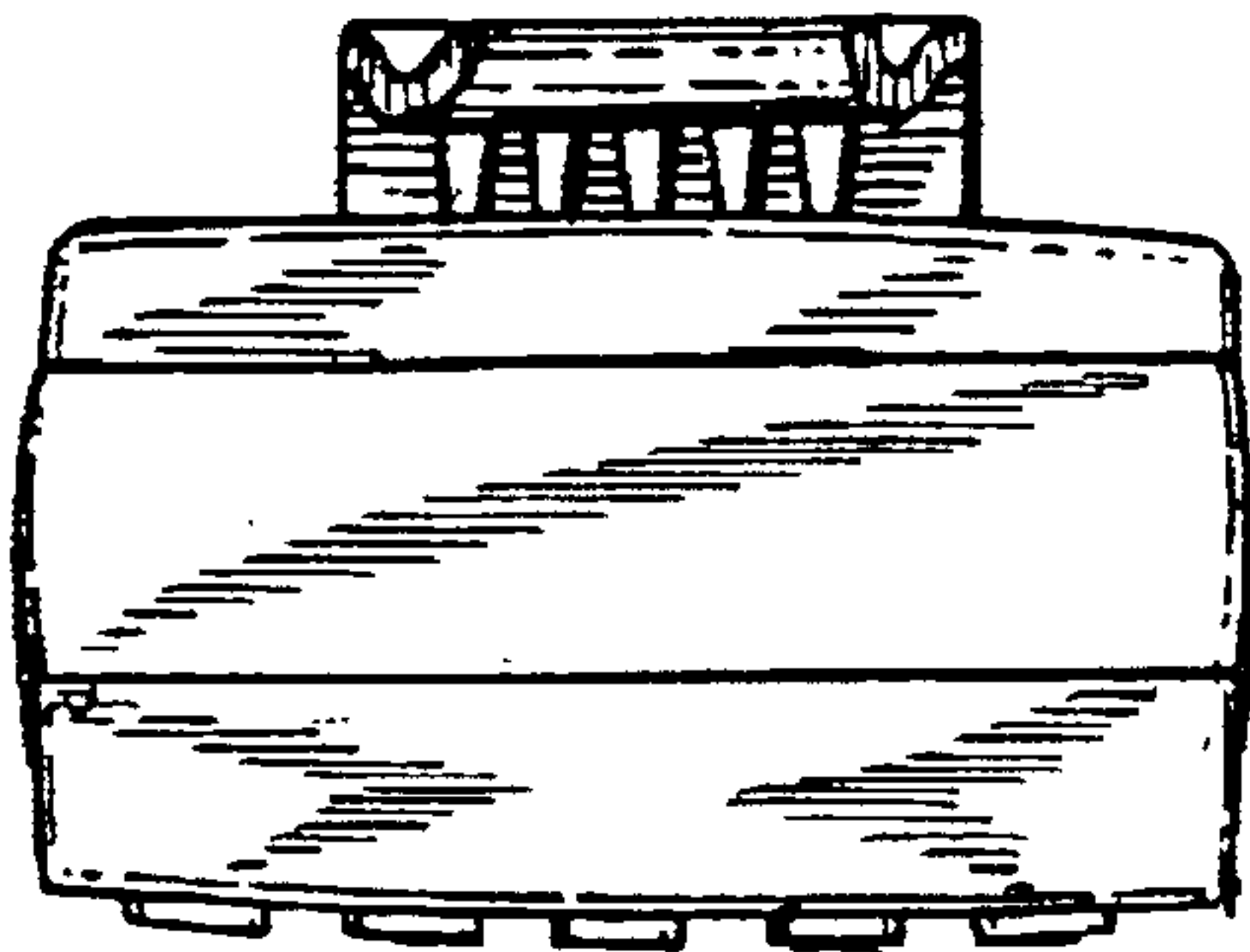
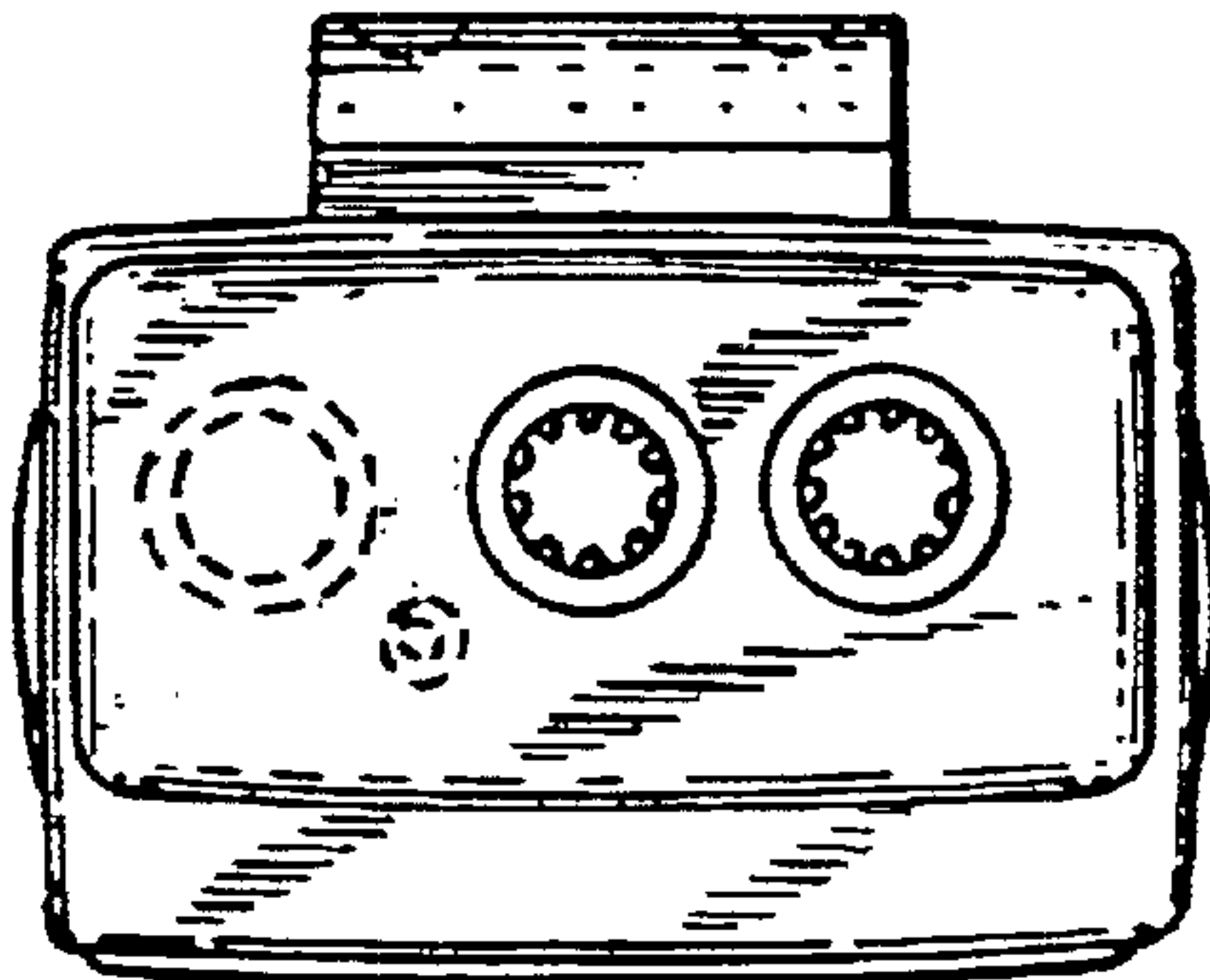


FIG.4



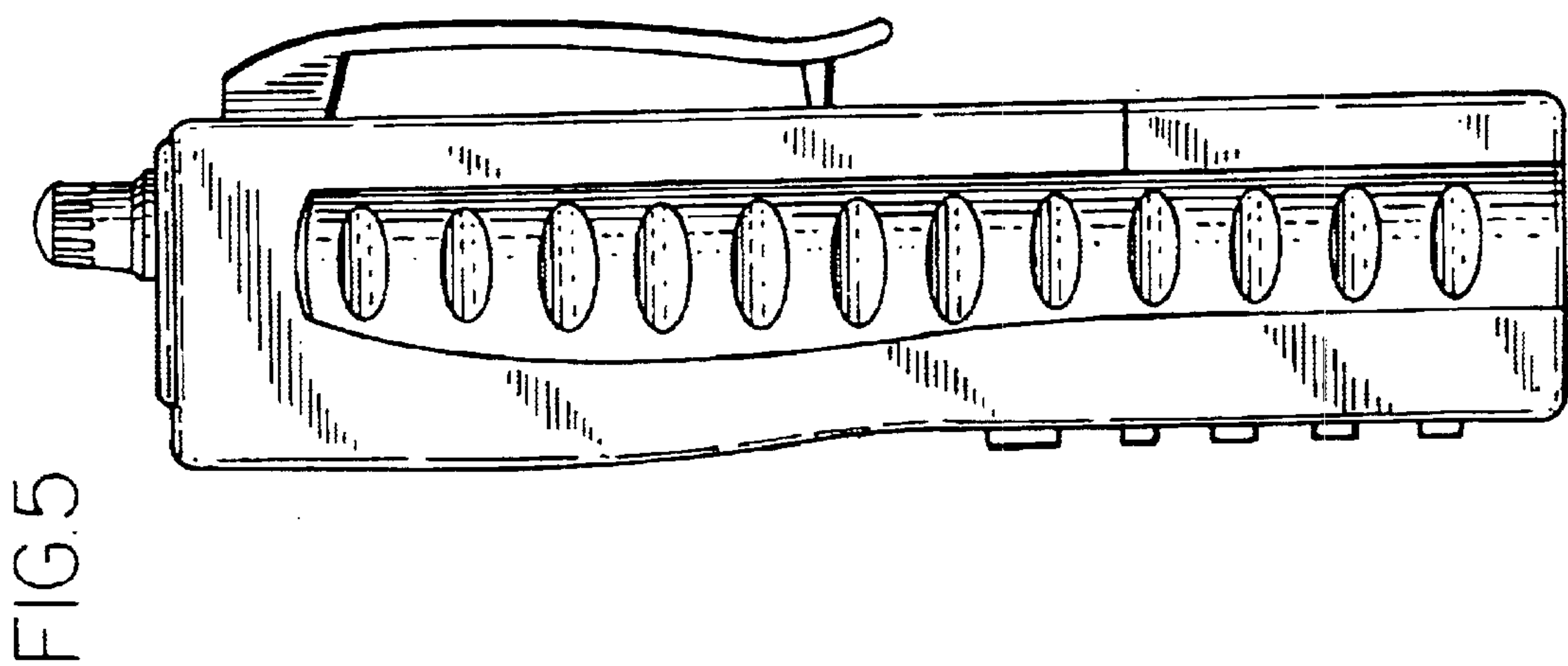
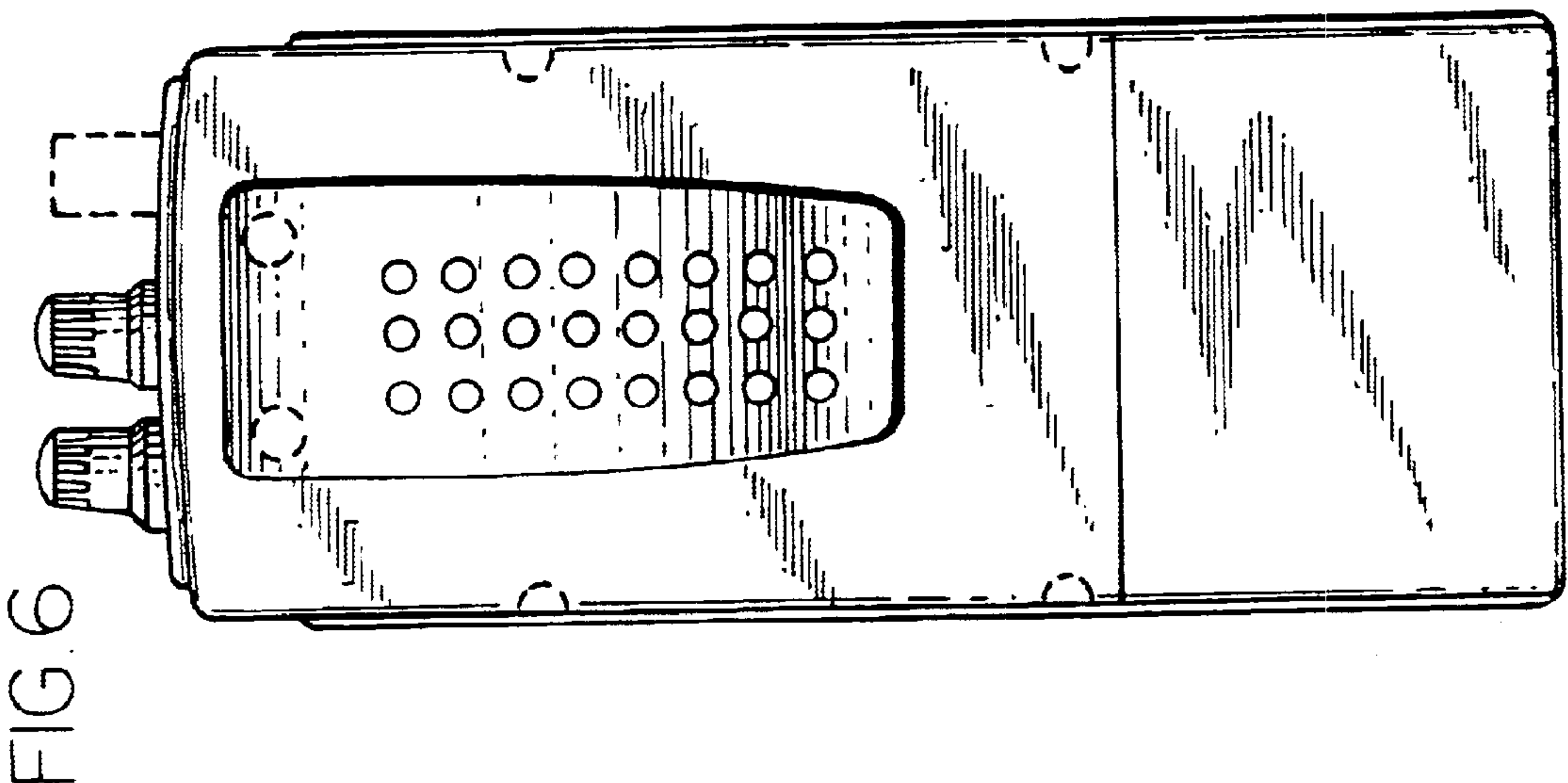
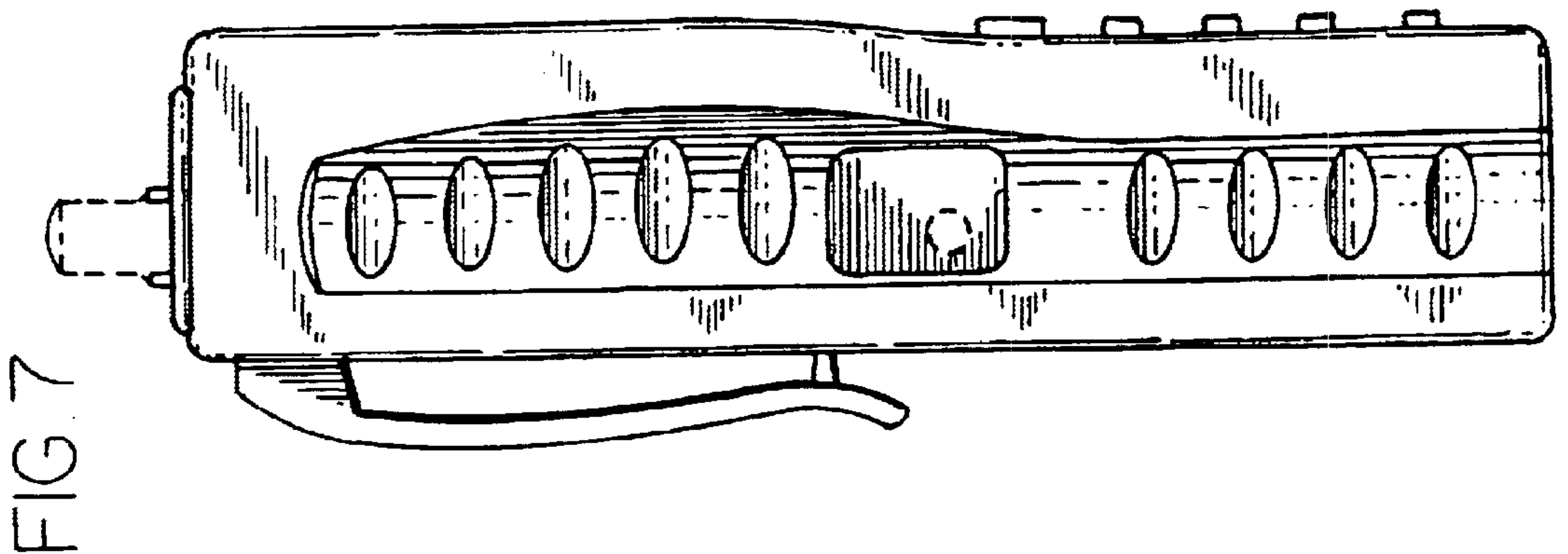


FIG. 8

