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(12) **United States Design Patent** (10) **Patent No.:** **US D494,286 S**  
**Matsui et al.** (45) **Date of Patent:** **\*\* Aug. 10, 2004**

(54) **LIGHT EMITTING DIODE LAMP UNIT**

**FOREIGN PATENT DOCUMENTS**

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**Masanori Shimizu**, Kyoto (JP)

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JP 808267 2/1991

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\* cited by examiner

(\*\*) Term: **14 Years**

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(21) Appl. No.: **29/186,989**

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(22) Filed: **Jul. 24, 2003**

(57) **CLAIM**

(30) **Foreign Application Priority Data**

The ornamental design for a light emitting diode lamp unit, as shown and described.

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**DESCRIPTION**

(51) **LOC (7) Cl.** ..... **26-06**

(52) **U.S. Cl.** ..... **D26/2; D13/180**

(58) **Field of Search** ..... D26/2; D13/180,  
D13/182; 313/113, 315, 317, 318, 493,  
500; 362/231, 235, 237, 241, 249, 240,  
545, 555; 345/83

FIG. 1 is a perspective view of the front, bottom and right side of a light emitting diode lamp unit showing our new design;

FIG. 2 is a front view thereof;

FIG. 3 is a top plan view thereof, the bottom plan view being a mirror image thereof;

FIG. 4 is a right side view thereof, the left side view being a mirror image;

FIG. 5 is a rear view thereof;

FIG. 6 is a perspective view thereof showing the condition of raising the LED module part; and,

FIG. 7 is a perspective view thereof showing the condition of taking the LED module part out.

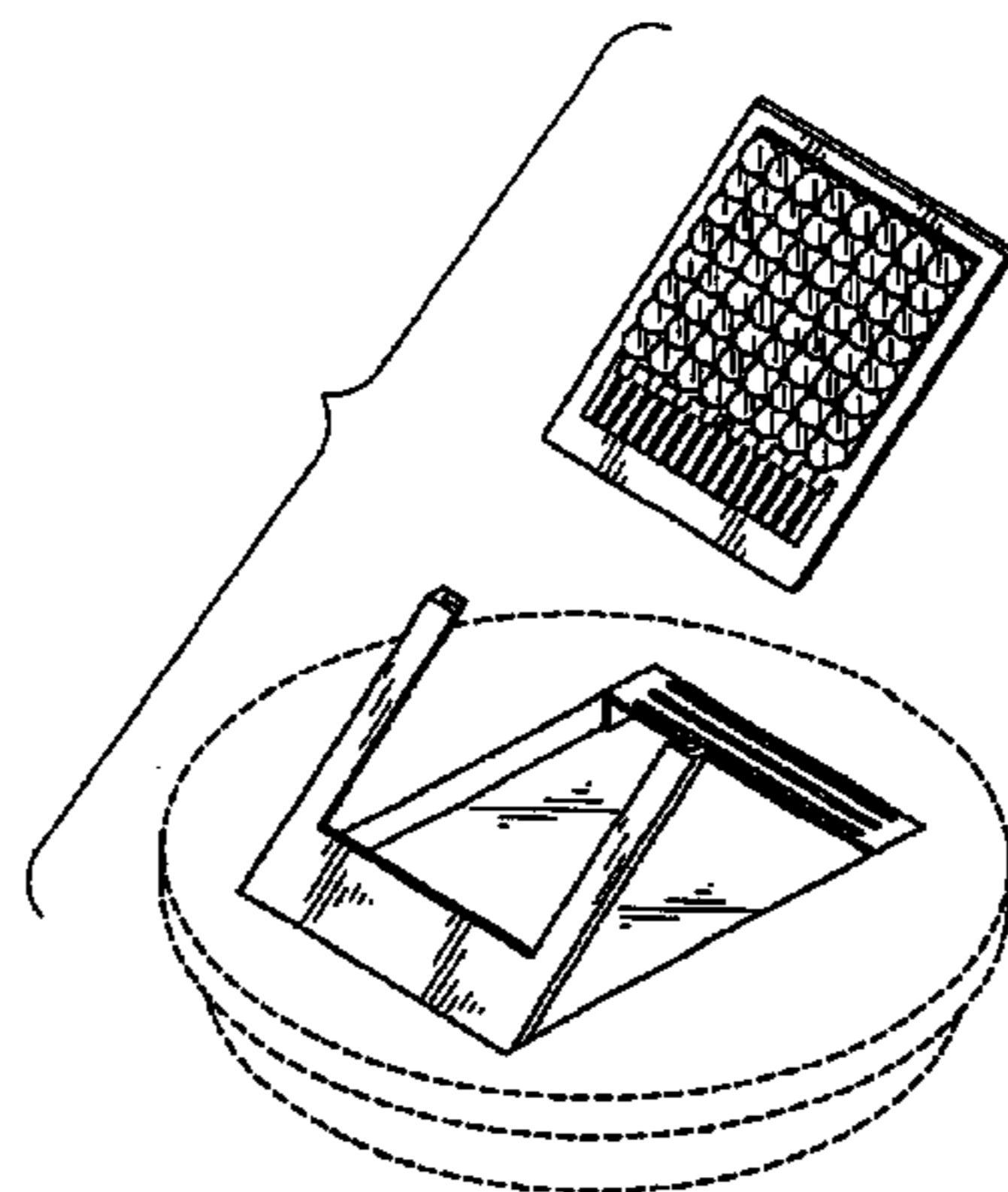
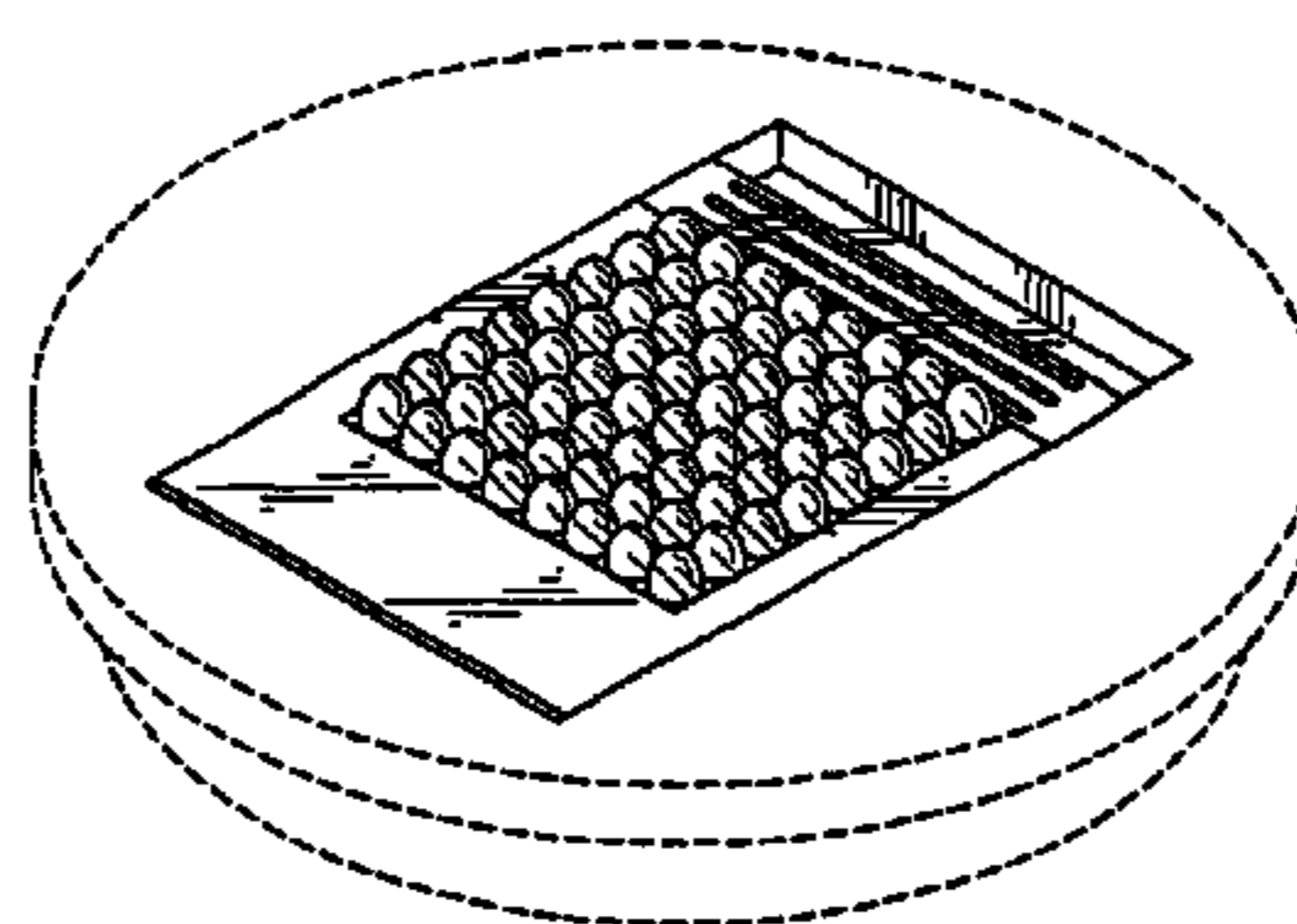
The broken line showing of the environment is for illustrative purpose only and forms no part of the claimed design.

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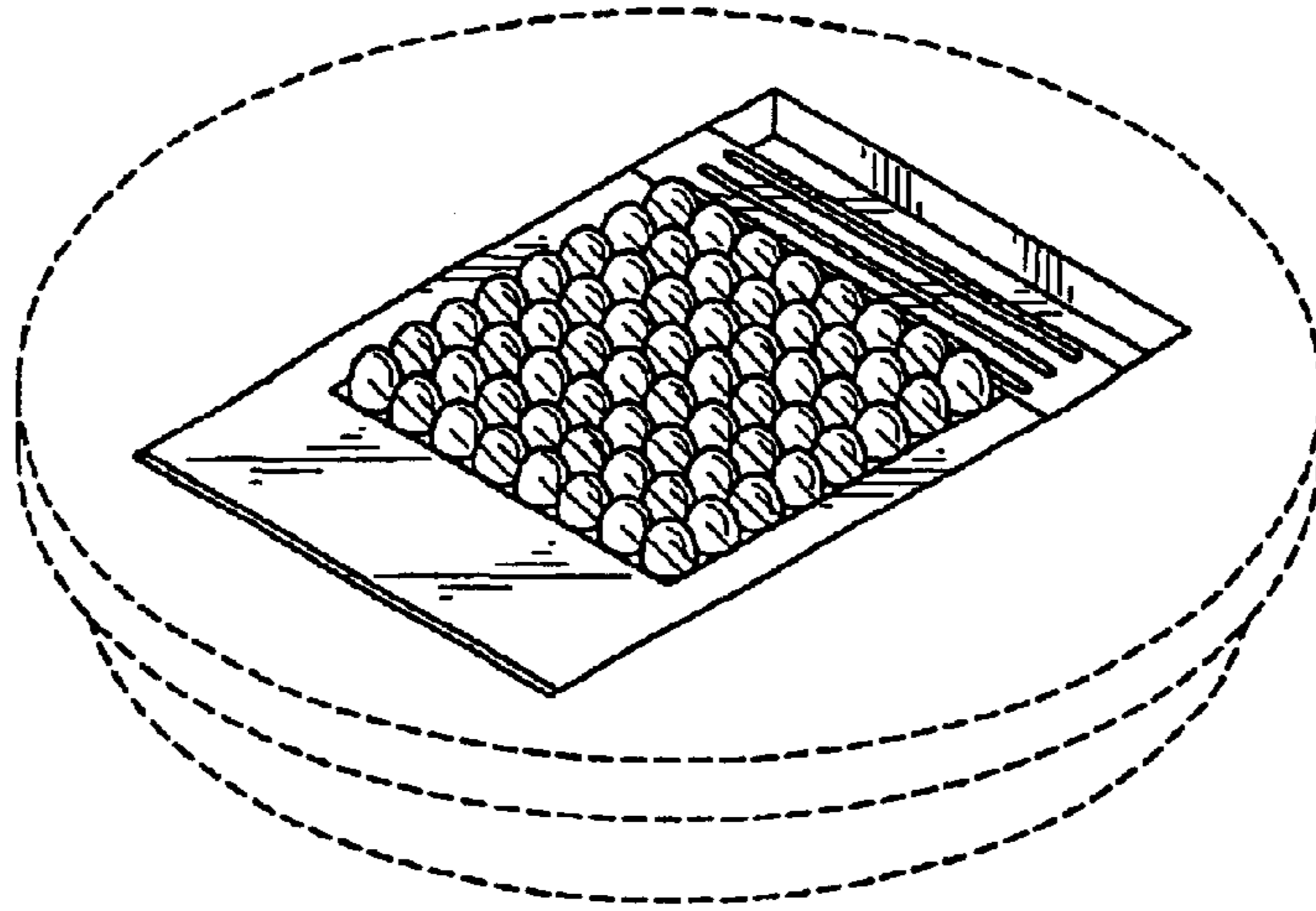
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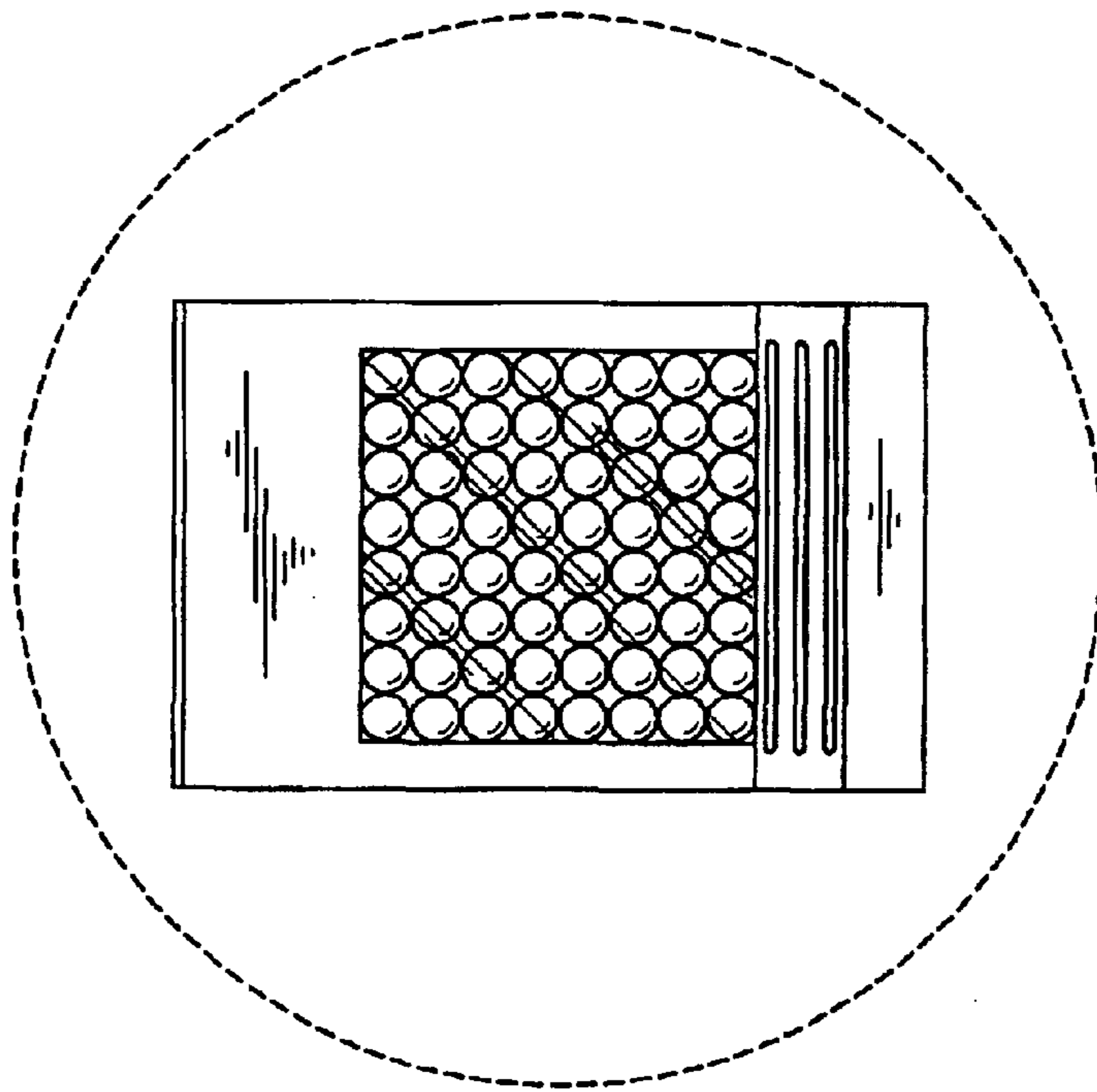
**1 Claim, 3 Drawing Sheets**



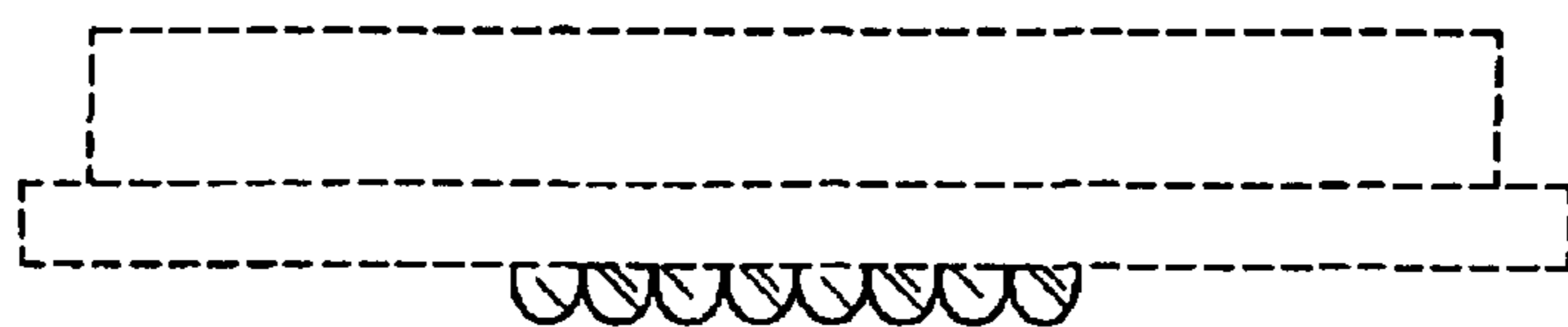
**FIG. 1**



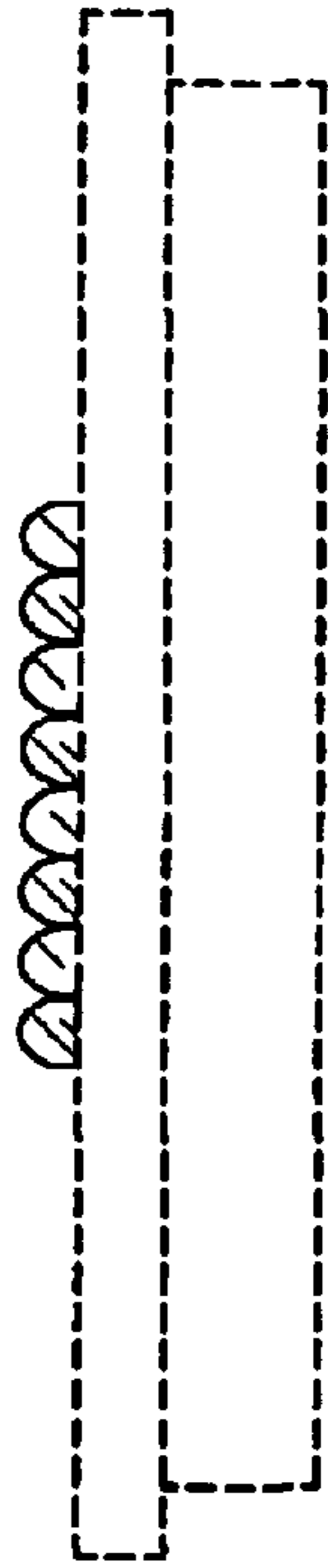
**FIG. 2**



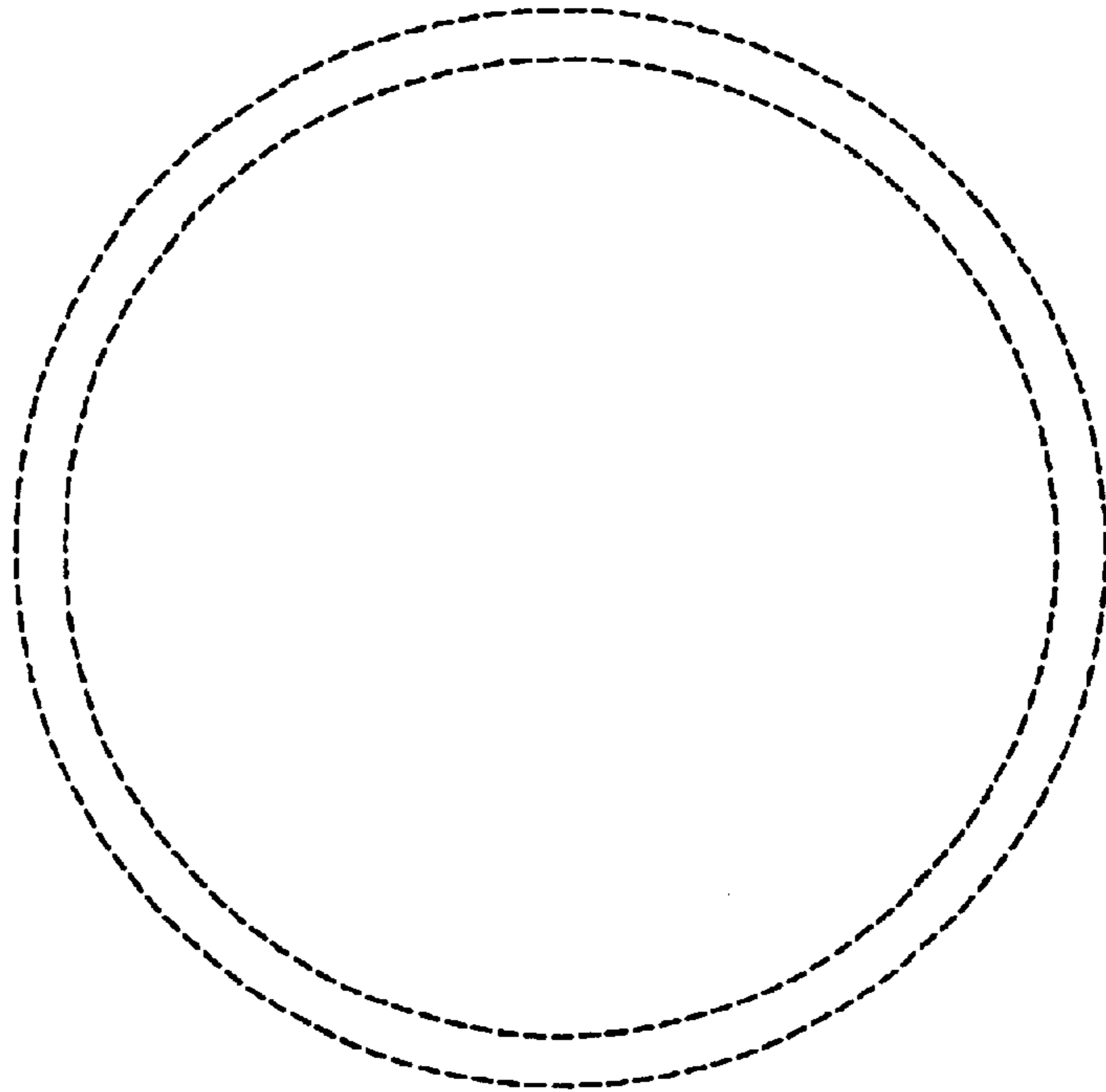
**FIG. 3**



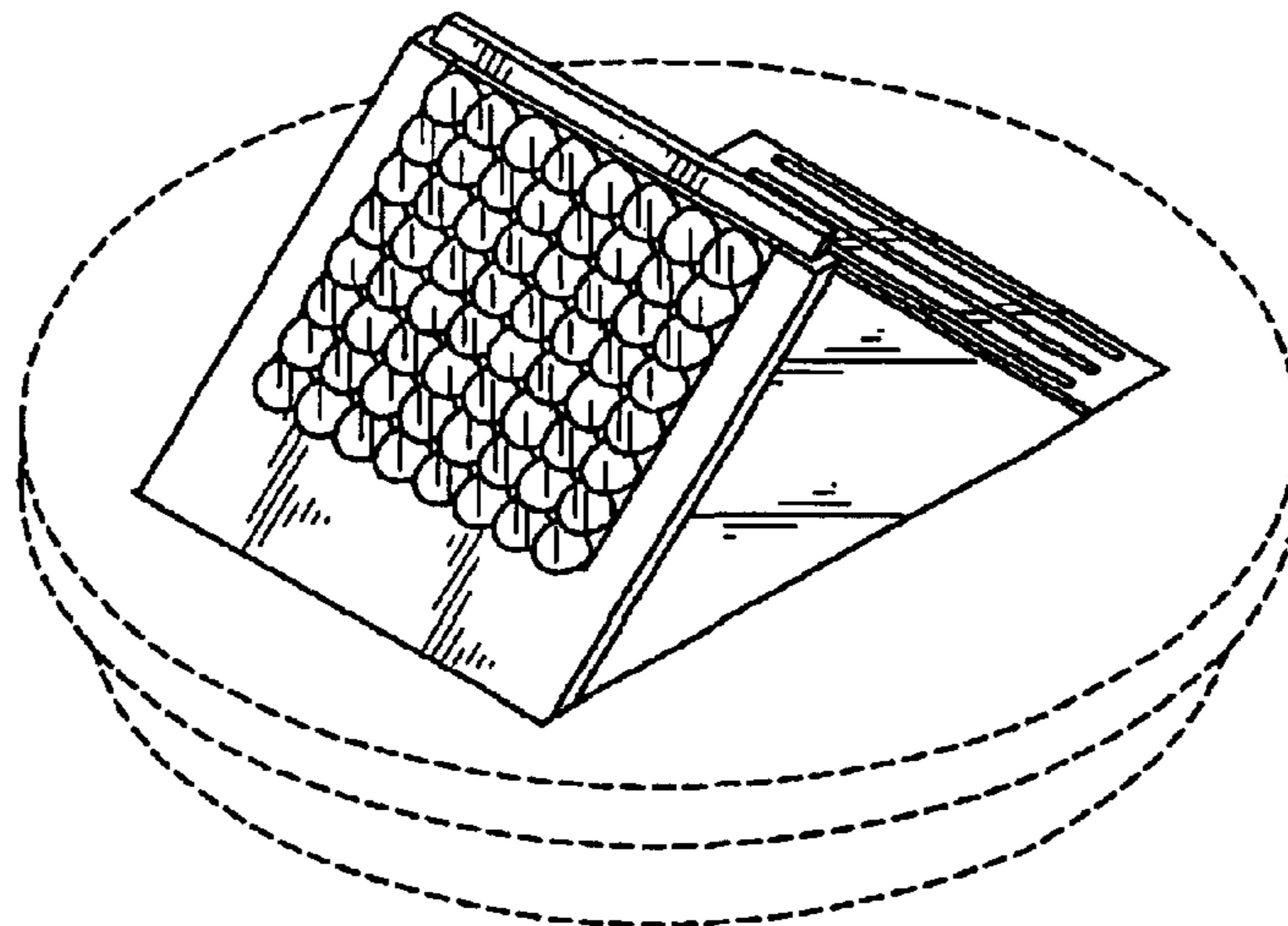
**FIG. 4**



**FIG. 5**



**FIG. 6**



*FIG. 7*

