

US00D494134S1

(12) **United States Design Patent** (10) **Patent No.:** **US D494,134 S**
Murzanski (45) **Date of Patent:** **** Aug. 10, 2004**

(54) **HOUSING FOR AN ELECTRONIC DEVICE**

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(73) Assignee: **Microsoft Corporation**, Redmond, WA
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(**) Term: **14 Years**

(21) Appl. No.: **29/195,879**

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(51) **LOC (7) Cl.** **13-02**

(52) **U.S. Cl.** **D13/110**

(58) **Field of Search** D13/110, 118,
D13/133; 307/150; 361/836; 363/141, 144,
146; 439/170, 176, 171-173, 483, 518,
668, 956

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(57) **CLAIM**

The ornamental design for a housing for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a housing for an electronic device showing my new design;

FIG. 2 is a front view thereof;

FIG. 3 is a rear view thereof;

FIG. 4 is a right view thereof;

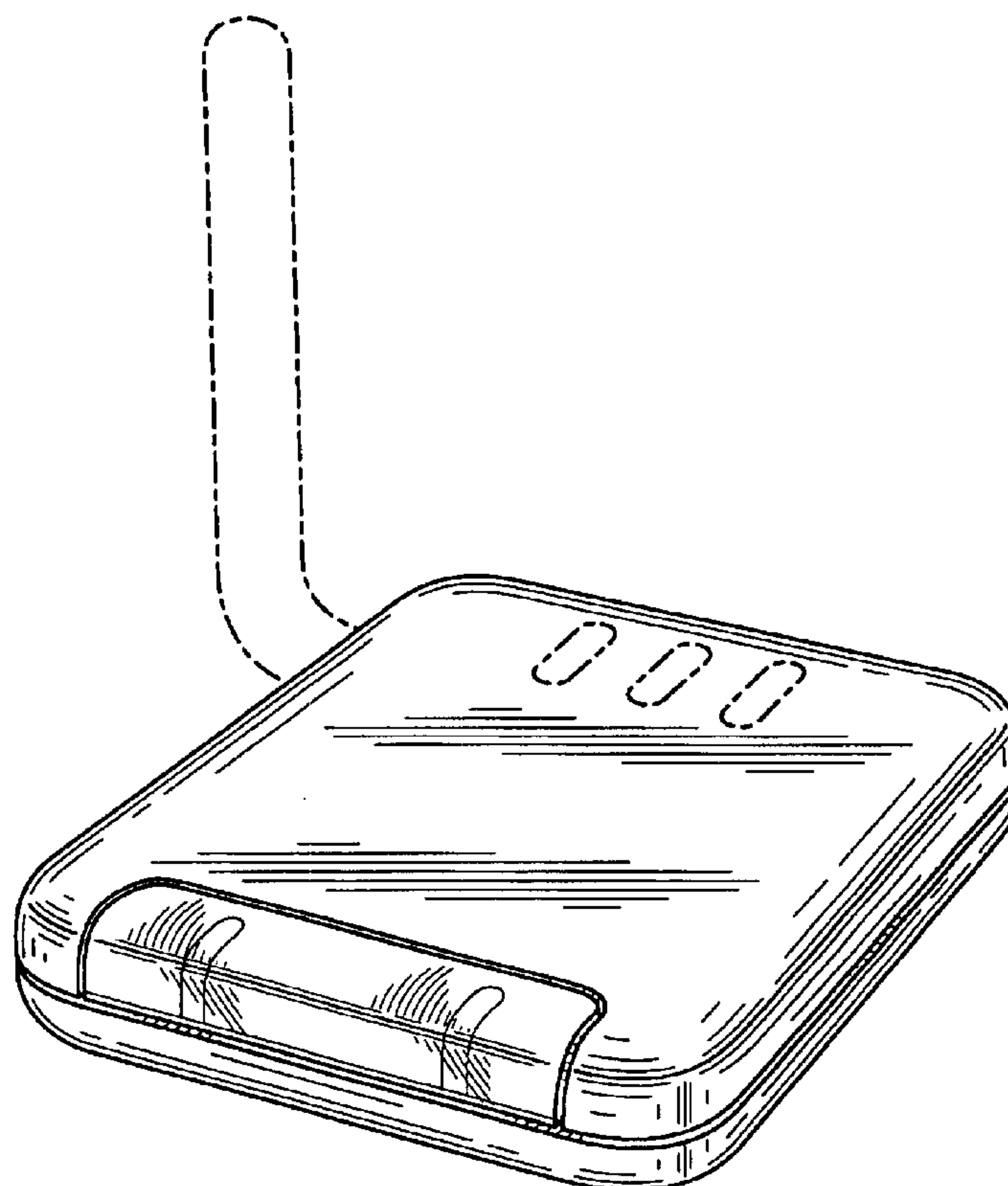
FIG. 5 is a left view thereof; and,

FIG. 6 is a top view thereof.

The broken lines showing of the regions on the rear of the housing, including the unshaded areas within the broken line region, the antenna and antenna connecton on the left of the housing, including the unshaded areas within the broken lines, and the regions on the top of the housing, including the unshaded areas within the broken line regions, are for illustrative purposes only and form no part of the claimed design. The unshown bottom of the housing forms no part of the claimed design.

The broken line showing of the environment is for illustrative purposes only and forms no part of the claimed design. The bottom view of the housing for an electronic device is not shown and forms no part of the claimed design.

1 Claim, 4 Drawing Sheets



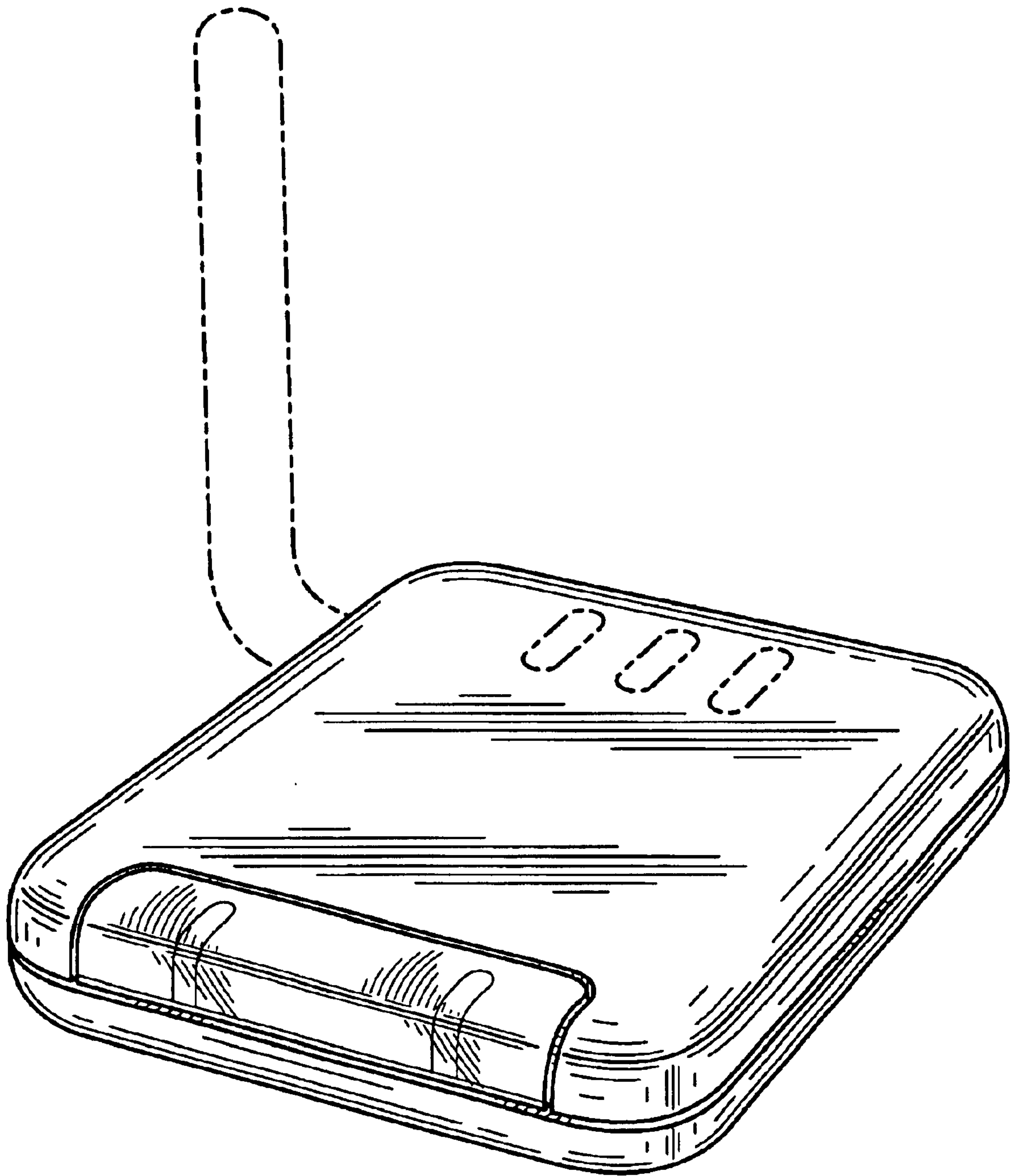


FIG. 1

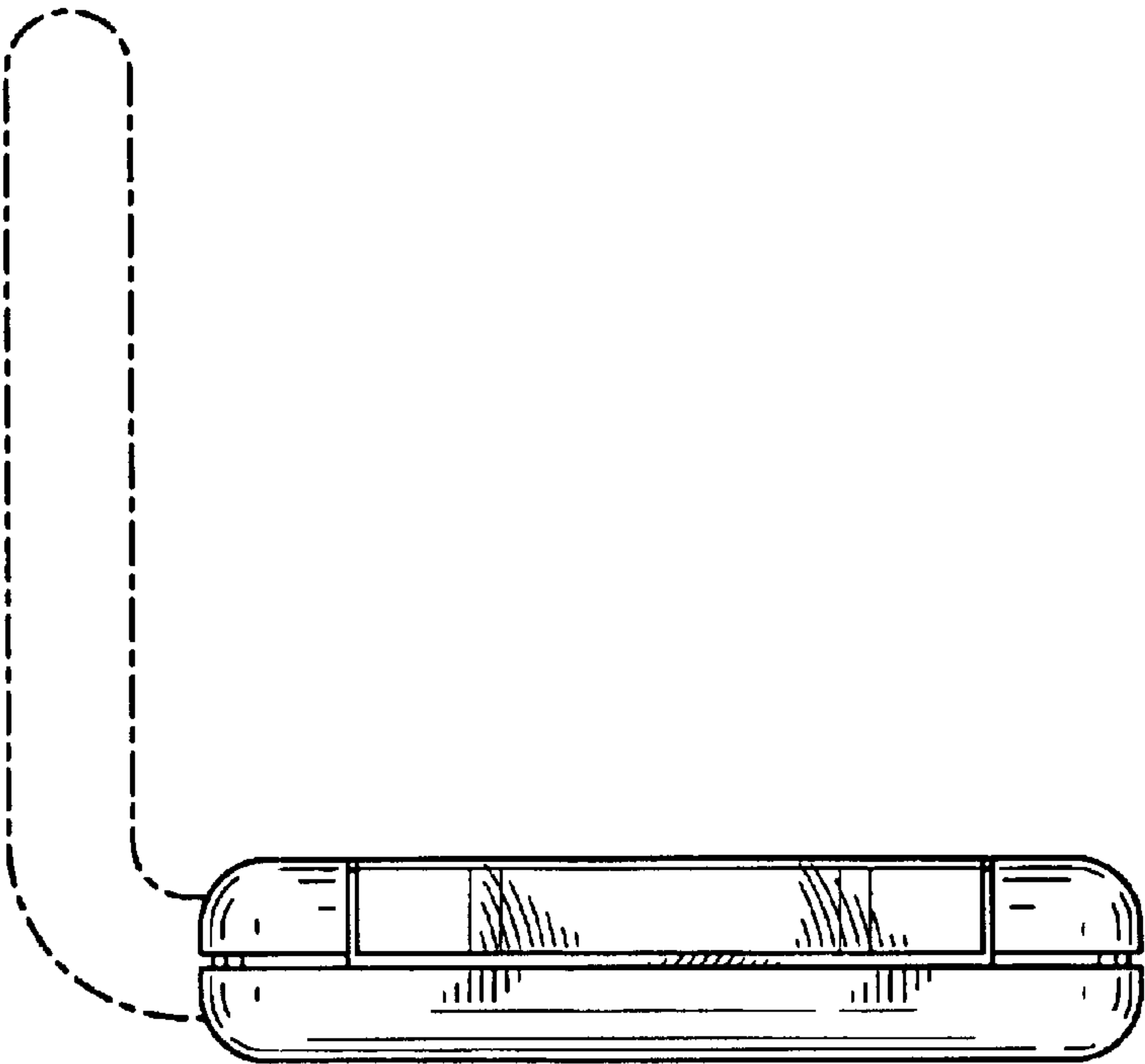


FIG. 2

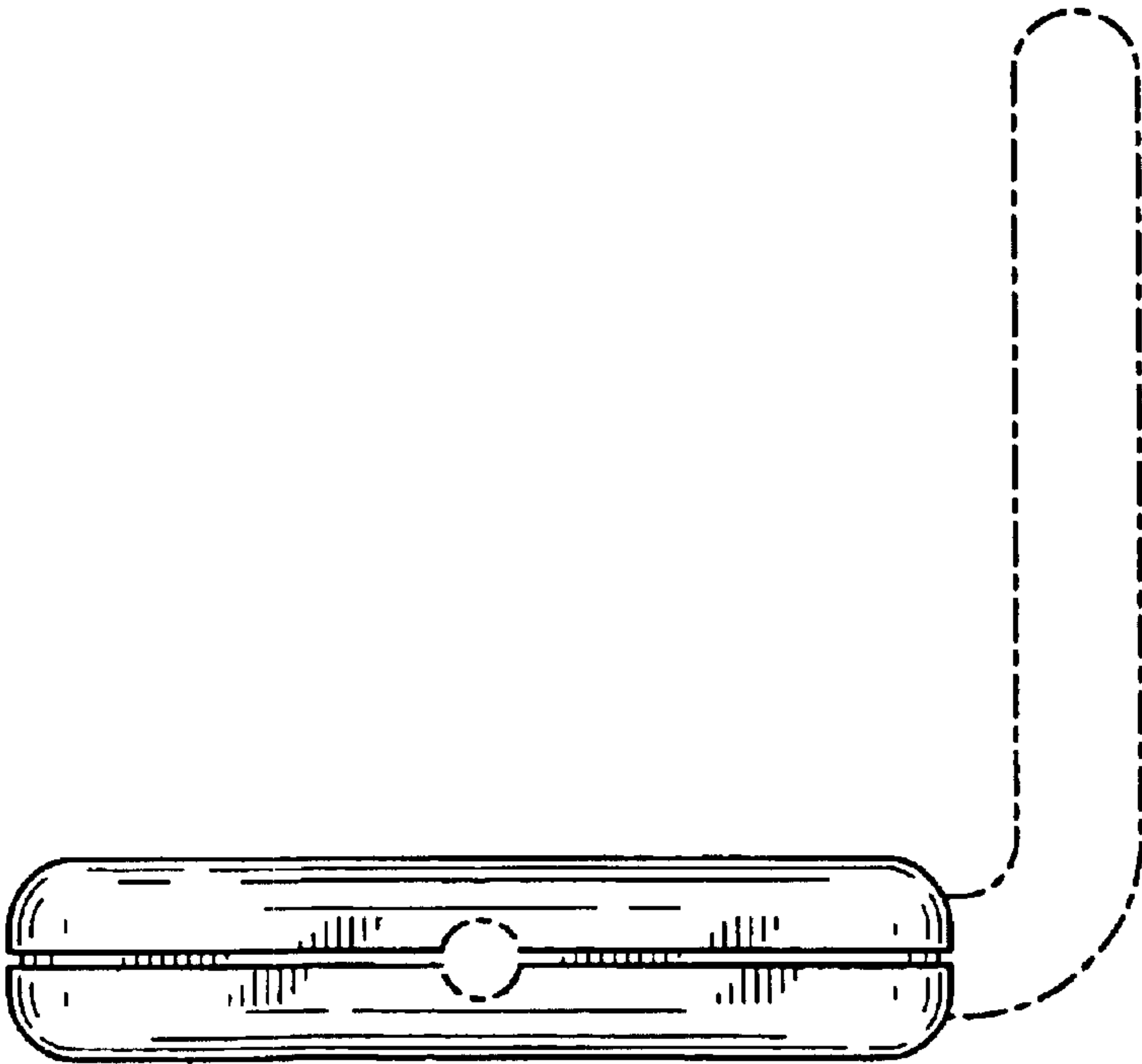


FIG. 3

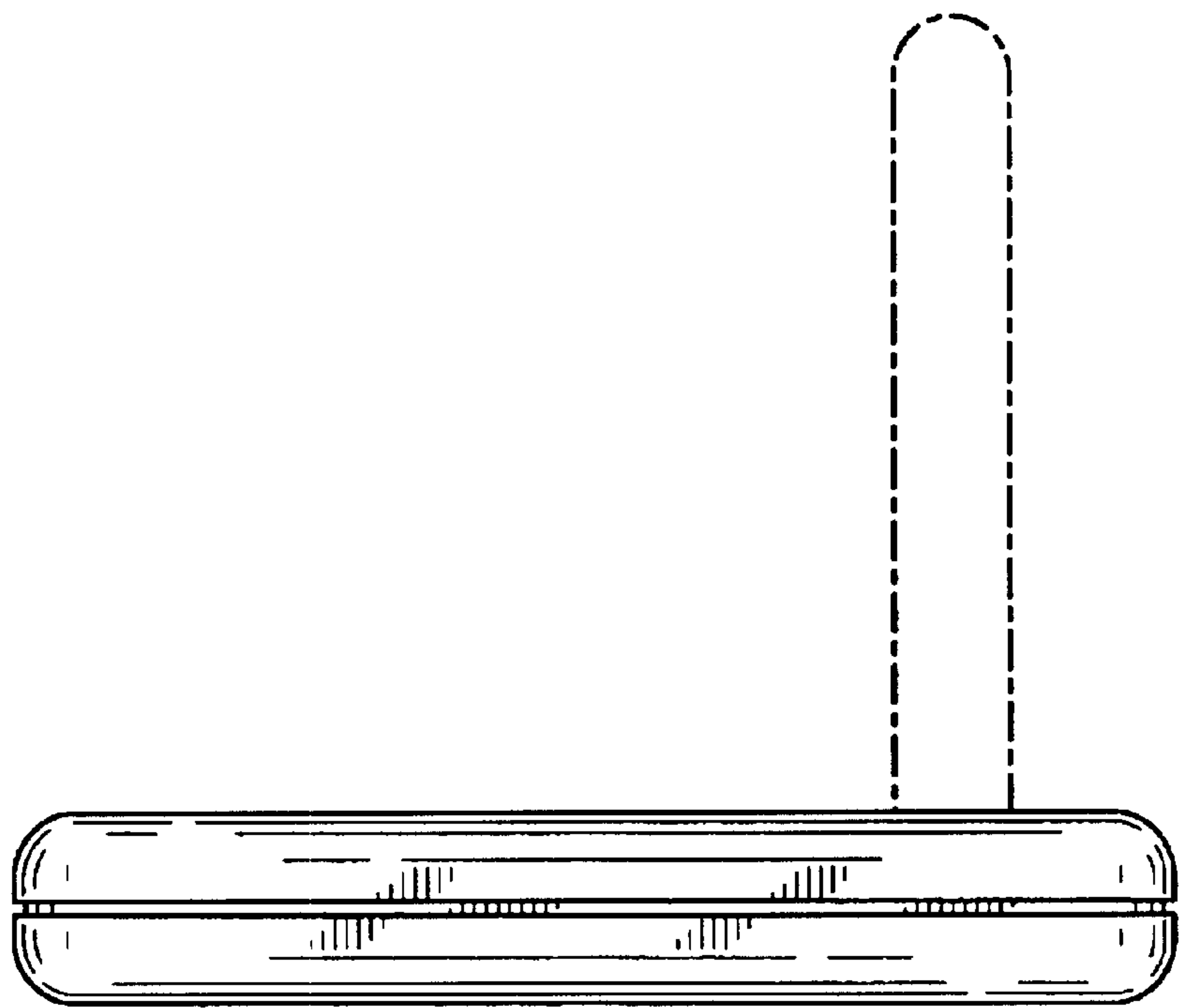


FIG. 4

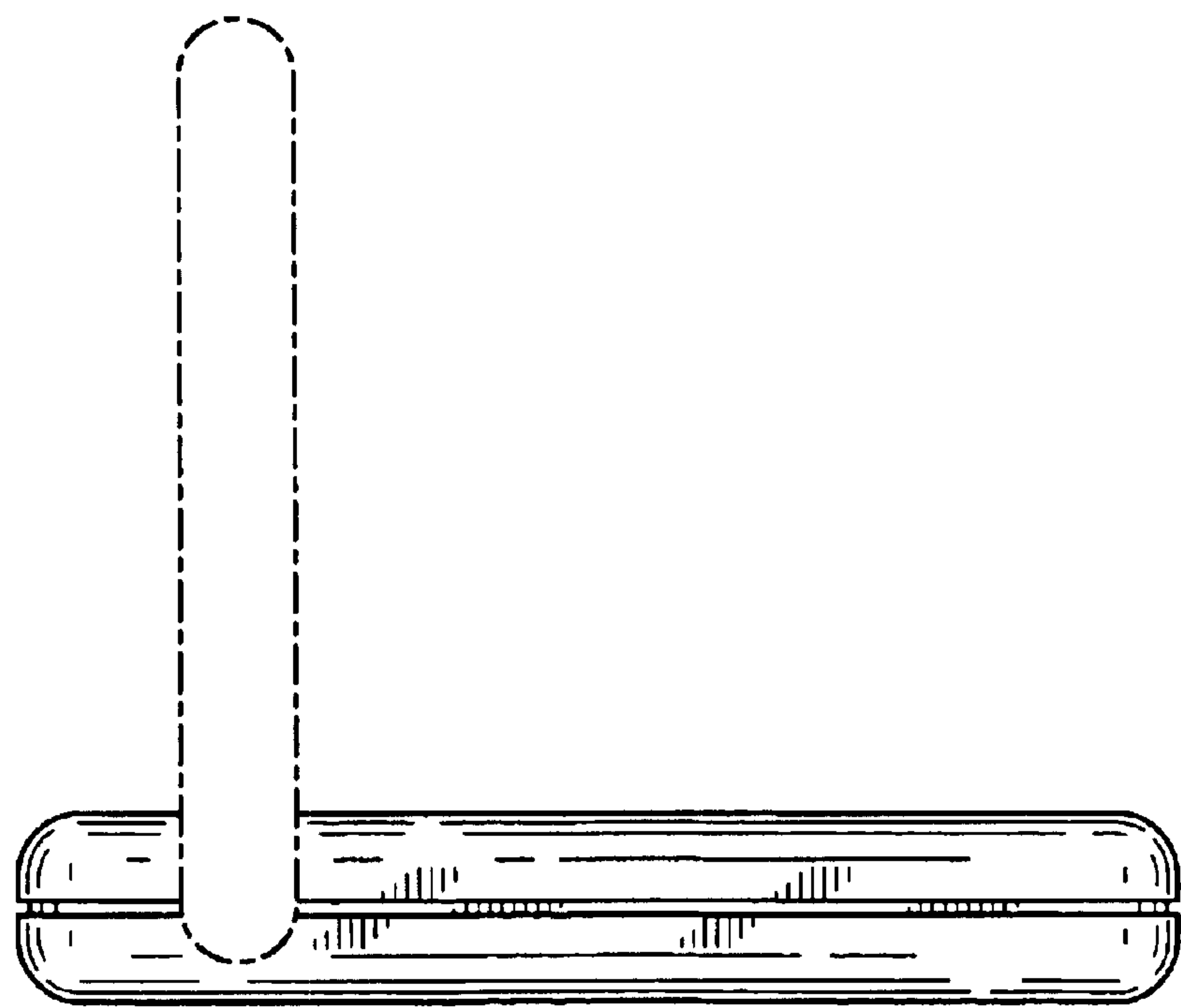


FIG. 5

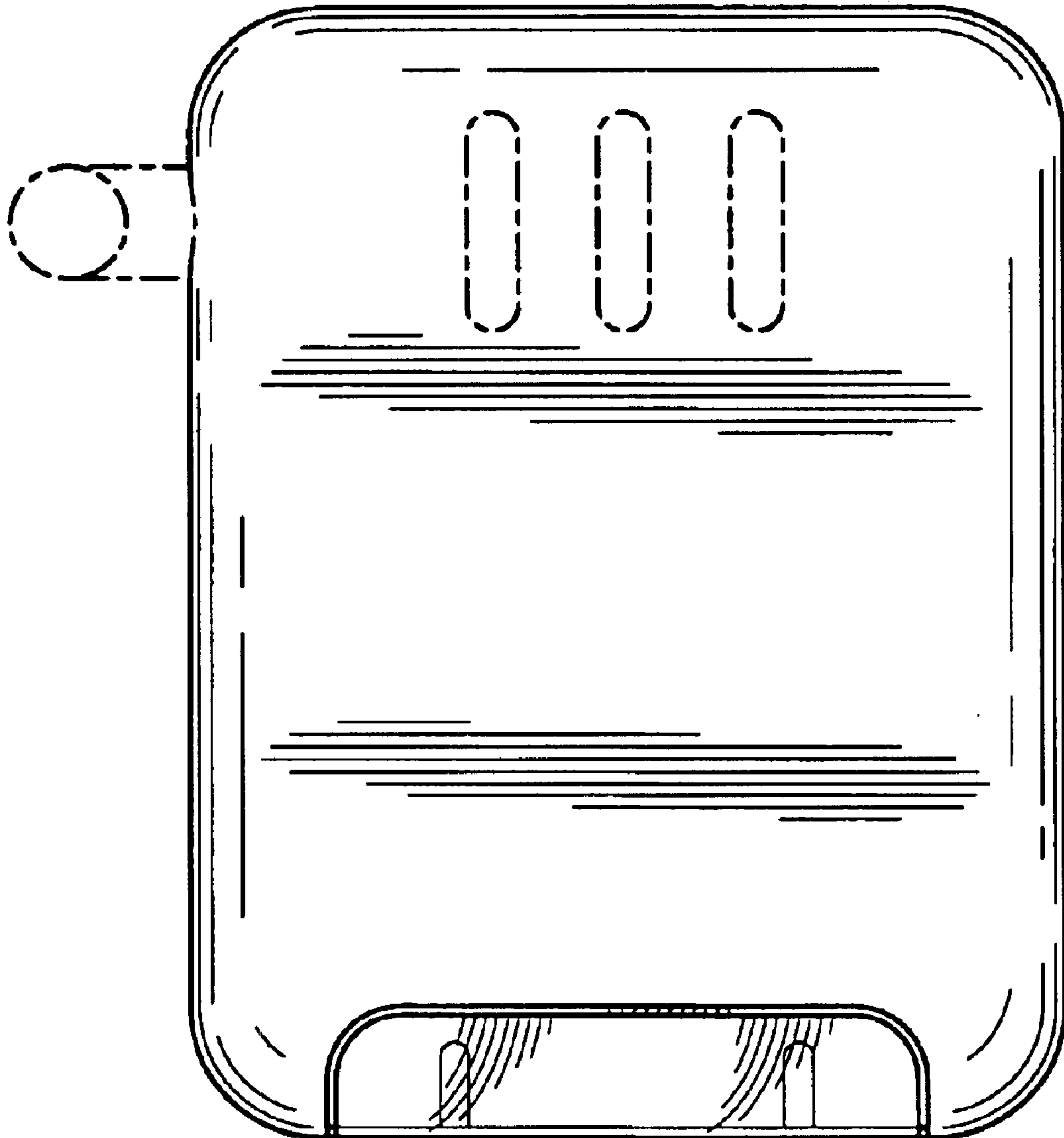


FIG. 6