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(12) **United States Design Patent**  
**Chan et al.**

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(54) **INFORMATION PROCESSOR AND ROUTER**

D463,411 S \* 9/2002 Stokes et al. .... D14/240  
D470,839 S \* 2/2003 Tuhkanen ..... D14/240  
D478,070 S \* 8/2003 Nagai et al. .... D14/240

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**FOREIGN PATENT DOCUMENTS**

GB 2081357 \* 7/1999

\* cited by examiner

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(\*\*) **Term:** **14 Years**

(57) **CLAIM**

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The ornamental design for an information processor and router, as shown and described.

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(51) **LOC (7) Cl.** ..... **14-02**

**DESCRIPTION**

(52) **U.S. Cl.** ..... **D14/240**

FIG. 1 is a front perspective view of an information processor and router showing our new design;  
FIG. 2 is a front elevation thereof;  
FIG. 3 is a top plan view thereof; and,  
FIG. 4 is a right side elevation thereof, the side opposite being a mirror image.  
The broken line showing of environment in the figure drawings is for illustrative purposes only and forms no part of the claimed design.

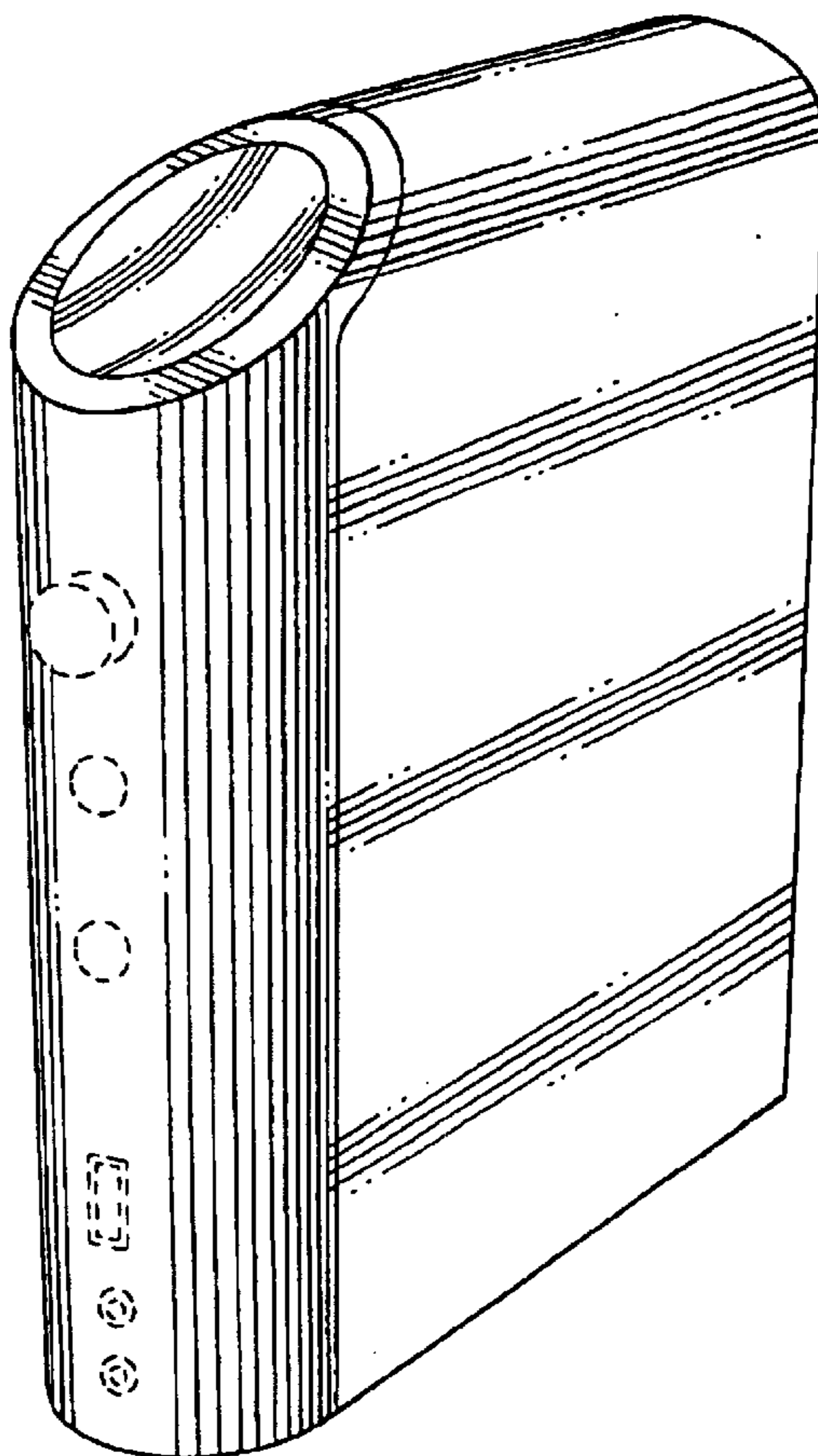
(58) **Field of Search** ..... D14/240, 242,  
D14/3, 553, 140, 358; 379/333–337, 730,  
826, 828, 387, 399.01, 399.02, 413.02,  
413.04, 419, 420, 67.01; 455/19; 361/728,  
730, 600, 622

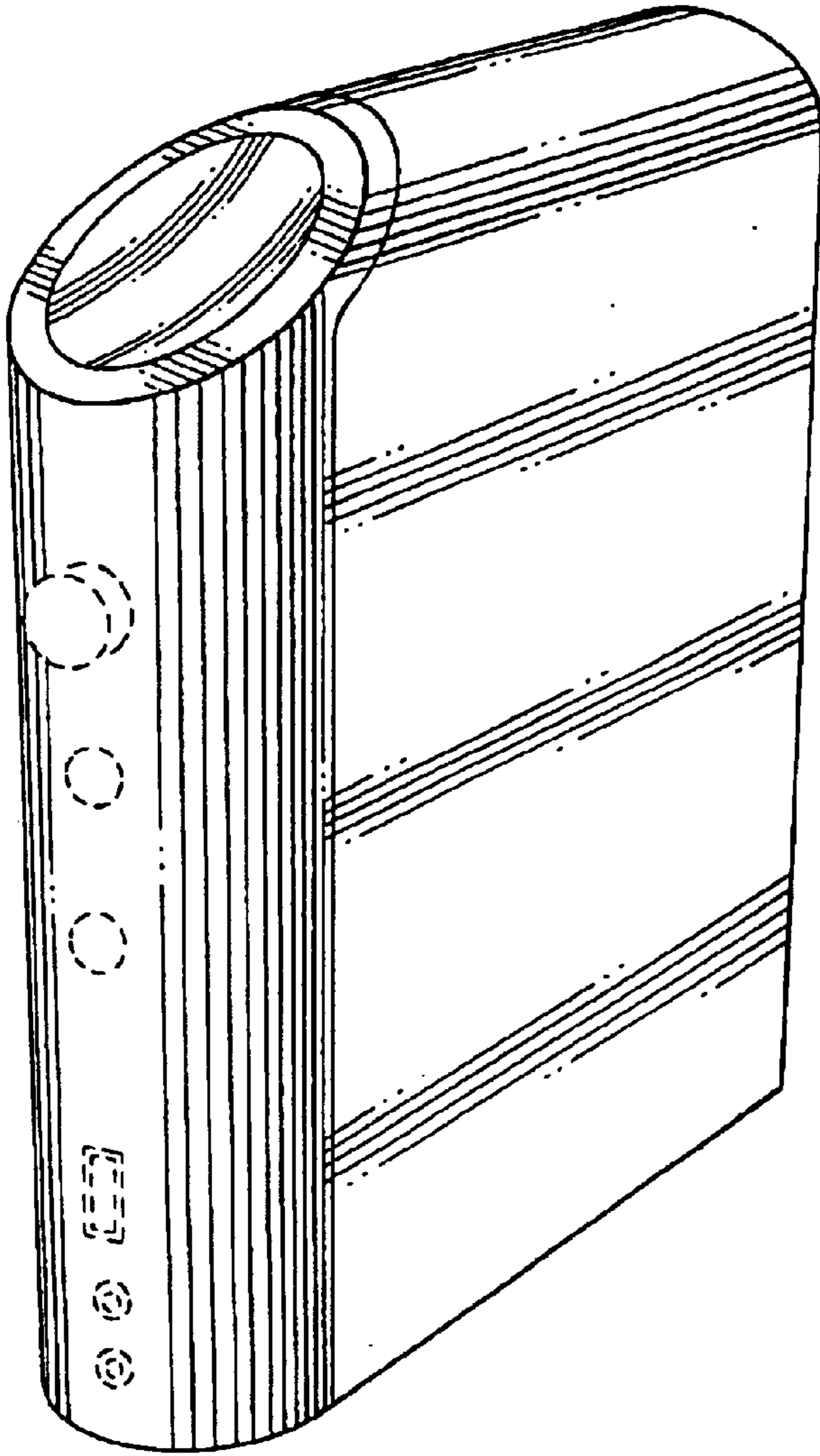
(56) **References Cited**

**U.S. PATENT DOCUMENTS**

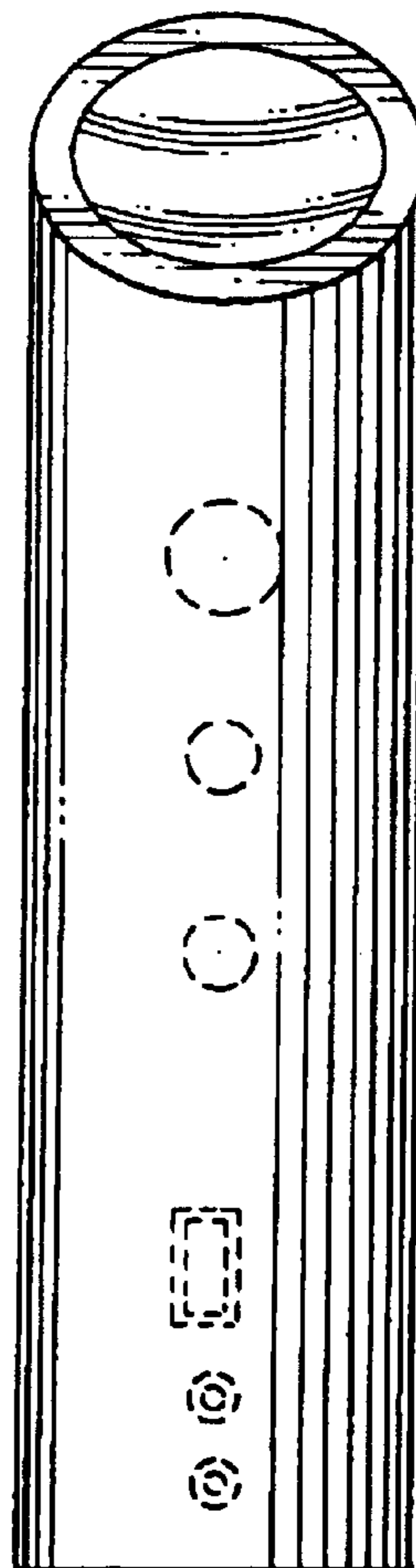
D435,041 S \* 12/2000 Coker et al. .... D14/240  
D438,197 S \* 2/2001 Arpe ..... D14/240  
D441,744 S \* 5/2001 Miyazaki ..... D14/240

**1 Claim, 2 Drawing Sheets**

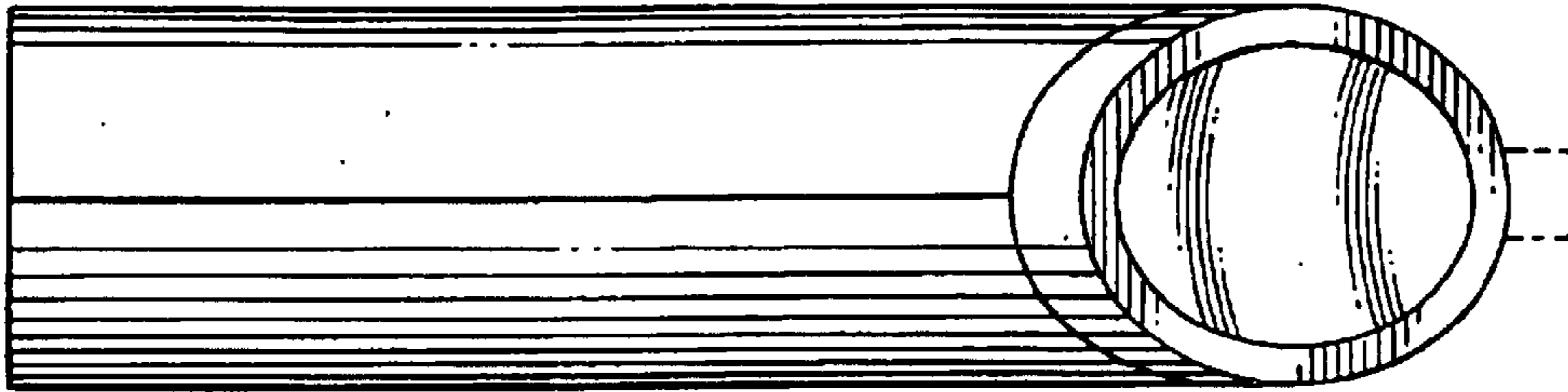




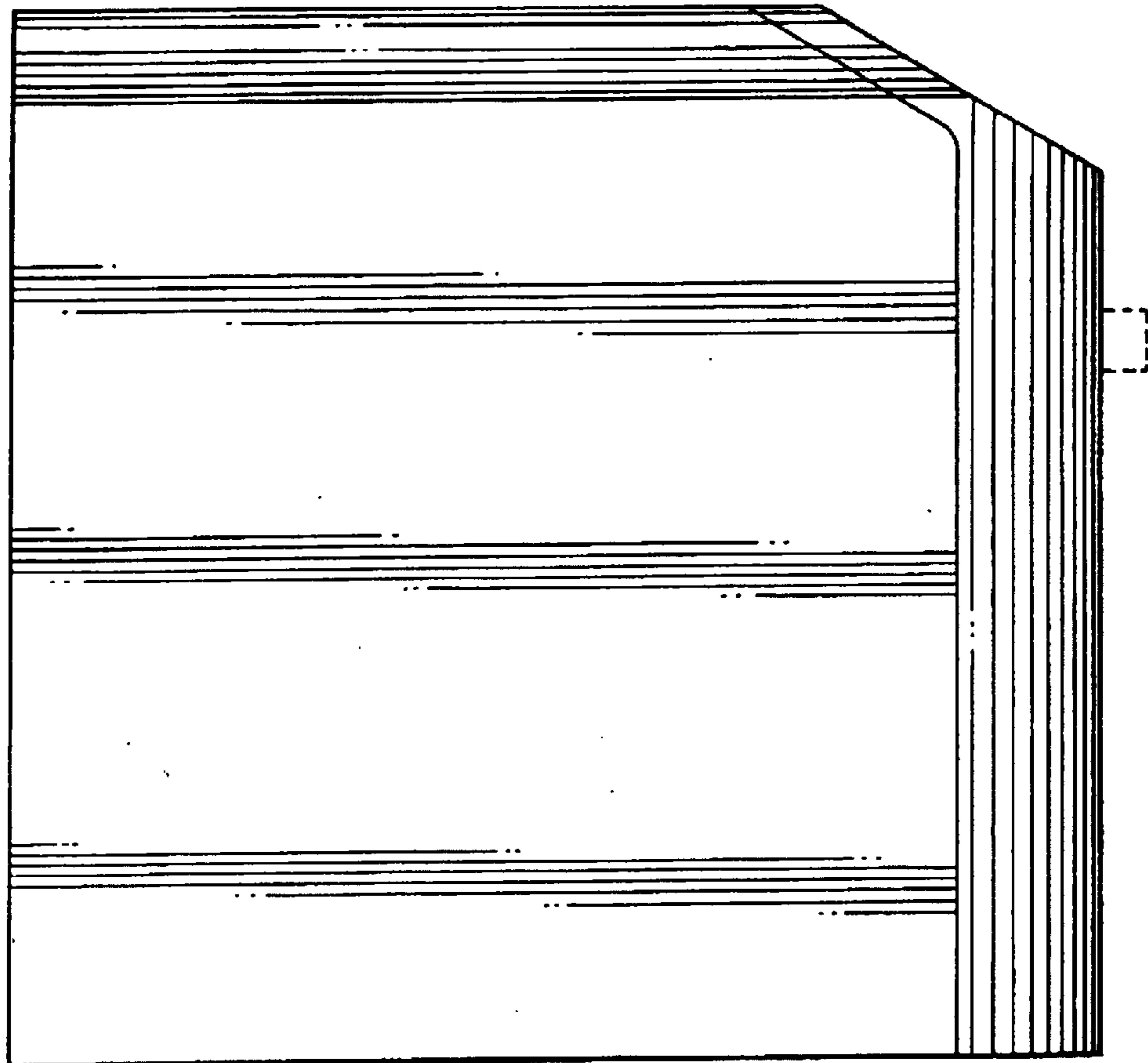
*FIG. 1.*



*FIG. 2.*



*FIG. 3.*



*FIG. 4.*