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(12) **United States Design Patent**  
**Dearborn et al.**

(10) **Patent No.:** **US D493,465 S**  
(45) **Date of Patent:** **\*\* Jul. 27, 2004**

- (54) **COMPUTER SYSTEM STATUS DISPLAY**
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(US)
- (73) Assignee: **Dell Products, L.P.**, Round Rock, TX  
(US)
- (\*\*) Term: **14 Years**

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- (21) Appl. No.: **29/195,146**
- (22) Filed: **Dec. 5, 2003**

**Related U.S. Application Data**

- (62) Division of application No. 29/178,601, filed on Mar. 28, 2003, now Pat. No. Des. 485,842, which is a division of application No. 29/143,122, filed on Jun. 7, 2001, now Pat. No. Des. 476,009.

- (51) **LOC (7) Cl.** ..... **14-02**
- (52) **U.S. Cl.** ..... **D14/432**
- (58) **Field of Search** ..... D14/441-446,  
D14/239, 371, 432; D13/162, 184, 199;  
312/223.2; 361/680-686, 690-696; 340/691.1

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(57) **CLAIM**

The ornamental design for a computer system status display, as shown and described.

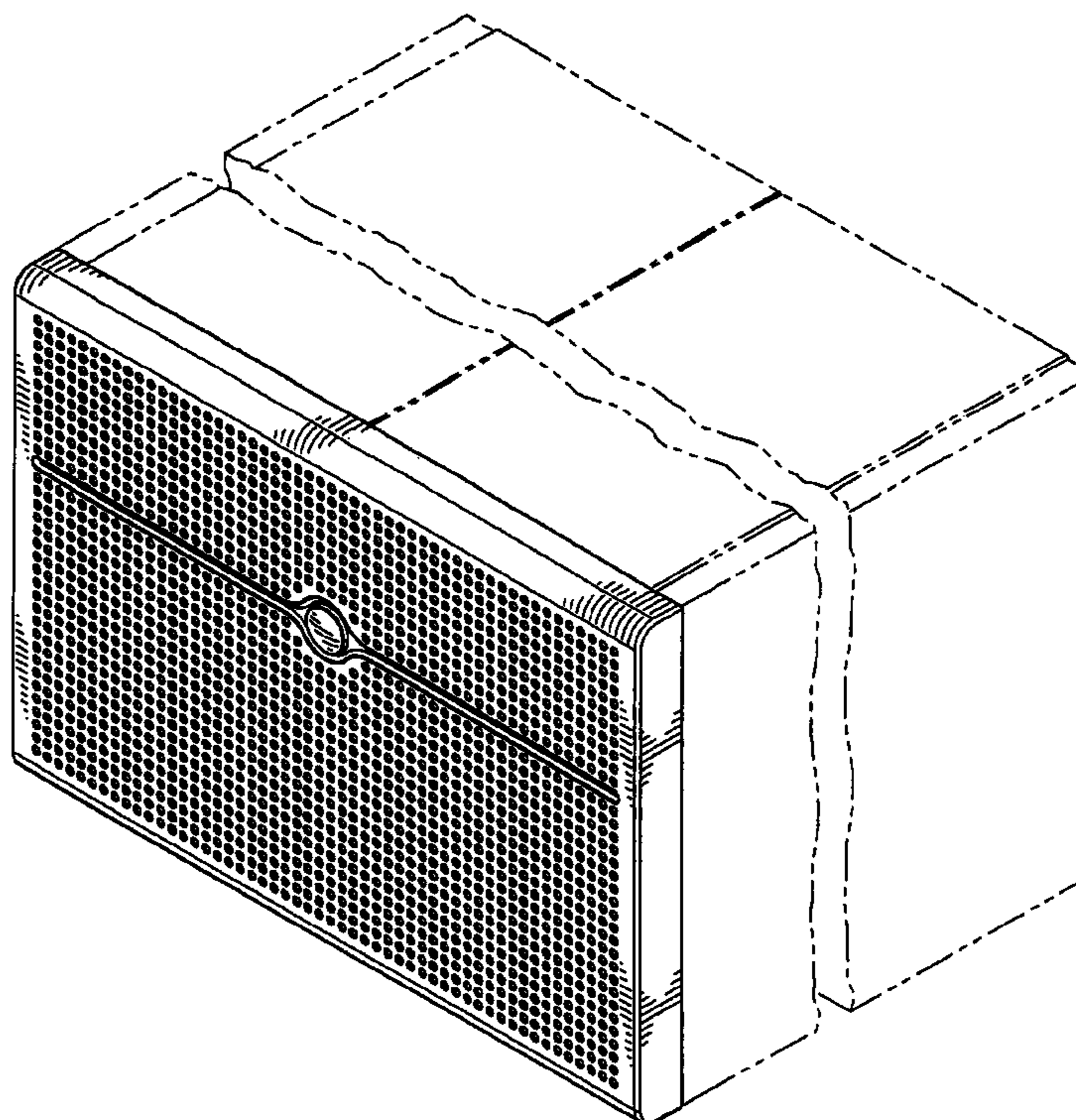
**DESCRIPTION**

This application is related to U.S. utility patent application titled "System and Method for Displaying Computer System Status Information" by Timothy C. Dearborn and Shawn P. Hoss, U.S. application Ser. No. 09/876,594, which is pending herewith and which is incorporated herein by reference in its entirety for all purposes.

FIG. 1 is a front elevational view of a computer system status display; and,

FIG. 2 is a perspective view of the computer system status display wherein the computer system is shown in broken lines for illustrative purposes only and forms no part of the claimed design.

**1 Claim, 2 Drawing Sheets**



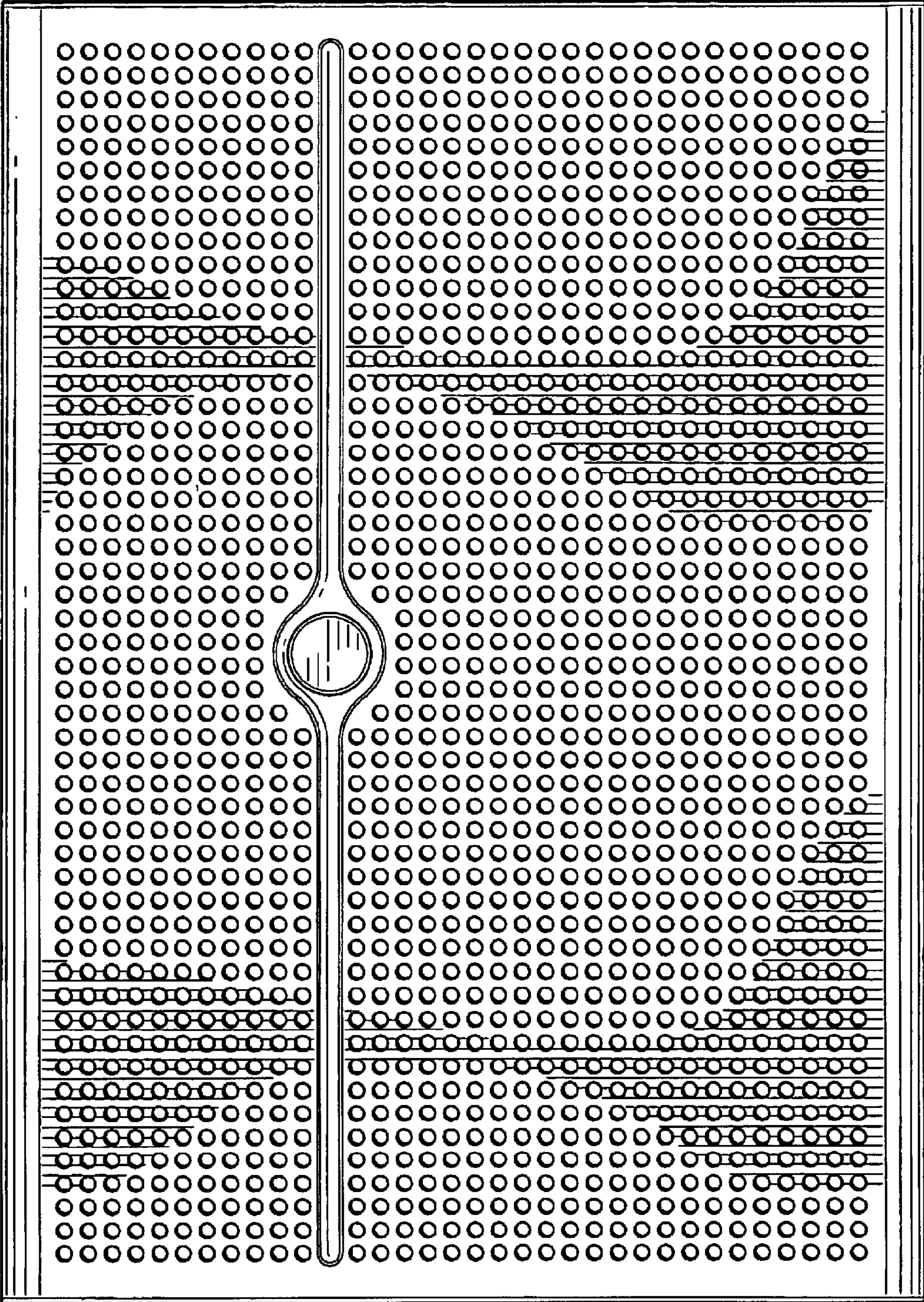


FIG. 1

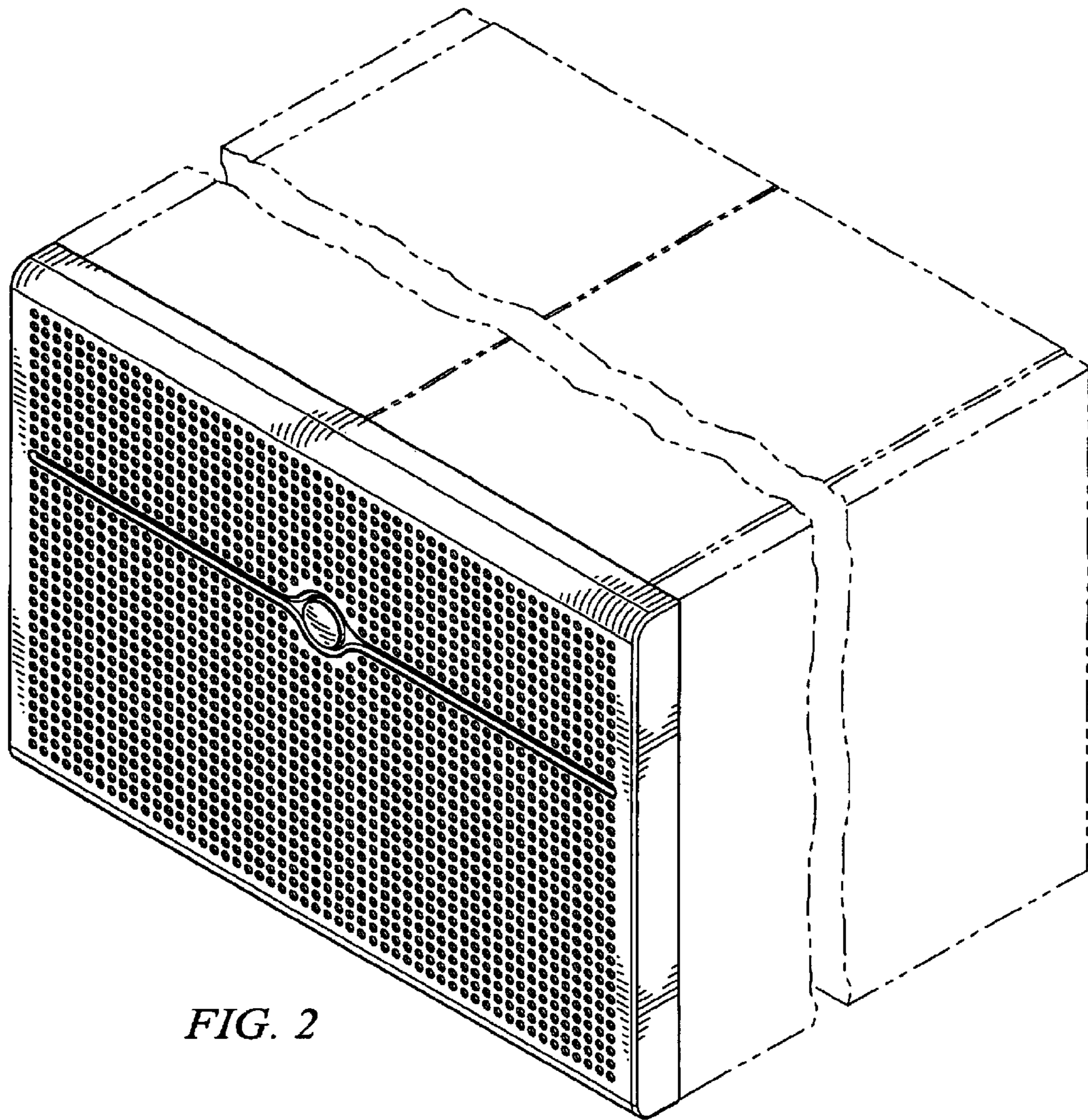


FIG. 2