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(12) **United States Design Patent** (10) **Patent No.:** **US D493,151 S**
Lee (45) **Date of Patent:** **** Jul. 20, 2004**

(54) **HEAT-CONDUCTING BLOCK OF VGA
CHIPSET COOLING DEVICE**

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(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (7) Cl.** **13-03**

(52) **U.S. Cl.** **D13/179**

(58) **Field of Search** D13/179; 361/688,
361/696-97, 700, 709

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(57) **CLAIM**

I claim the ornamental design for a heat-conducting block of
VGA chipset cooling device, as shown and described in the
attached six drawings.

DESCRIPTION

FIG. 1 is a perspective view of a heat-conducting block of
VGA chipset cooling device according to the present inven-
tion.

FIG. 2 is a front view of the heat-conducting block accord-
ing to the present invention; the rear view of the heat-
conducting block is a mirror image of FIG. 2.

FIG. 3 is a right side view of the heat-conducting block
according to the present invention; the left side view of the
heat-conducting block is a mirror image of FIG. 3.

FIG. 4 is a top plan view of the heat-conducting block
according to the present invention.

FIG. 5 is a bottom plan view of the heat-conducting block
according to the present invention; and,

FIG. 6 is a perspective view illustrating use of the heat-
conducting block according to the present invention.

The broken lines in the figures indicate environmental
elements for illustrative purposes only and form no part of
the claimed design.

1 Claim, 4 Drawing Sheets

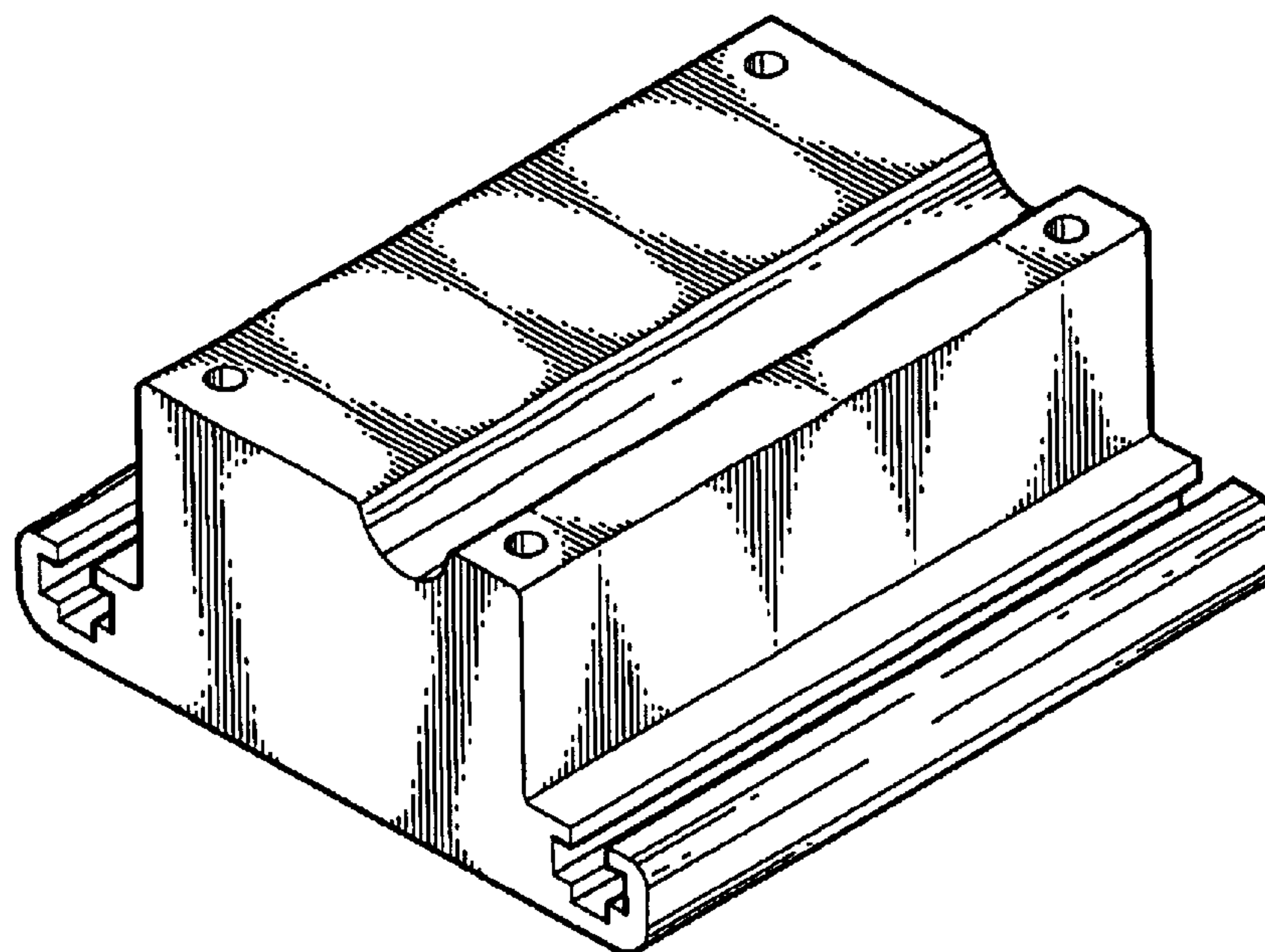


FIG. 1

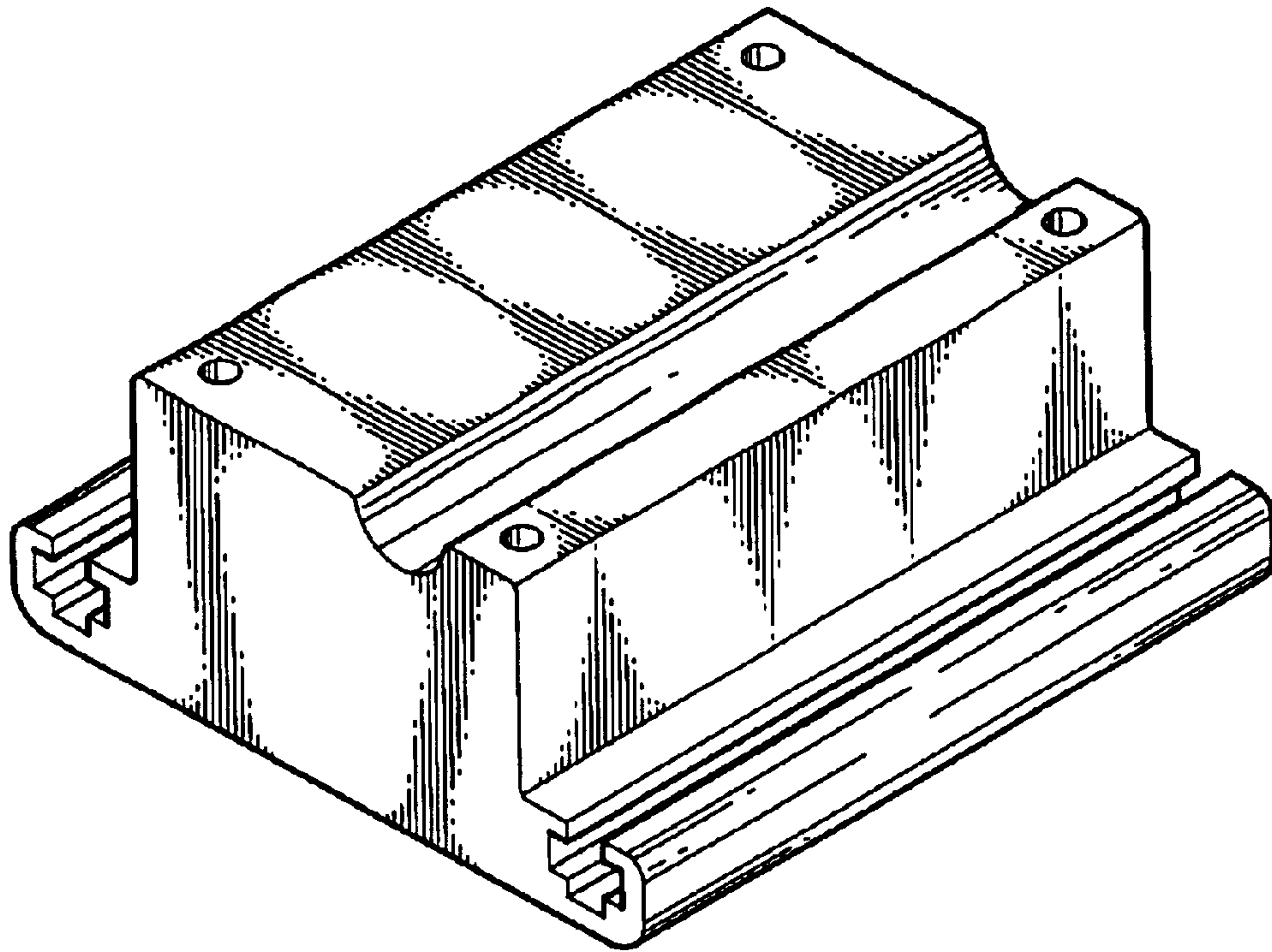


FIG. 2

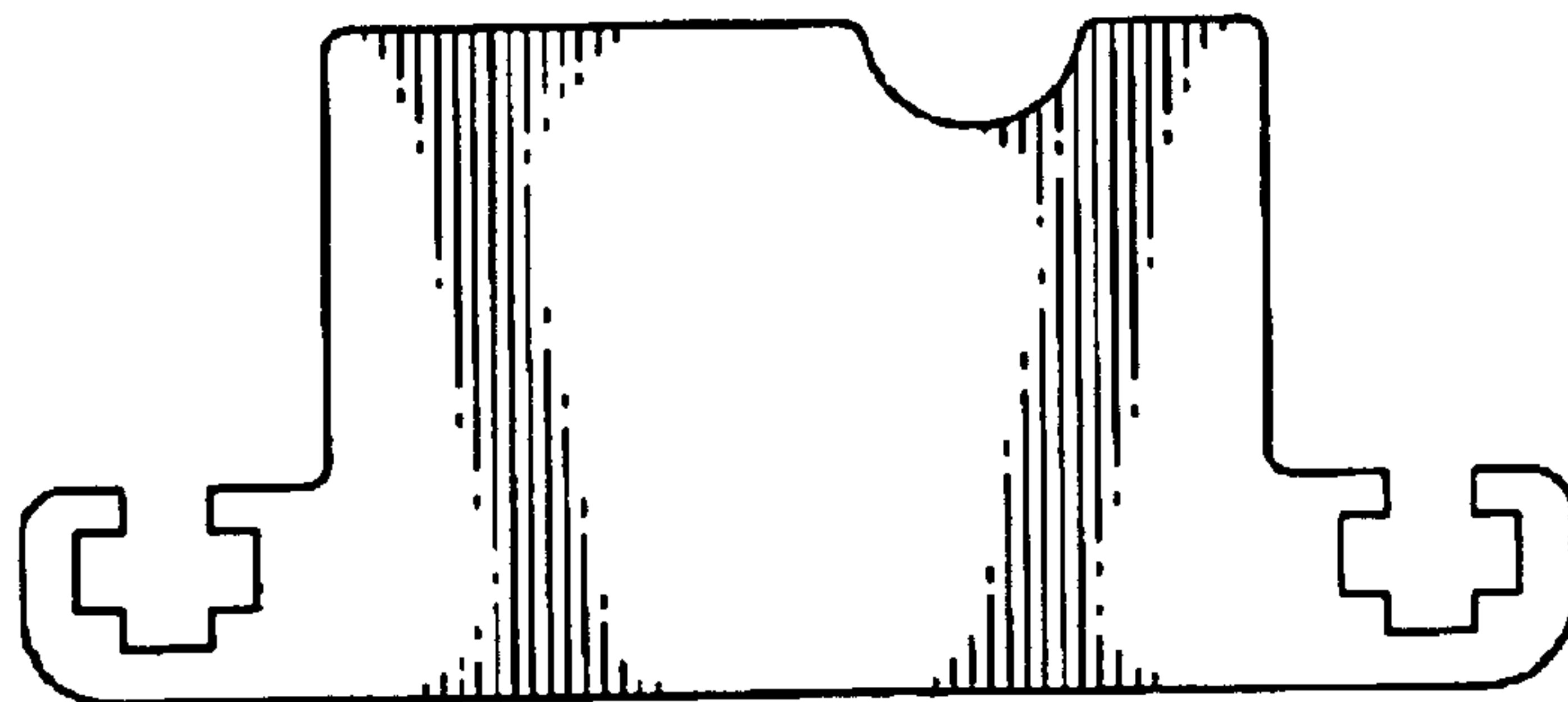


FIG. 3

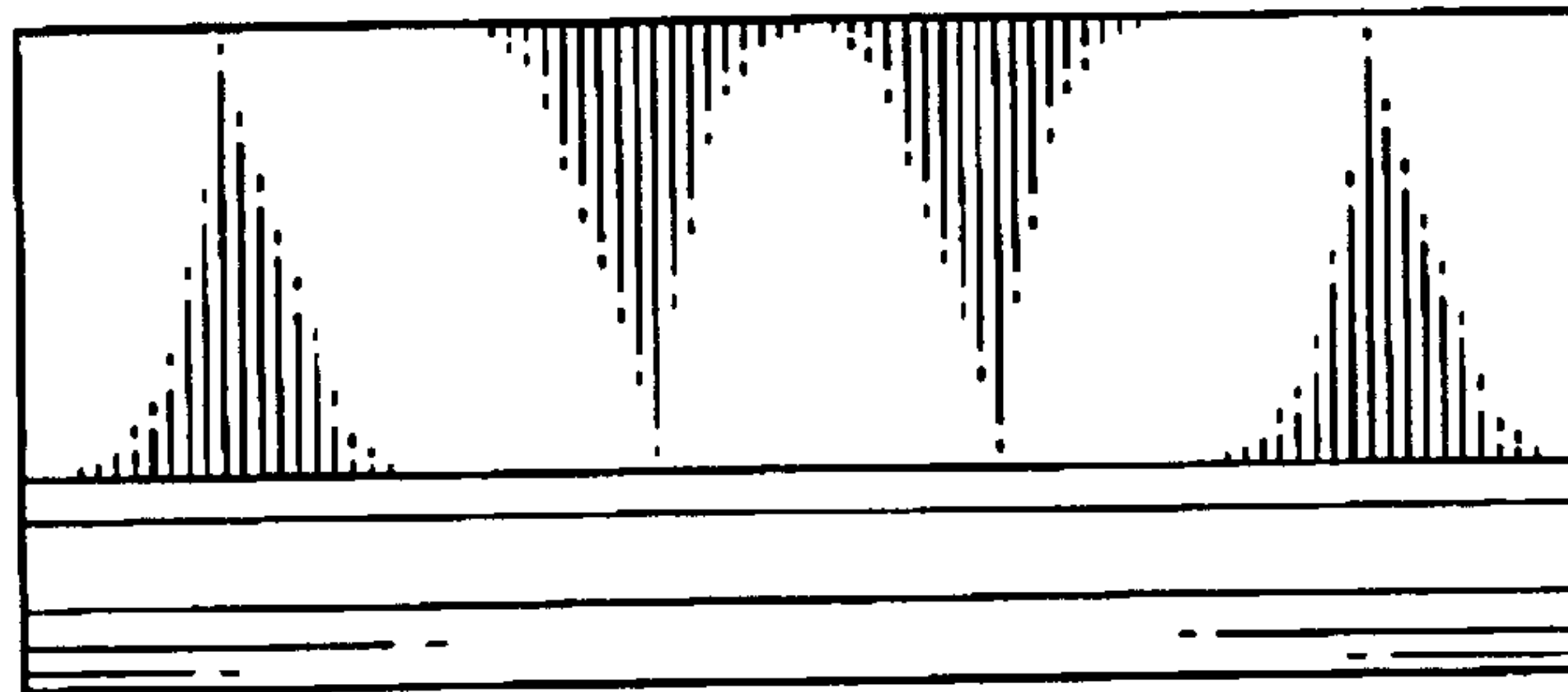


FIG. 4

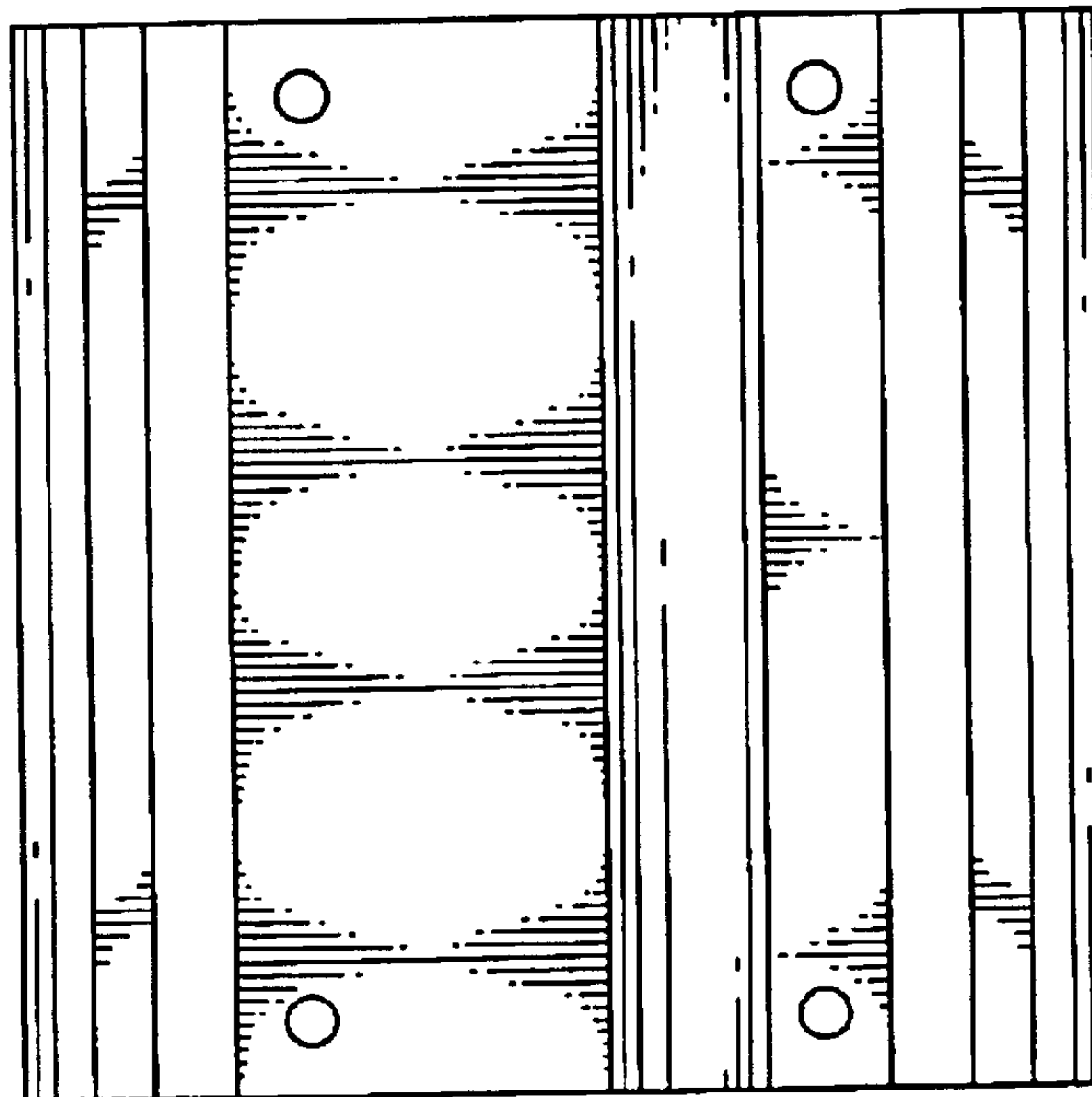


FIG. 5

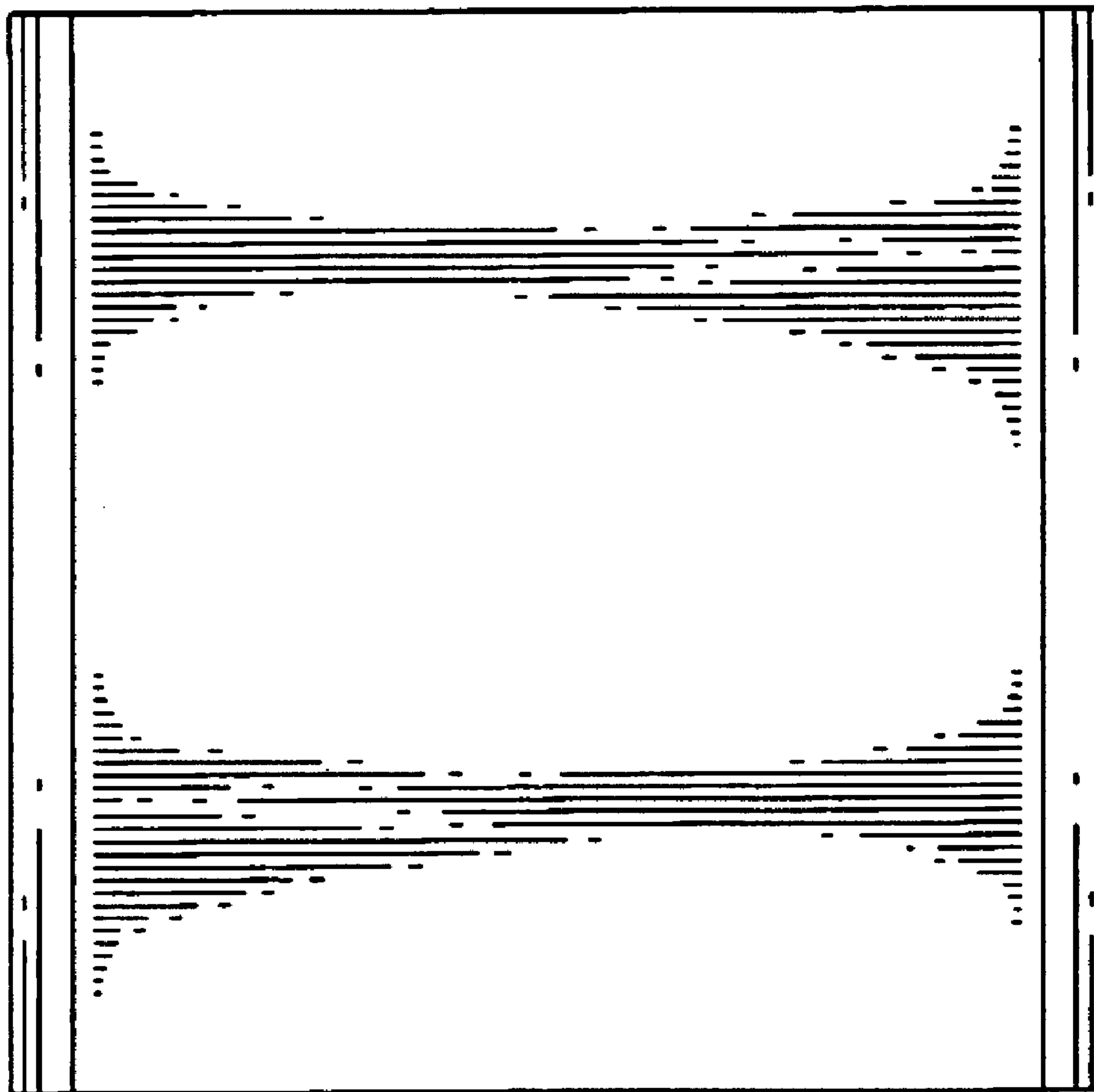


FIG. 6

