



US00D491178S

(12) **United States Design Patent**  
**Kataoka**

(10) **Patent No.:** **US D491,178 S**

(45) **Date of Patent:** **\*\* Jun. 8, 2004**

(54) **COMPUTER**

D452,861 S 1/2002 Ogasawara

(75) Inventor: **Tetsu Kataoka**, Tokyo (JP)

\* cited by examiner

(73) Assignee: **Sony Corporation**, Tokyo (JP)

*Primary Examiner*—Freda S. Nunn

(\*\*) Term: **14 Years**

(74) *Attorney, Agent, or Firm*—Rader, Fishman & Grauer PLLC

(21) Appl. No.: **29/181,459**

(57) **CLAIM**

(22) Filed: **May 13, 2003**

The ornamental design for a computer, as shown and described.

(51) **LOC (7) Cl.** ..... **14-02**

**DESCRIPTION**

(52) **U.S. Cl.** ..... **D14/318**

(58) **Field of Search** ..... D14/315, 318–320,  
D14/322, 323, 327; D18/1, 2, 7; 235/145 A,  
145 R; 341/22, 23; 345/104, 156, 168,  
169, 173; 361/680–686

FIG. 1 is a perspective view of a computer showing my new design;

FIG. 2 is a top plan view thereof;

FIG. 3 is a left side elevational view thereof;

FIG. 4 is a right side elevational view thereof; and,

FIG. 5 is a front elevational view thereof.

Portions in broken lines in FIGS. 1–5 are for illustrative purposes only and form no part of the claimed design.

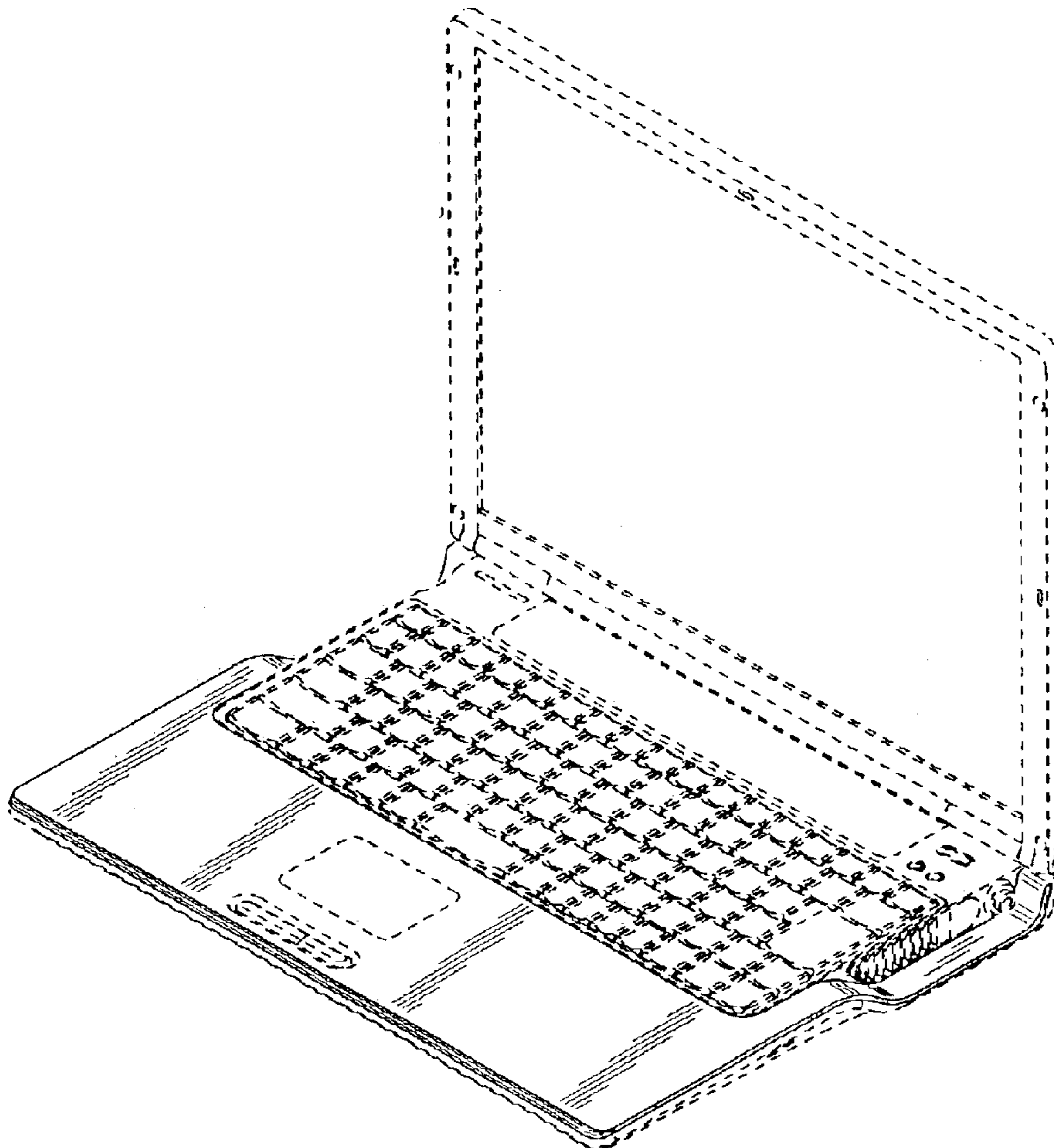
(56) **References Cited**

**U.S. PATENT DOCUMENTS**

D391,927 S \* 3/1998 Faranda et al. .... D14/318

D402,968 S \* 12/1998 Wolff et al. .... D14/318

**1 Claim, 4 Drawing Sheets**



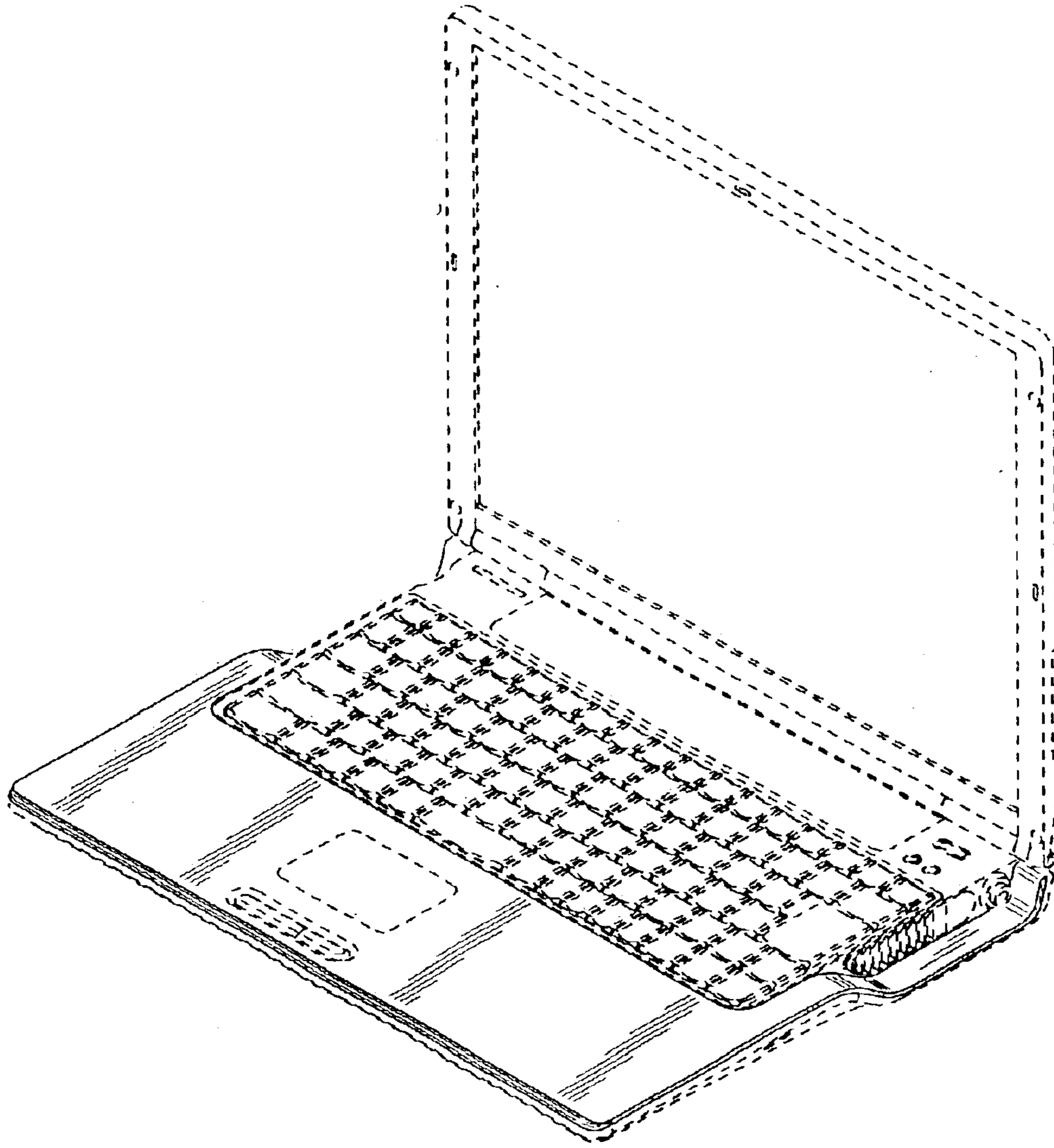


FIG. 1

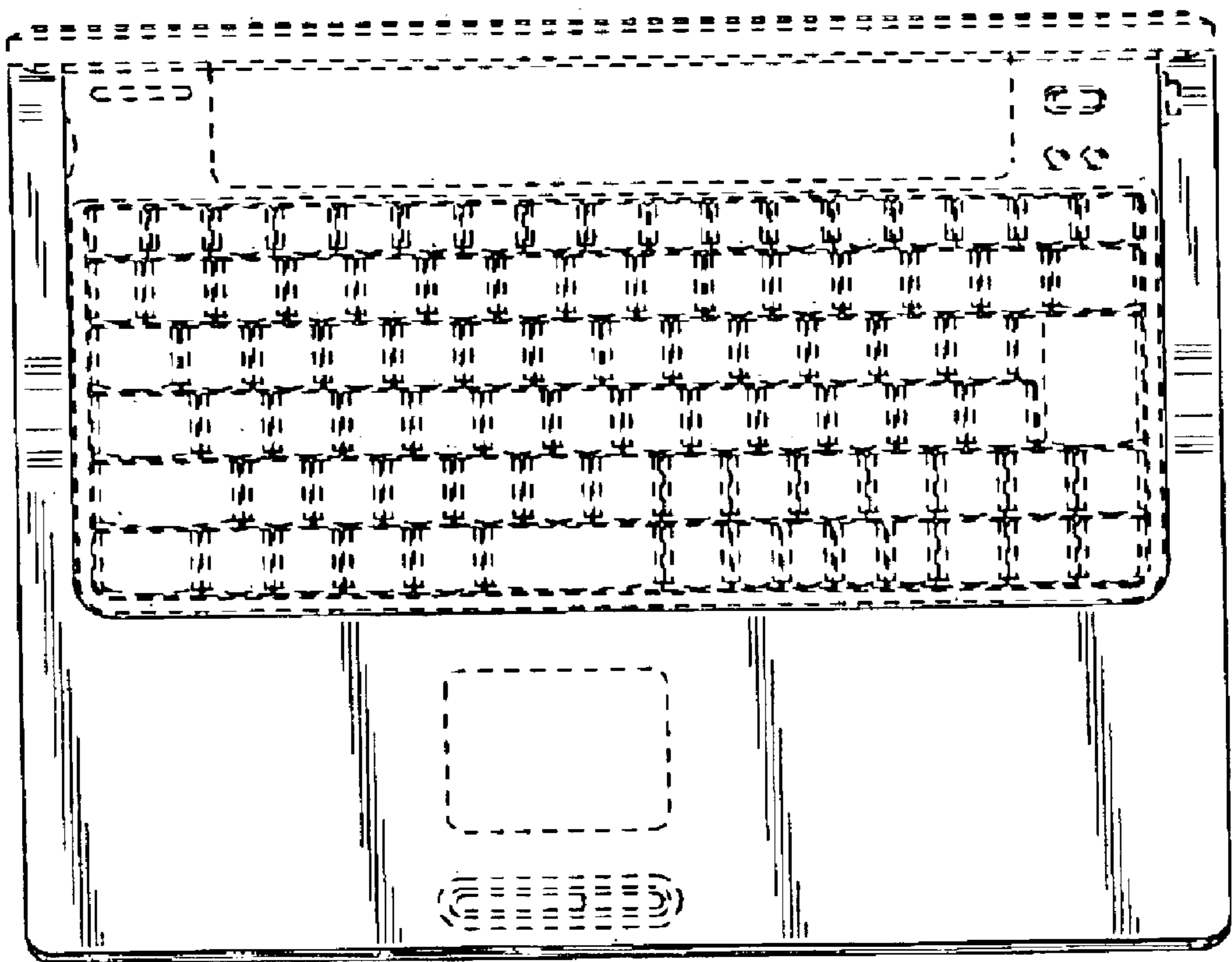


FIG. 2

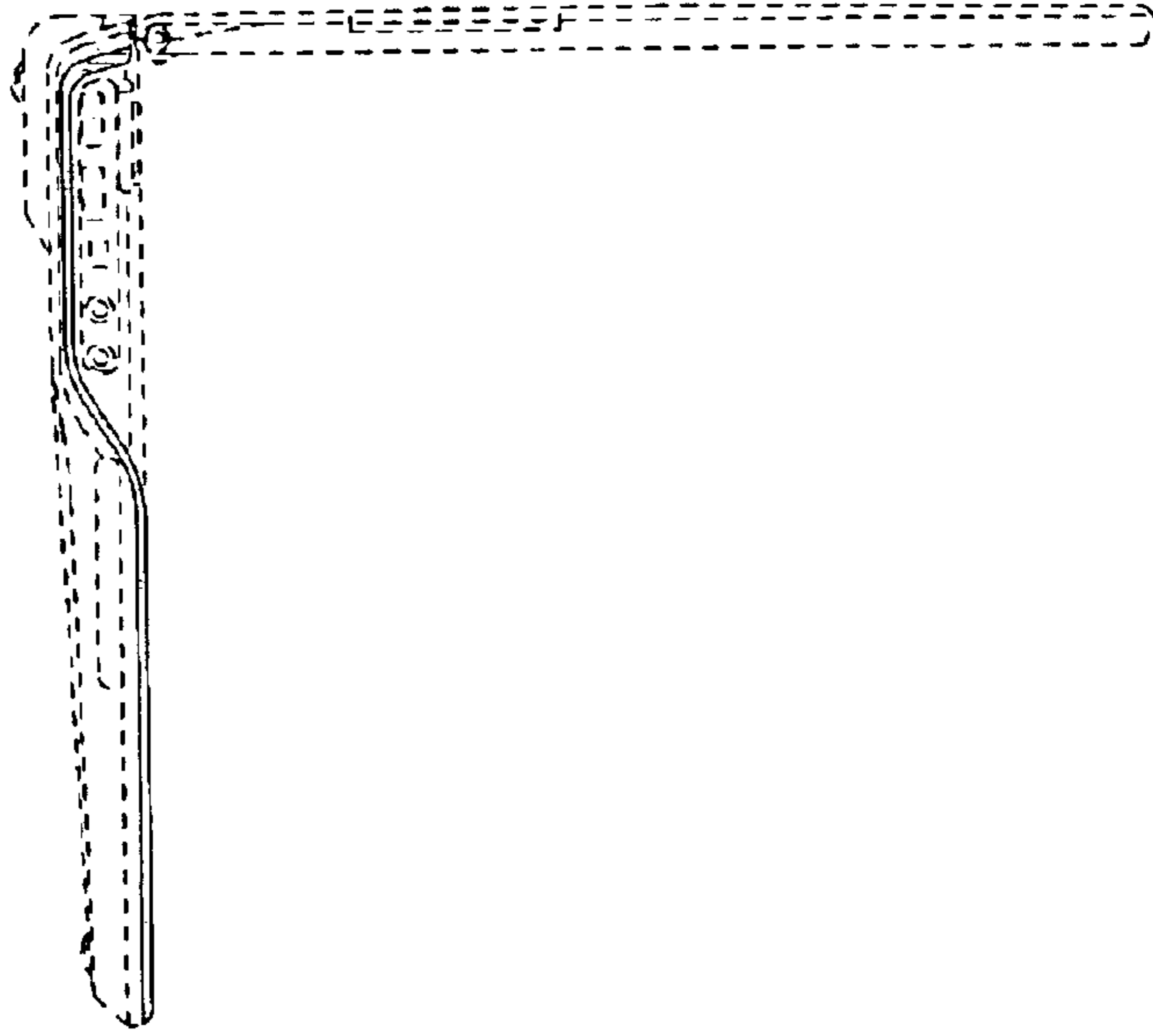


FIG. 3

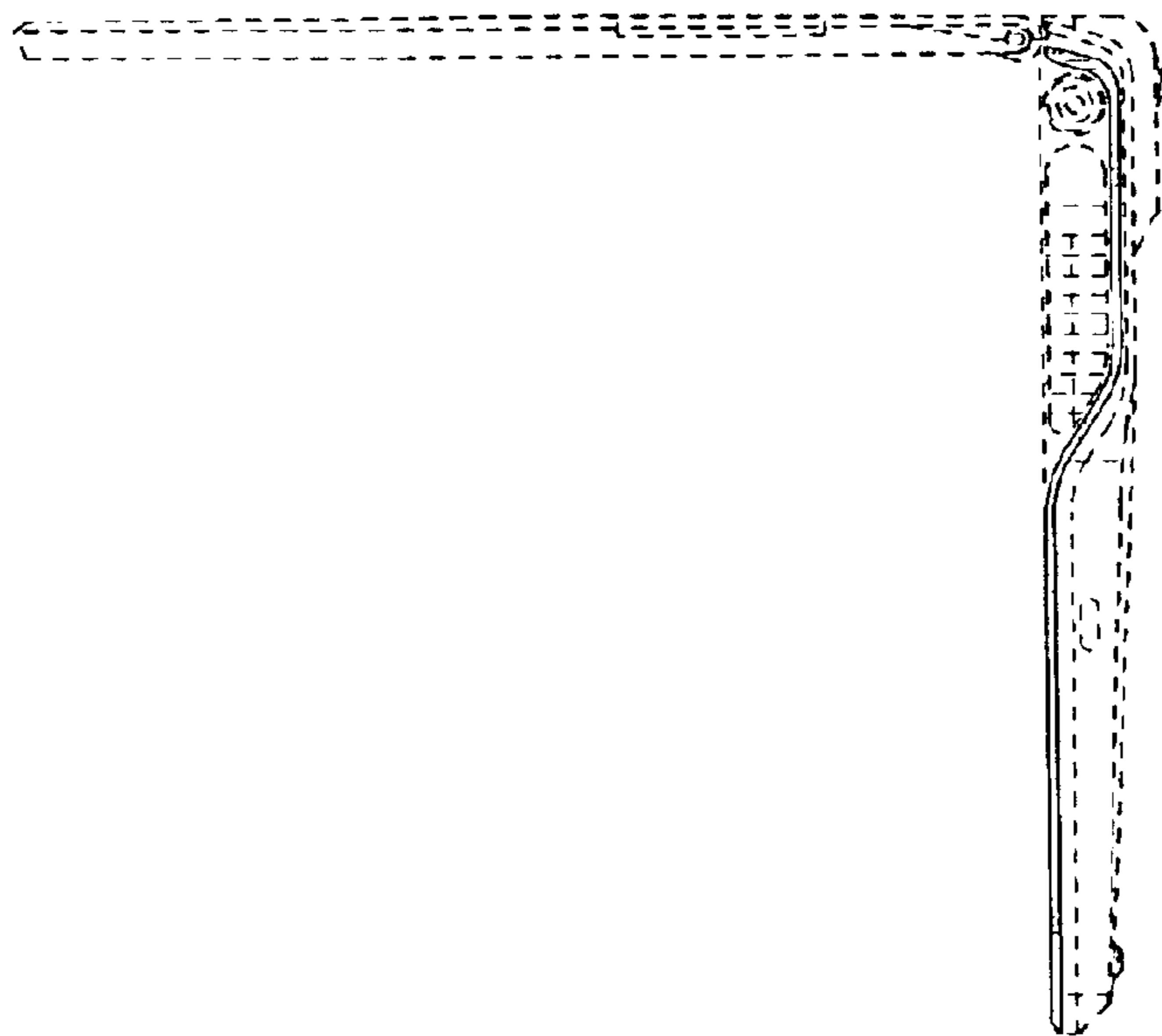


FIG. 4

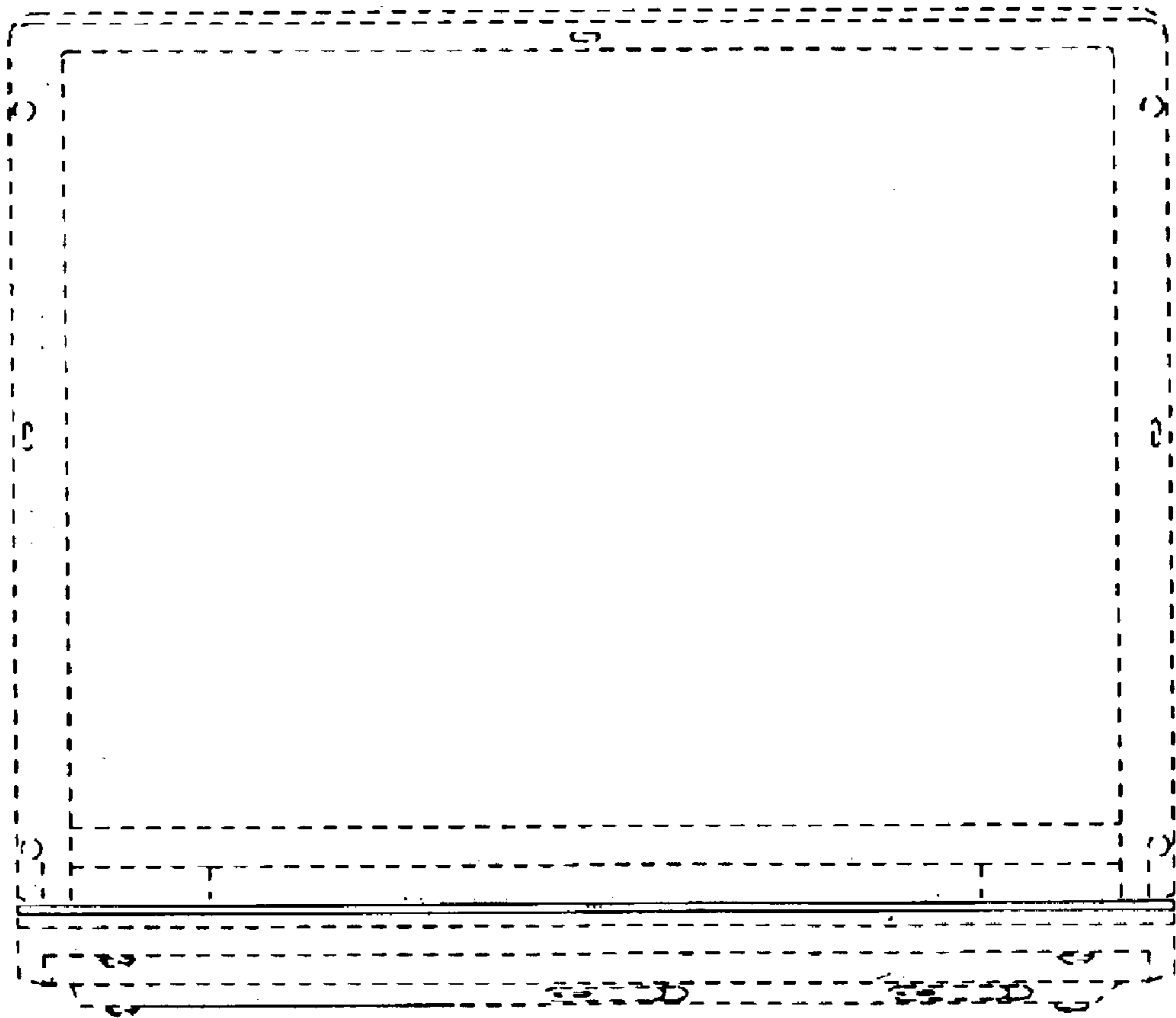


FIG. 5