



US00D490081S

(12) **United States Design Patent**  
**Oura et al.**

(10) **Patent No.:** **US D490,081 S**

(45) **Date of Patent:** **\*\* May 18, 2004**

(54) **MEMORY CONTROL EQUIPMENT FOR COMPUTERS**

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(73) Assignee: **NEC Corporation**, Tokyo (JP)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/178,893**

(22) Filed: **Apr. 2, 2003**

(30) **Foreign Application Priority Data**

Oct. 3, 2002 (JP) ..... 2002-027099

(51) **LOC (7) Cl.** ..... **14-02**

(52) **U.S. Cl.** ..... **D14/434; D14/358**

(58) **Field of Search** ..... **D14/432-438, D14/357, 358, 125; D6/632; 709/217, 219, 227, 206; 370/465; 707/4**

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(57) **CLAIM**

The ornamental design for a memory control equipment for computers, as shown.

**DESCRIPTION**

FIG. 1 is a perspective view of the top, front and right side of a memory control equipment for computers showing our new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a left side elevational view thereof;

FIG. 5 is a right side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

**1 Claim, 3 Drawing Sheets**

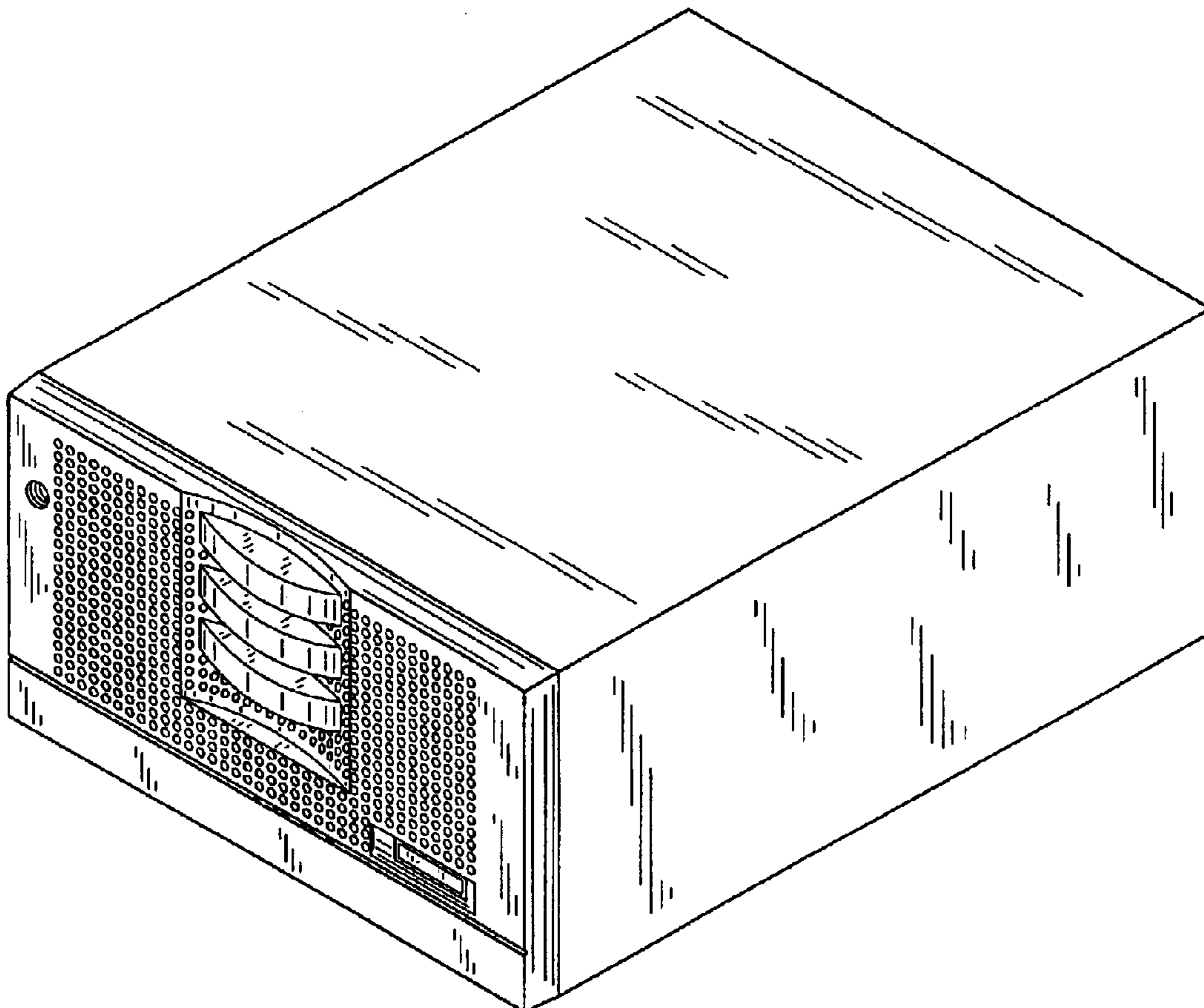


FIG. 1

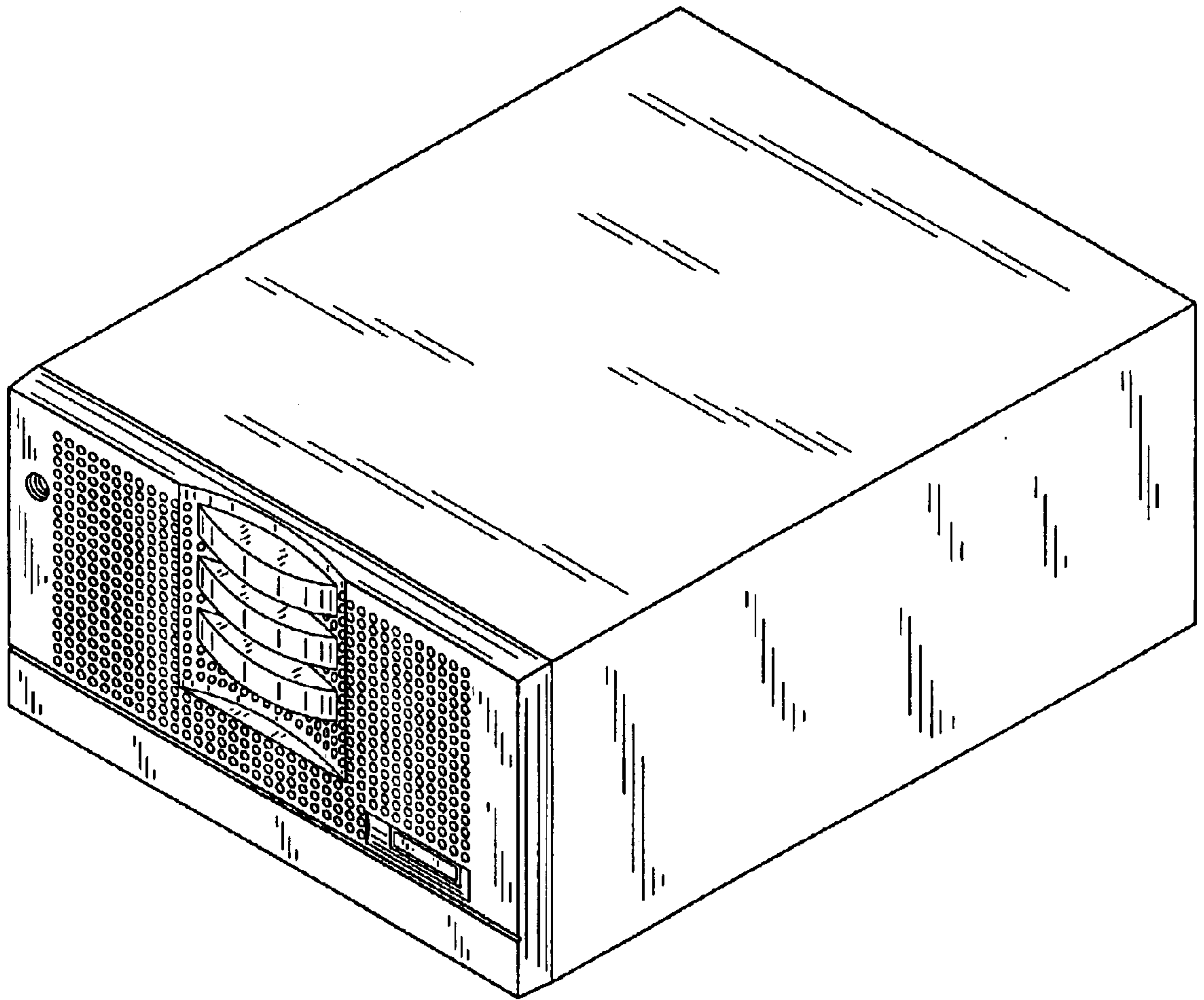


FIG. 2

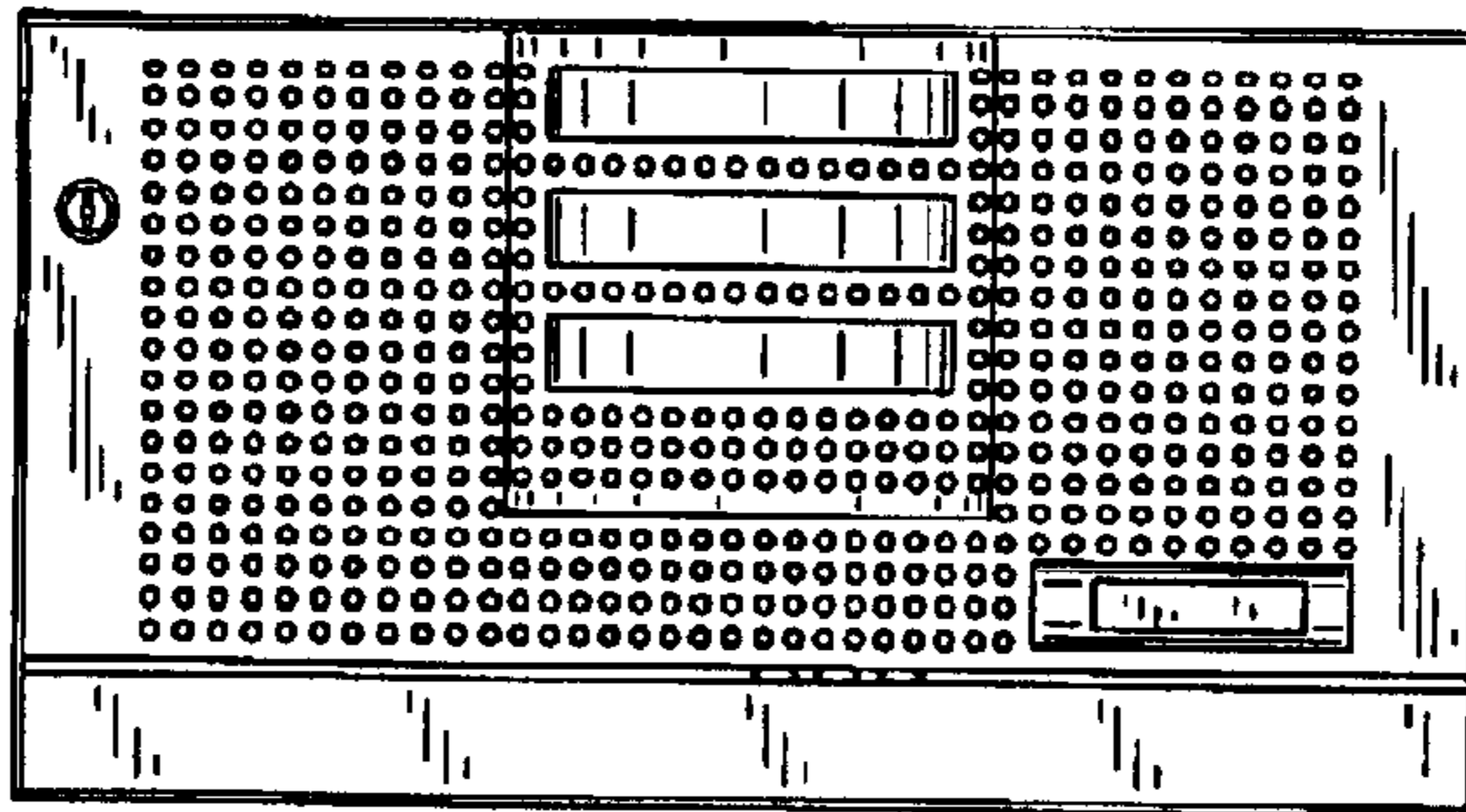


FIG. 3

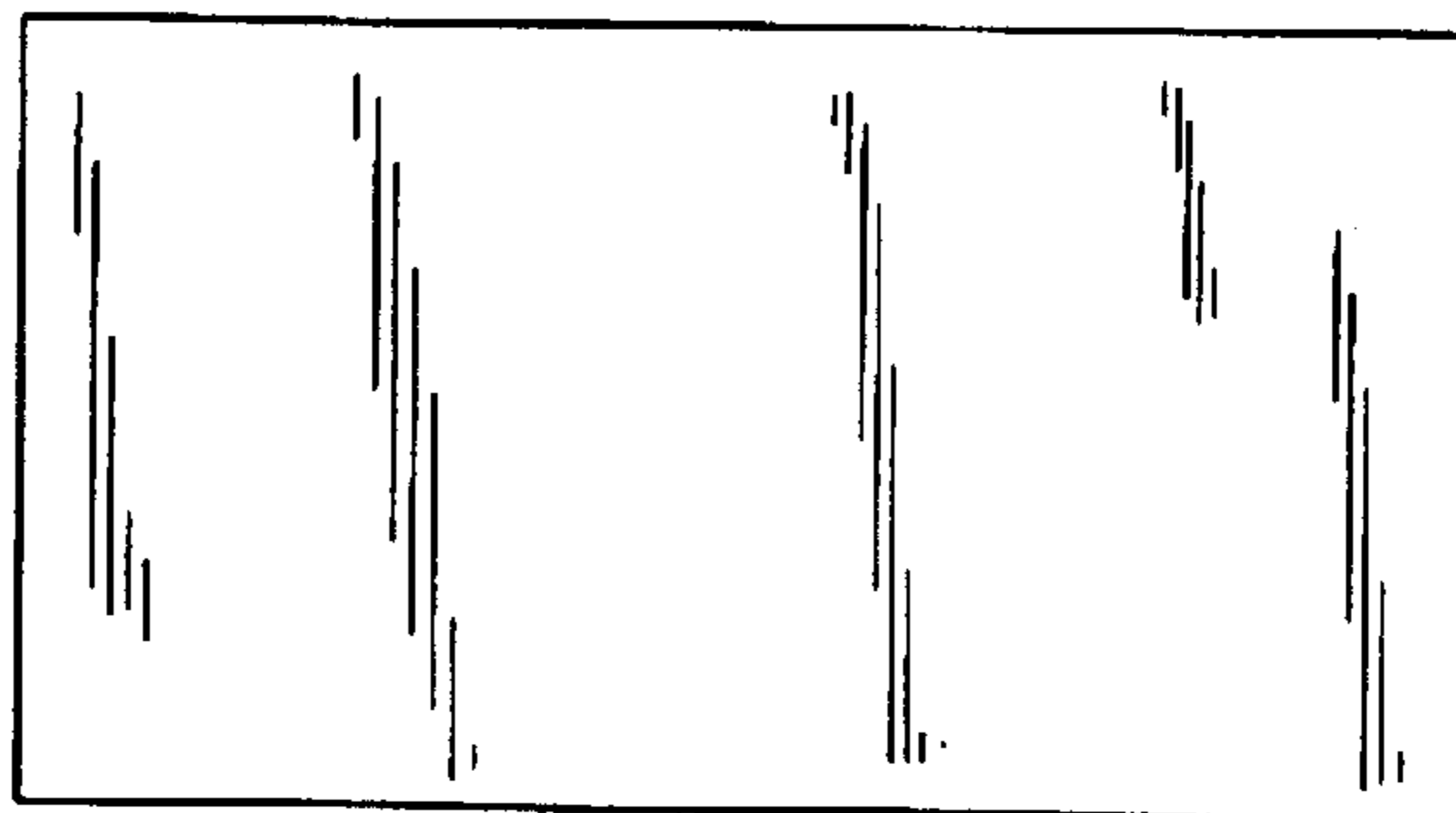


FIG. 4

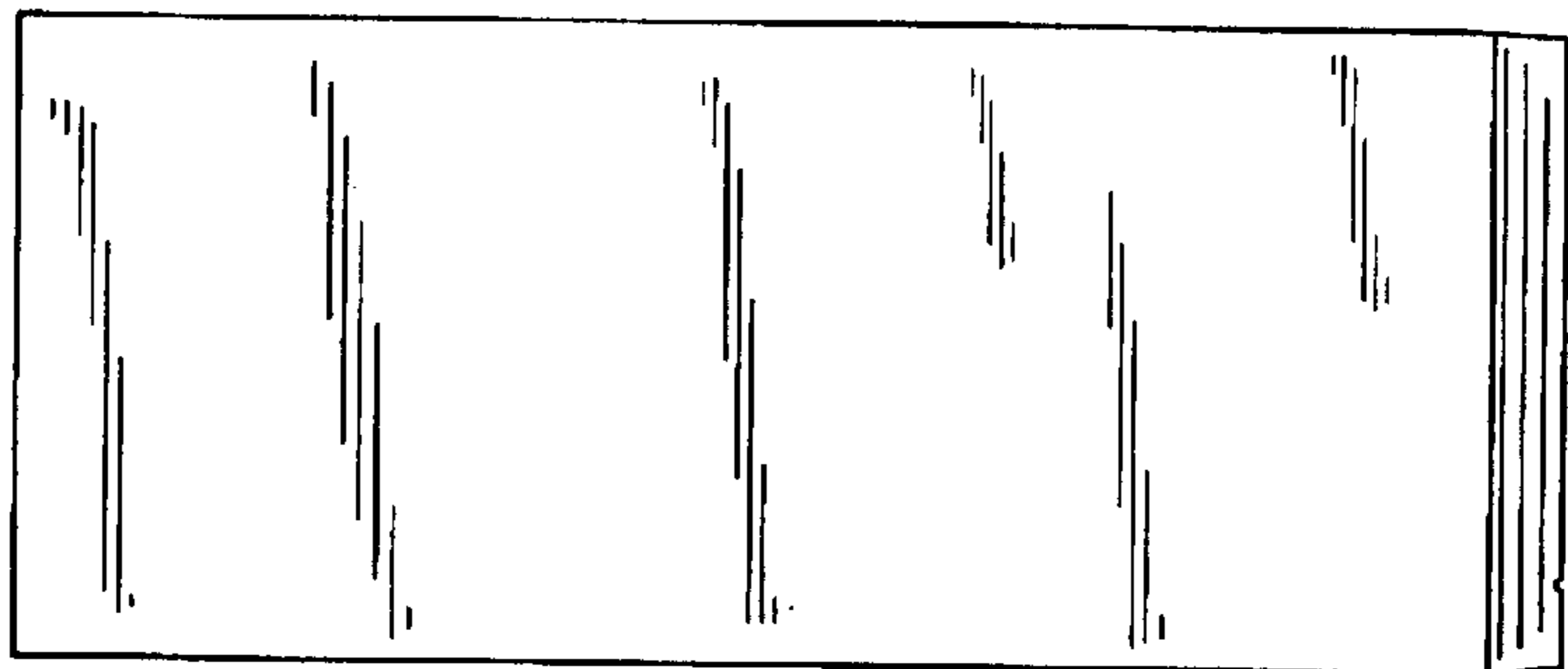


FIG. 5

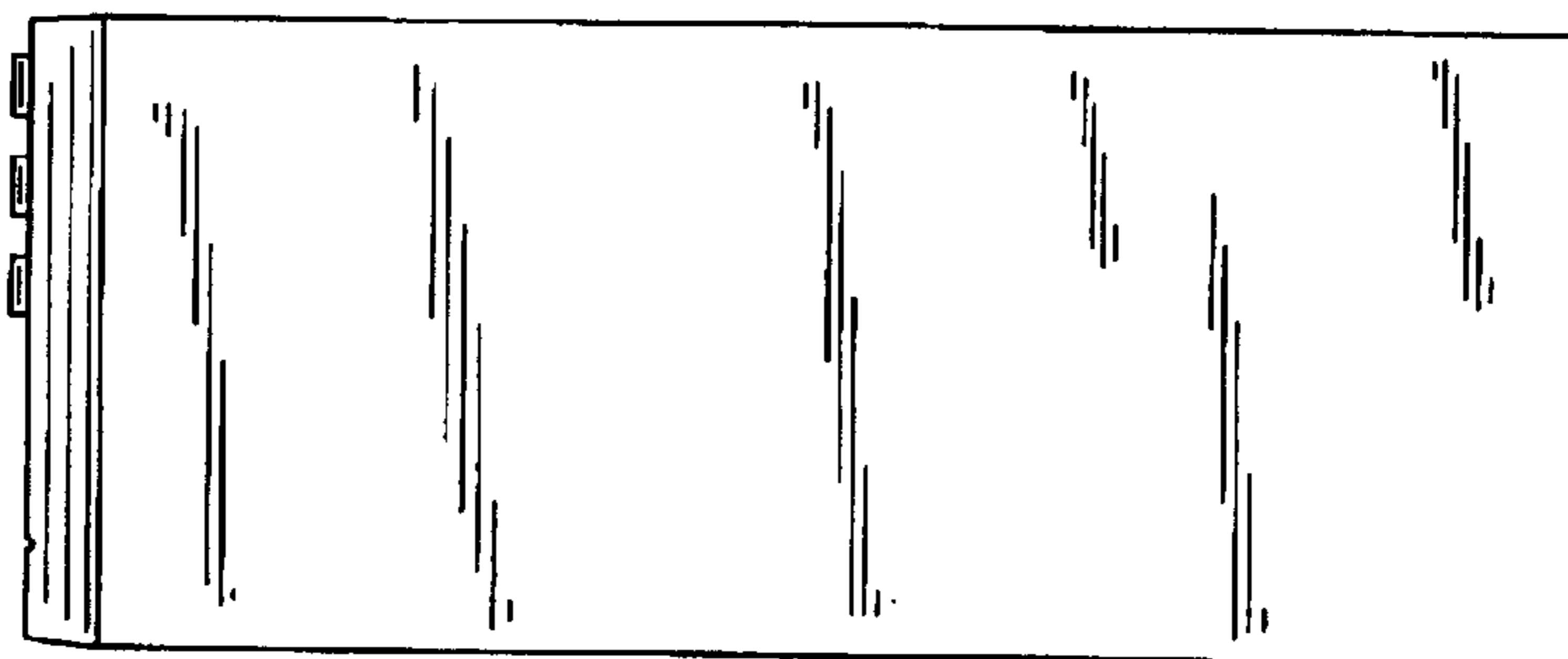


FIG. 6

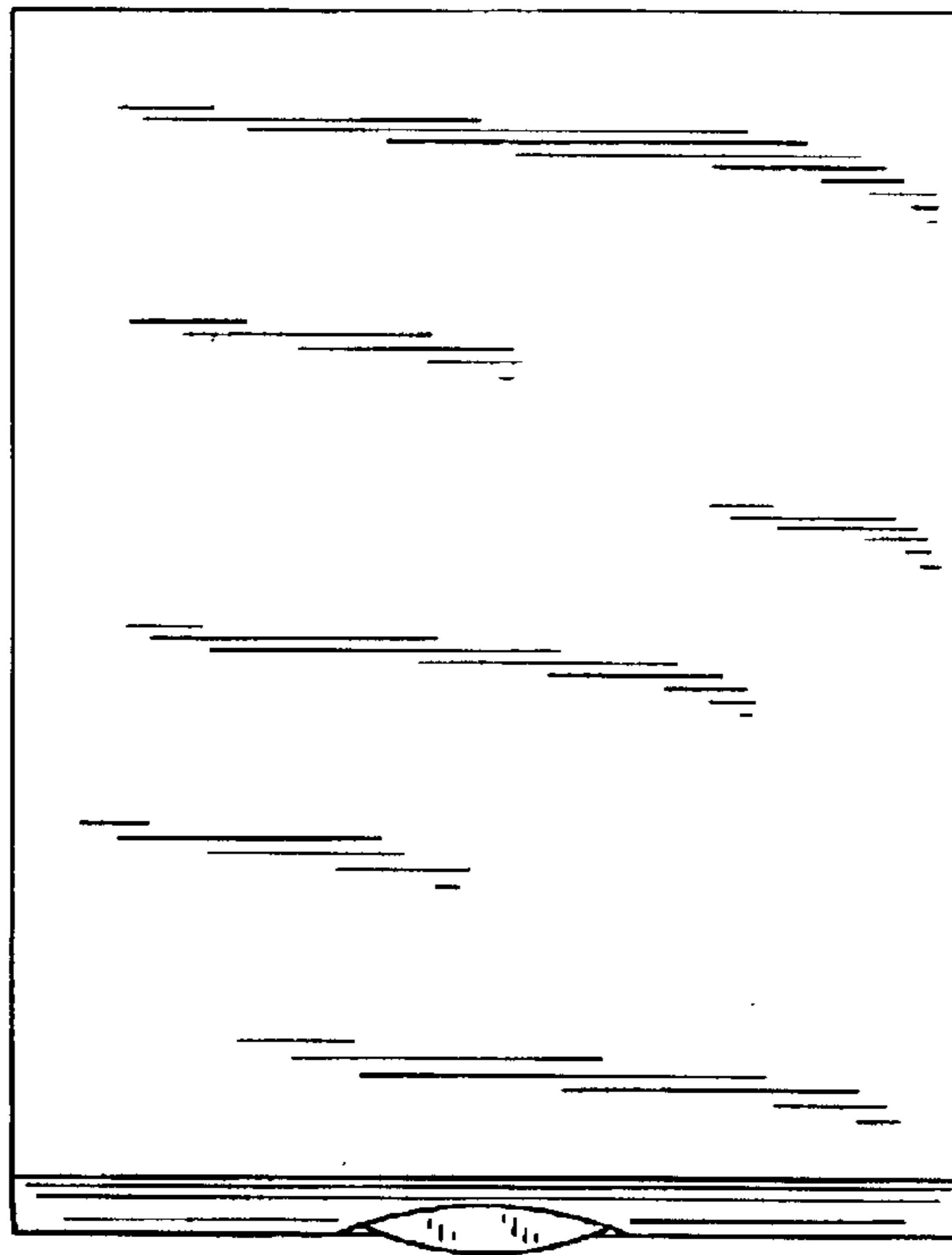


FIG. 7

