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(12) **United States Design Patent**  
**Nishi**

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(54) **SIGNAL PROCESSOR FOR COMPUTER**

**FOREIGN PATENT DOCUMENTS**

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(73) Assignee: **Kabushiki Kaisha Toshiba**, Tokyo (JP)  
(\*\*) Term: **14 Years**

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(21) Appl. No.: **29/164,465**  
(22) Filed: **Jul. 26, 2002**

\* cited by examiner

(30) **Foreign Application Priority Data**

Feb. 27, 2002 (JP) ..... 2002-004980

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(51) **LOC (7) Cl.** ..... **14-02**  
(52) **U.S. Cl.** ..... **D14/358**  
(58) **Field of Search** ..... D14/301, 302,  
D14/349, 356, 357, 358, 359; D13/162,  
184, 199; 360/97.01, 97.04, 98.01; 361/690-696;  
369/34.01, 36.01; 709/202, 217; 340/825.64

(57) **CLAIM**

The ornamental design for a signal processor for computer, as shown and described.

(56) **References Cited**

**DESCRIPTION**

**U.S. PATENT DOCUMENTS**

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FIG. 1 is a top, front and right side perspective view of a signal processor for computer showing my new design, FIG. 2 is a front elevational view thereof, FIG. 3 is a rear elevational view thereof, FIG. 4 is a right side elevational view thereof, FIG. 5 is a top plan view thereof, FIG. 6 is a bottom plan view thereof; and, FIG. 7 is a left side elevational view thereof.

**1 Claim, 3 Drawing Sheets**

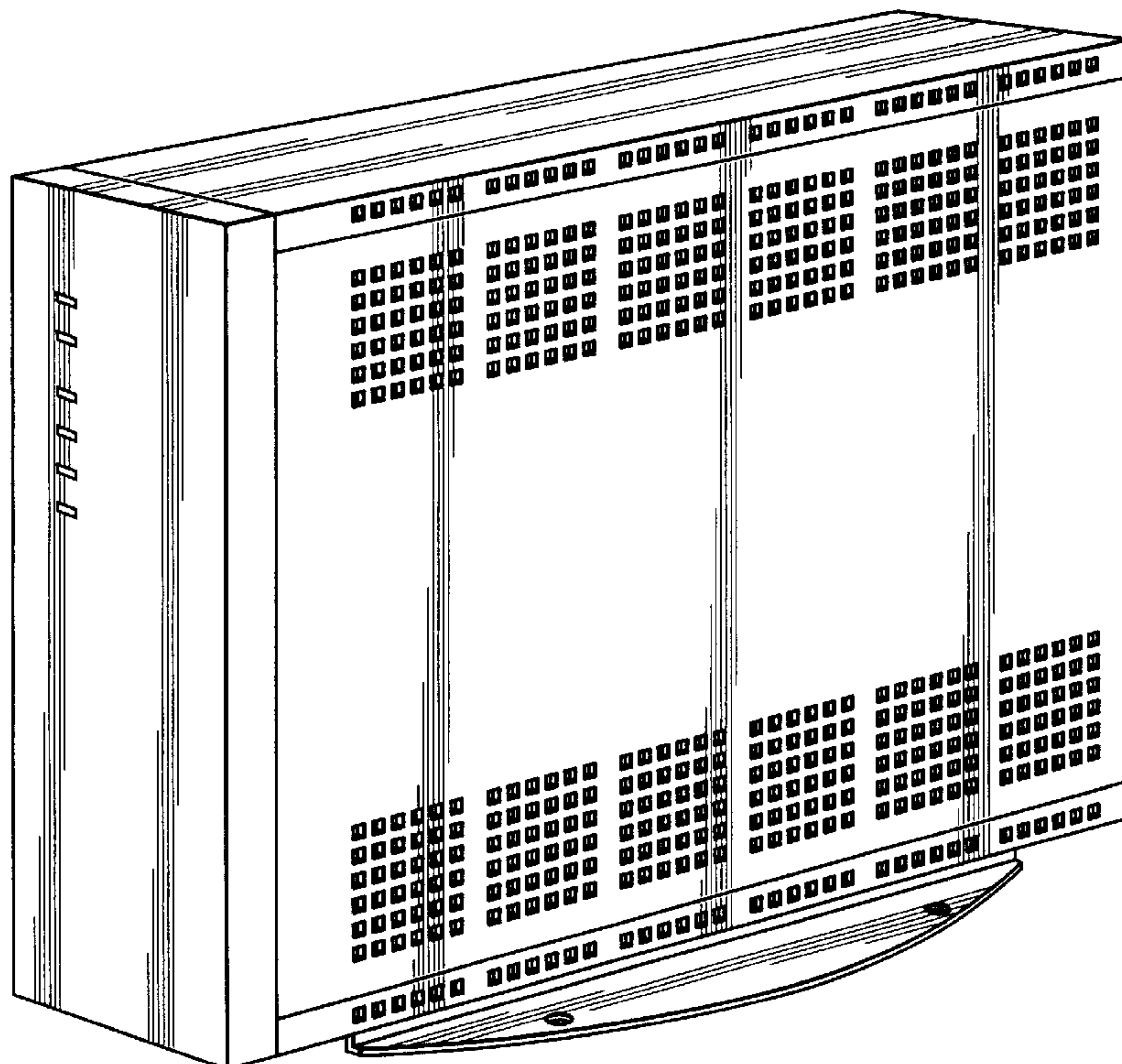


FIG. 1

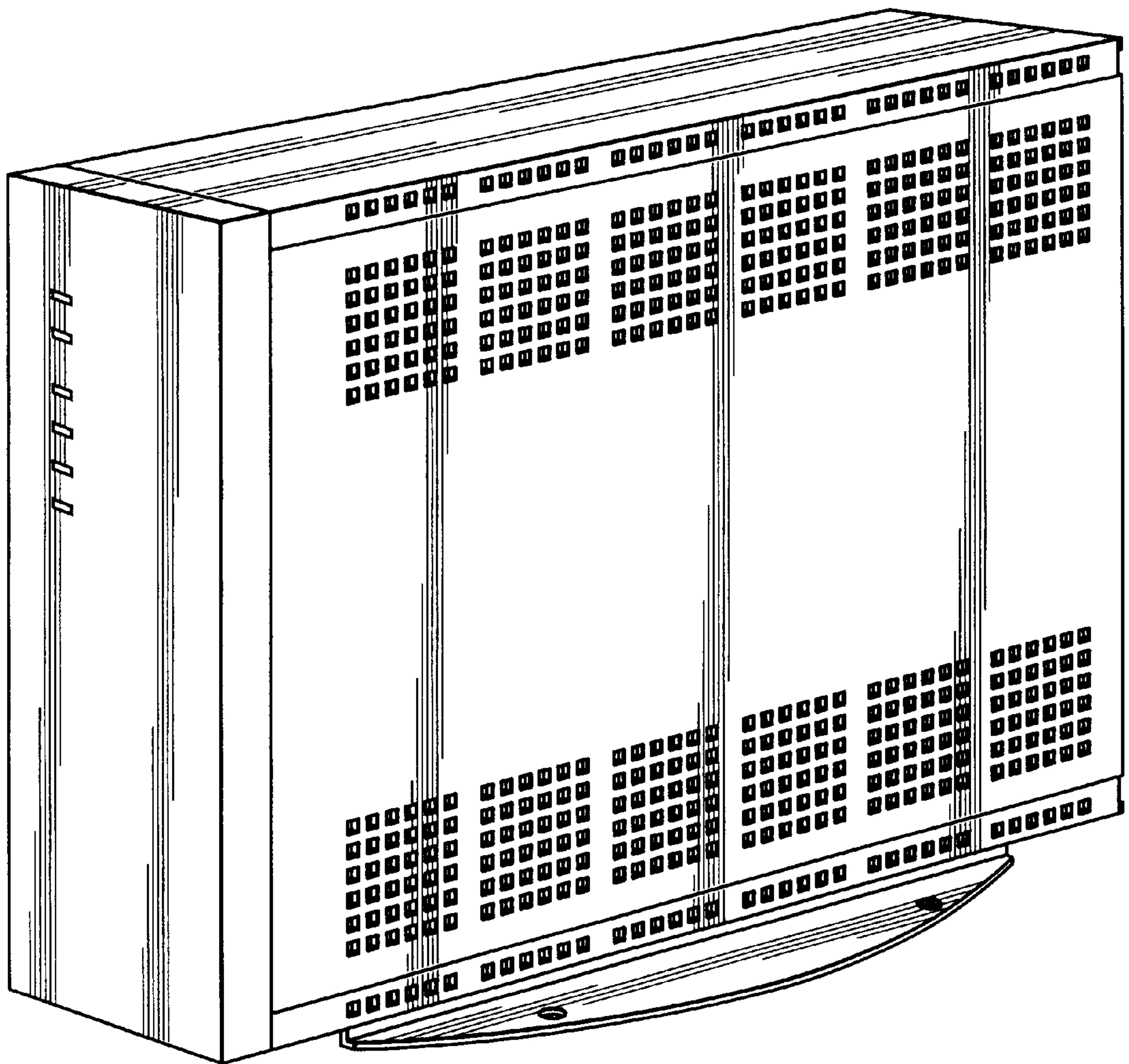


FIG. 2

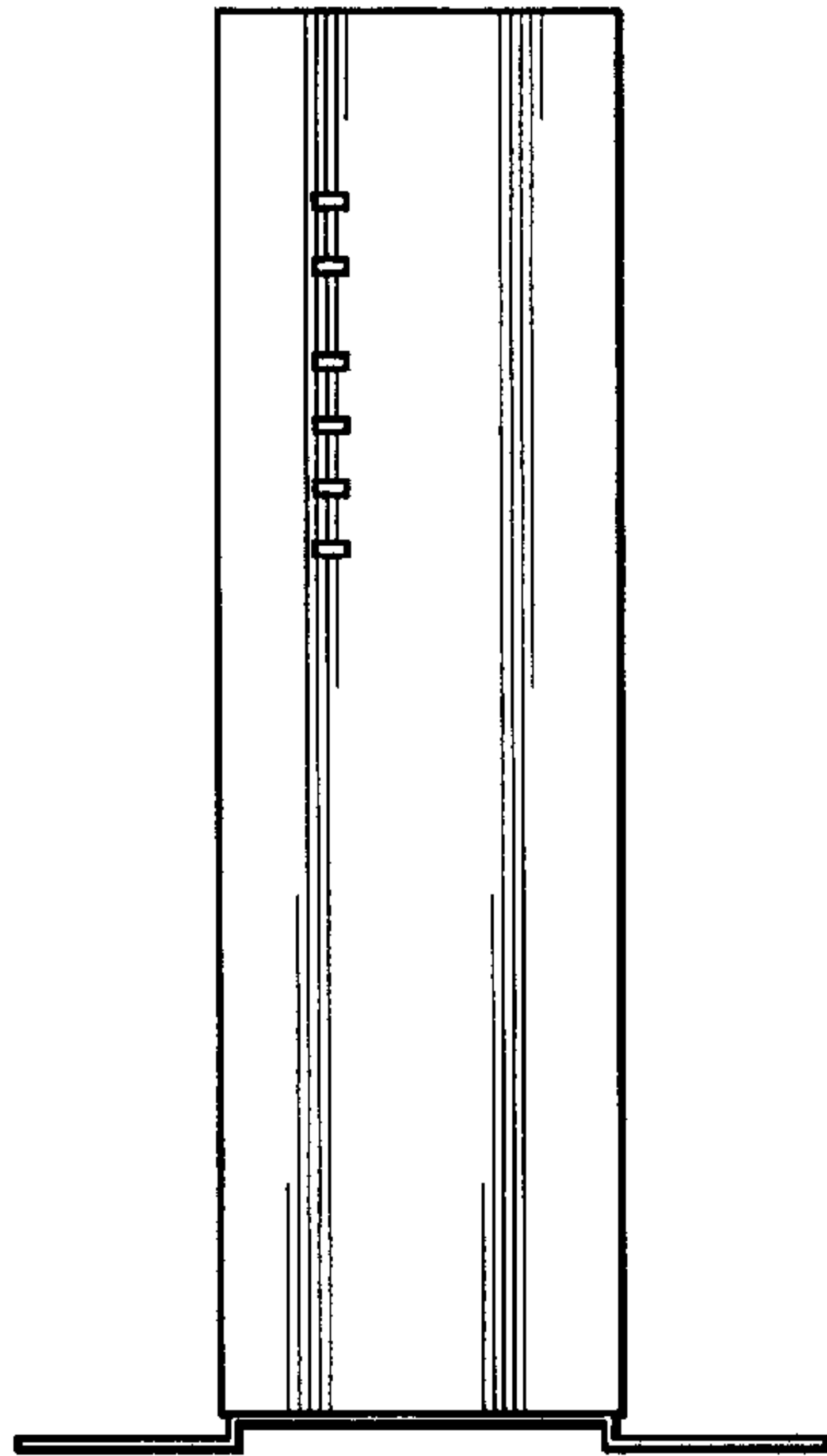


FIG. 3

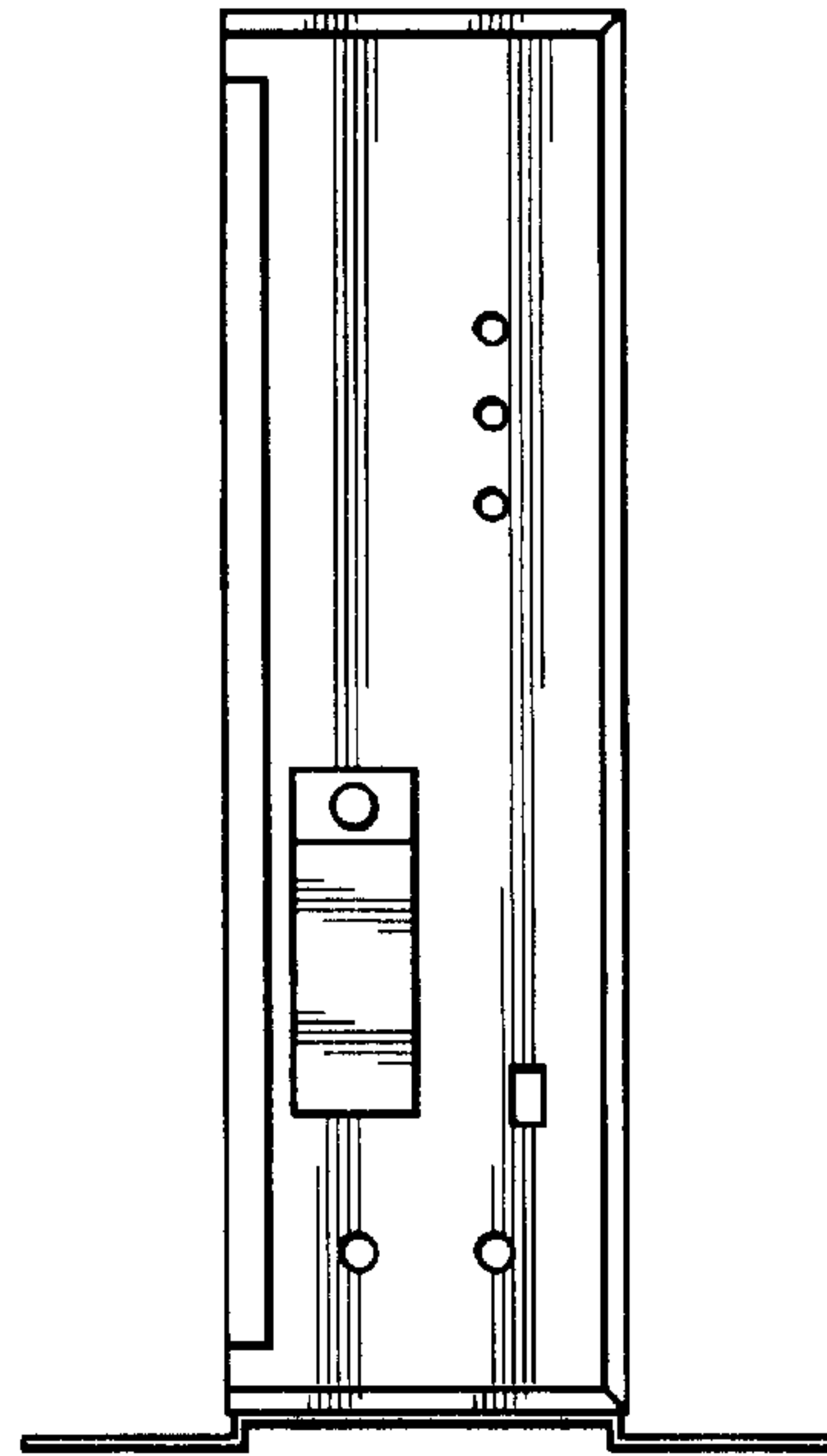


FIG. 4

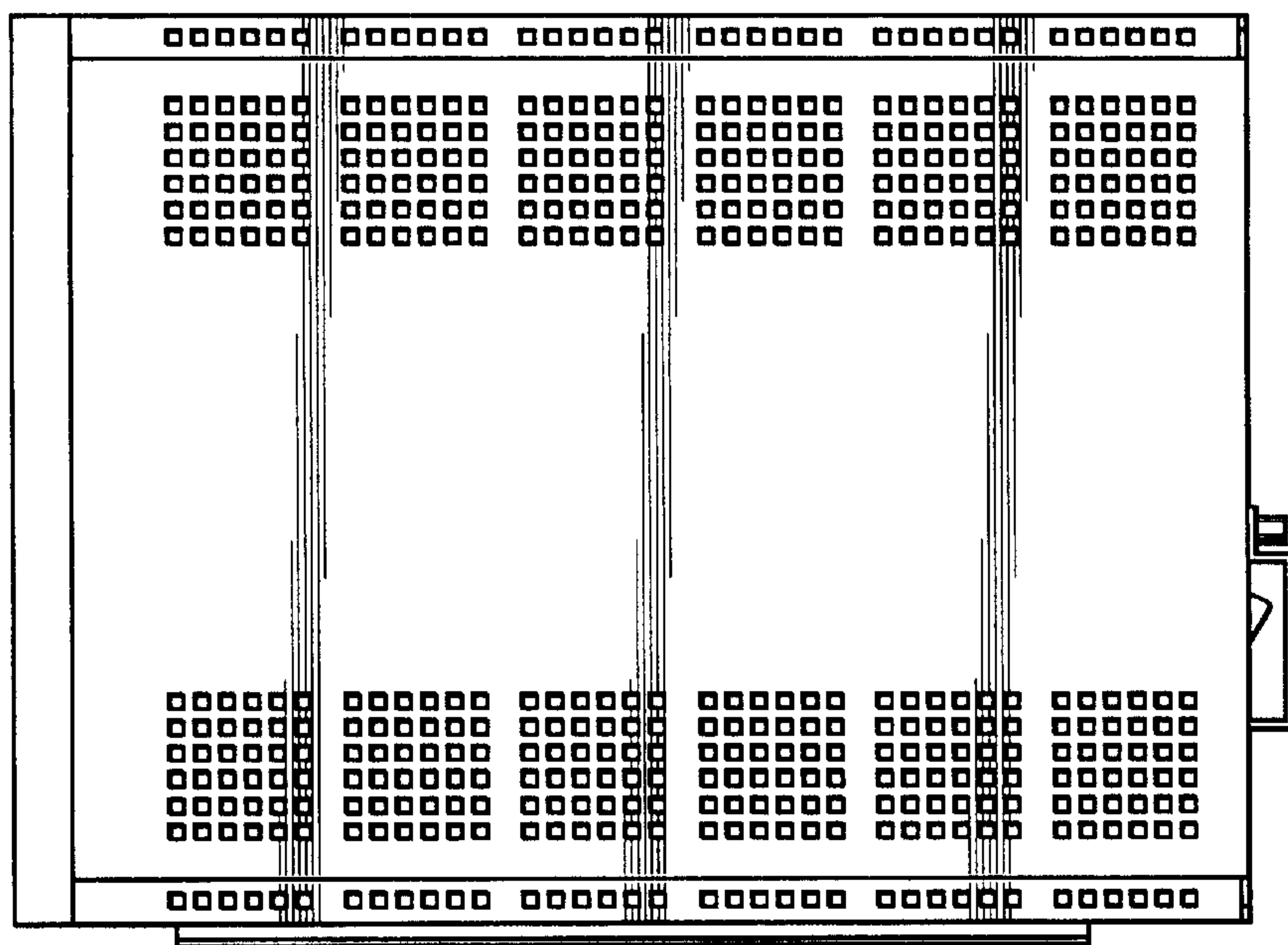


FIG. 5

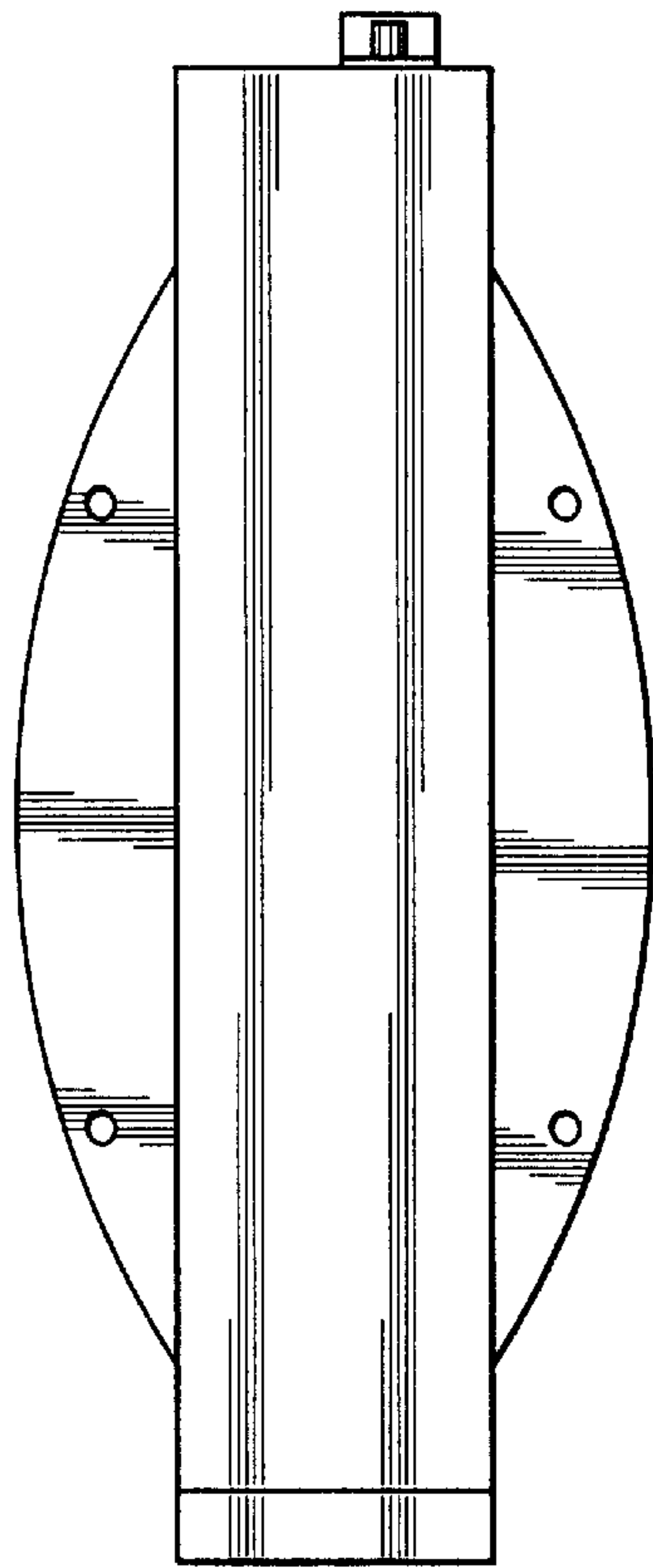


FIG. 6

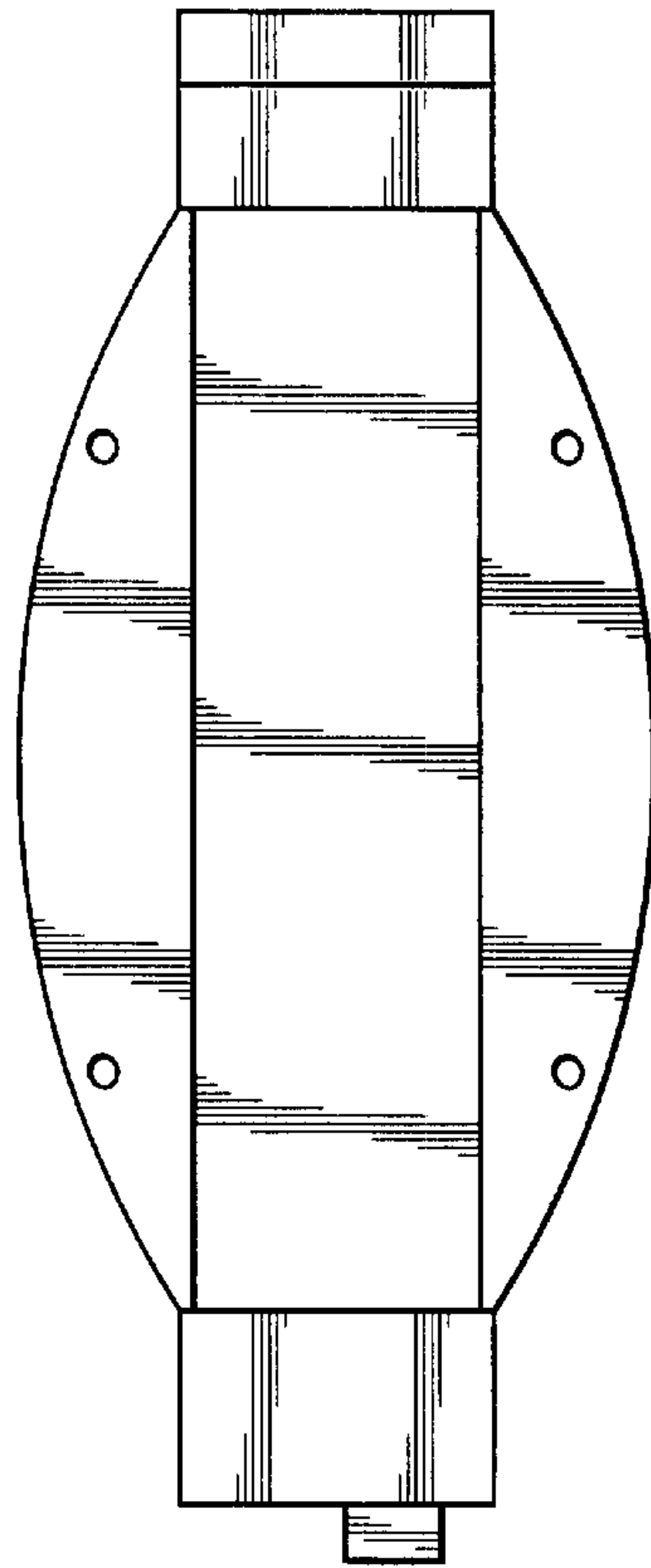


FIG. 7

