



US00D479841S

(12) **United States Design Patent** (10) **Patent No.:** **US D479,841 S**
Tago et al. (45) **Date of Patent:** **** Sep. 23, 2003**

(54) **ELECTRONIC COMPUTER**

JP	1131716	1/2002
TW	458652	10/2001

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(73) Assignee: **Kabushiki Kaisha Toshiba**, Tokyo (JP)

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(**) Term: **14 Years**

(21) Appl. No.: **29/167,871**

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(22) Filed: **Sep. 24, 2002**

(30) **Foreign Application Priority Data**

Primary Examiner—Freda Nunn

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(51) **LOC (7) Cl.** **14-02**

(57) **CLAIM**

(52) **U.S. Cl.** **D14/323**

The ornamental design for an electronic computer, as shown and described.

(58) **Field of Search** D14/315-327;
D18/1, 2, 7; 235/145 A, 145 R; 341/22,
23; 345/104, 156, 168, 169, 173; 361/680-686

DESCRIPTION

(56) **References Cited**

FIG. 1 is a top, front and right side perspective view of an electronic computer showing our new design,
 FIG. 2 is a front elevational view thereof,
 FIG. 3 is a rear elevational view thereof,
 FIG. 4 is a left side elevational view thereof,
 FIG. 5 is a right side elevational view thereof,
 FIG. 6 is a top plan view thereof,
 FIG. 7 is a bottom plan view thereof,
 FIG. 8 is a front elevational view showing the article in a closed position thereof,
 FIG. 9 is a right side elevational view showing the article in the closed position thereof; and,
 FIG. 10 is a top plan view showing the article in the closed position thereof.

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1 Claim, 5 Drawing Sheets

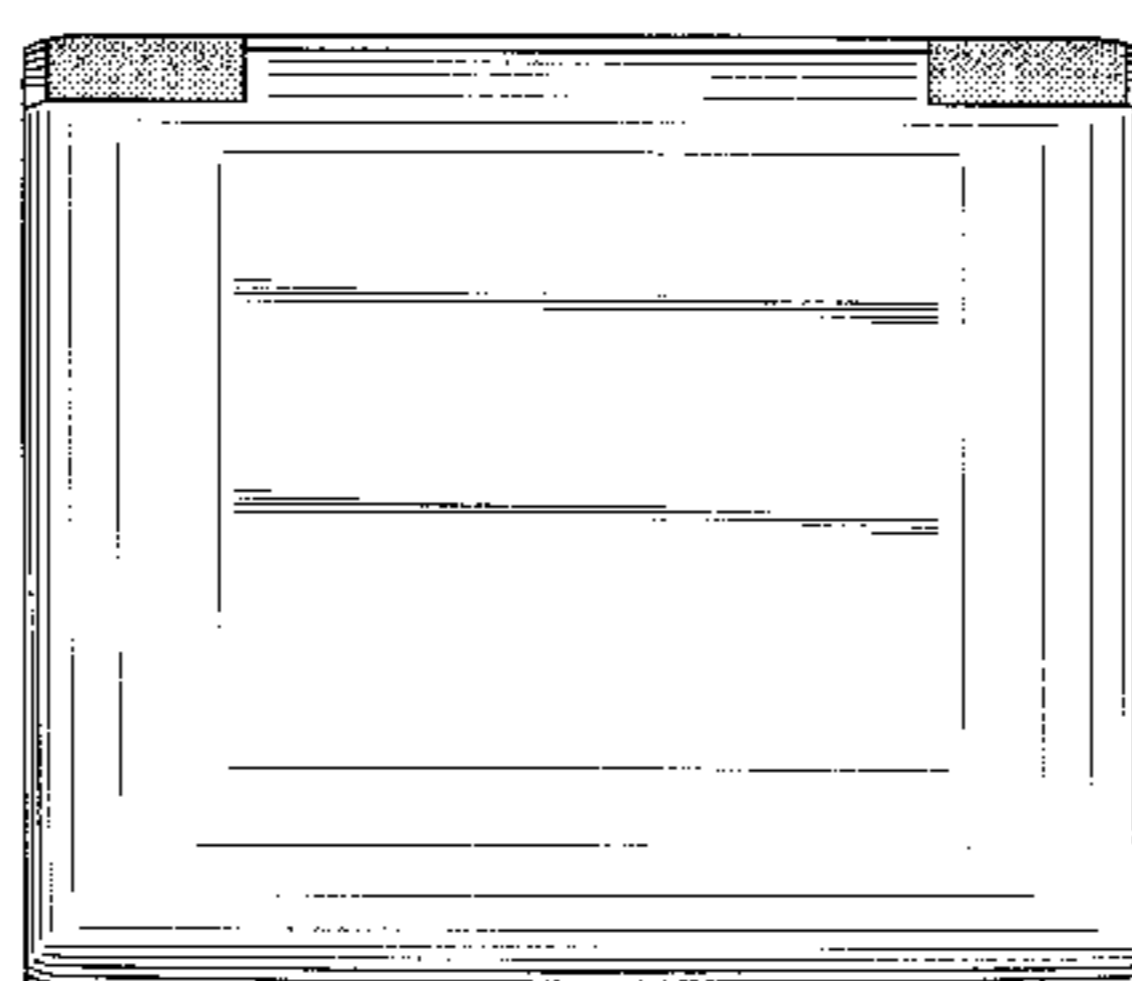
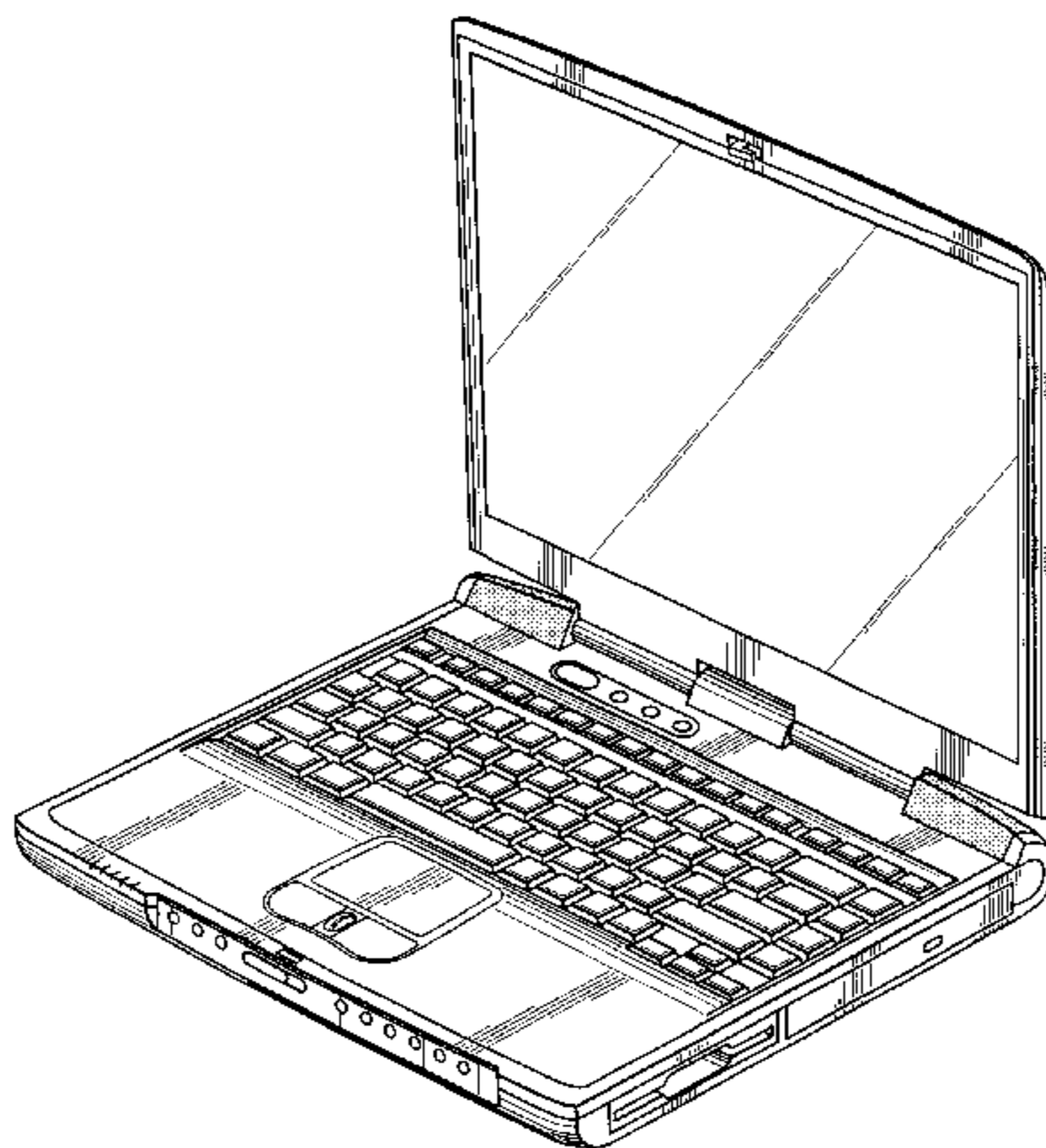


FIG. 1

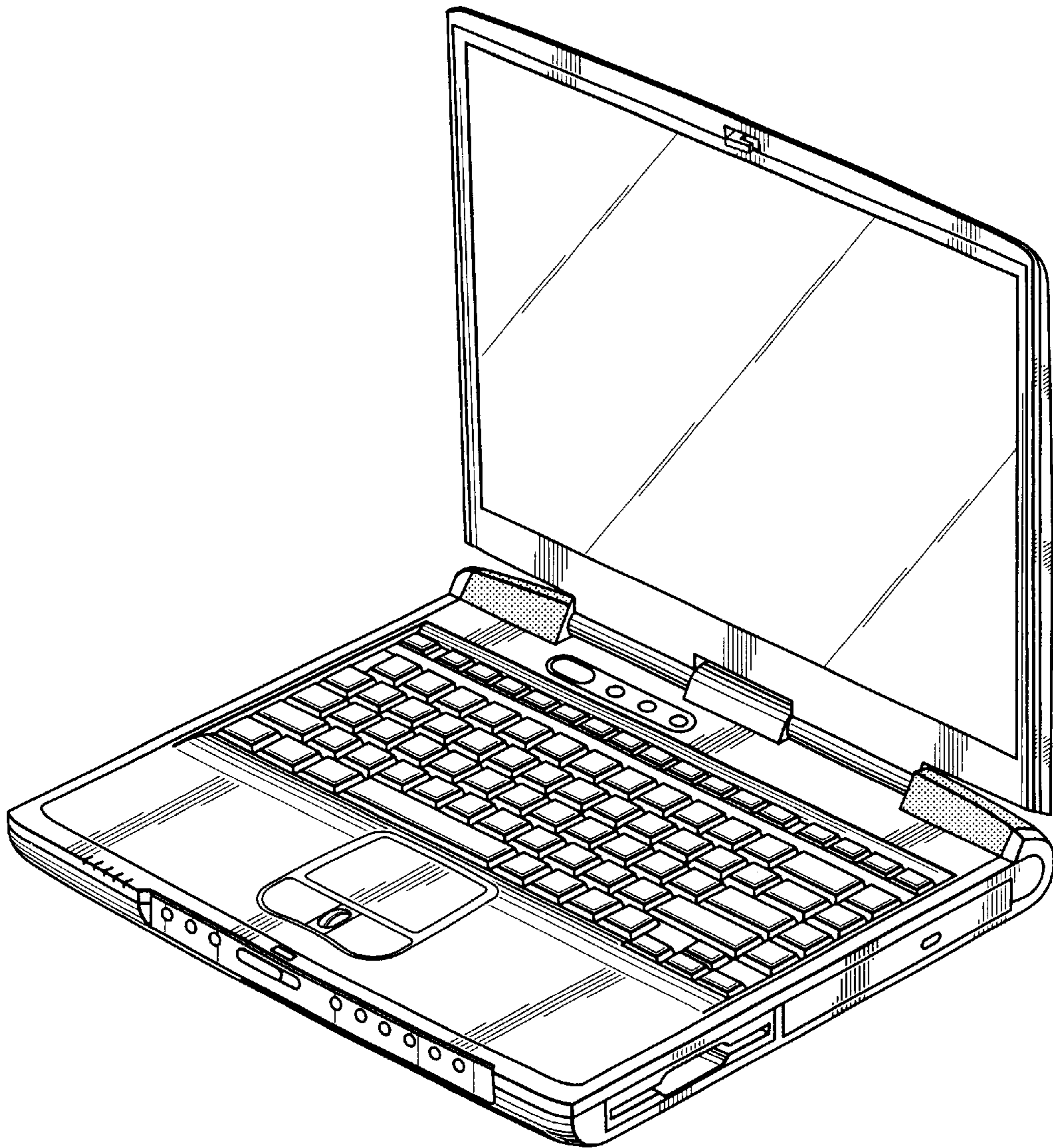


FIG. 2

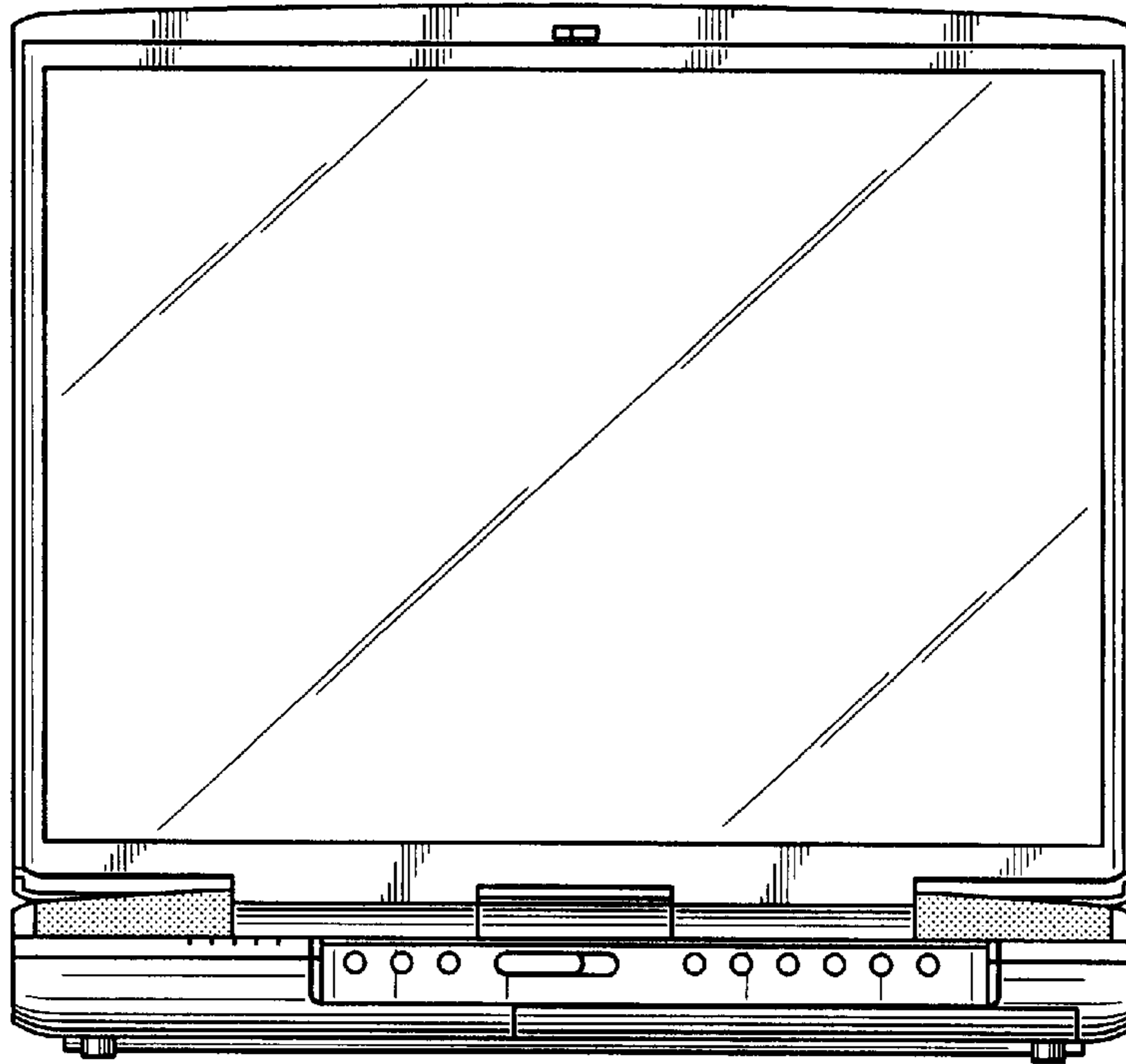


FIG. 3

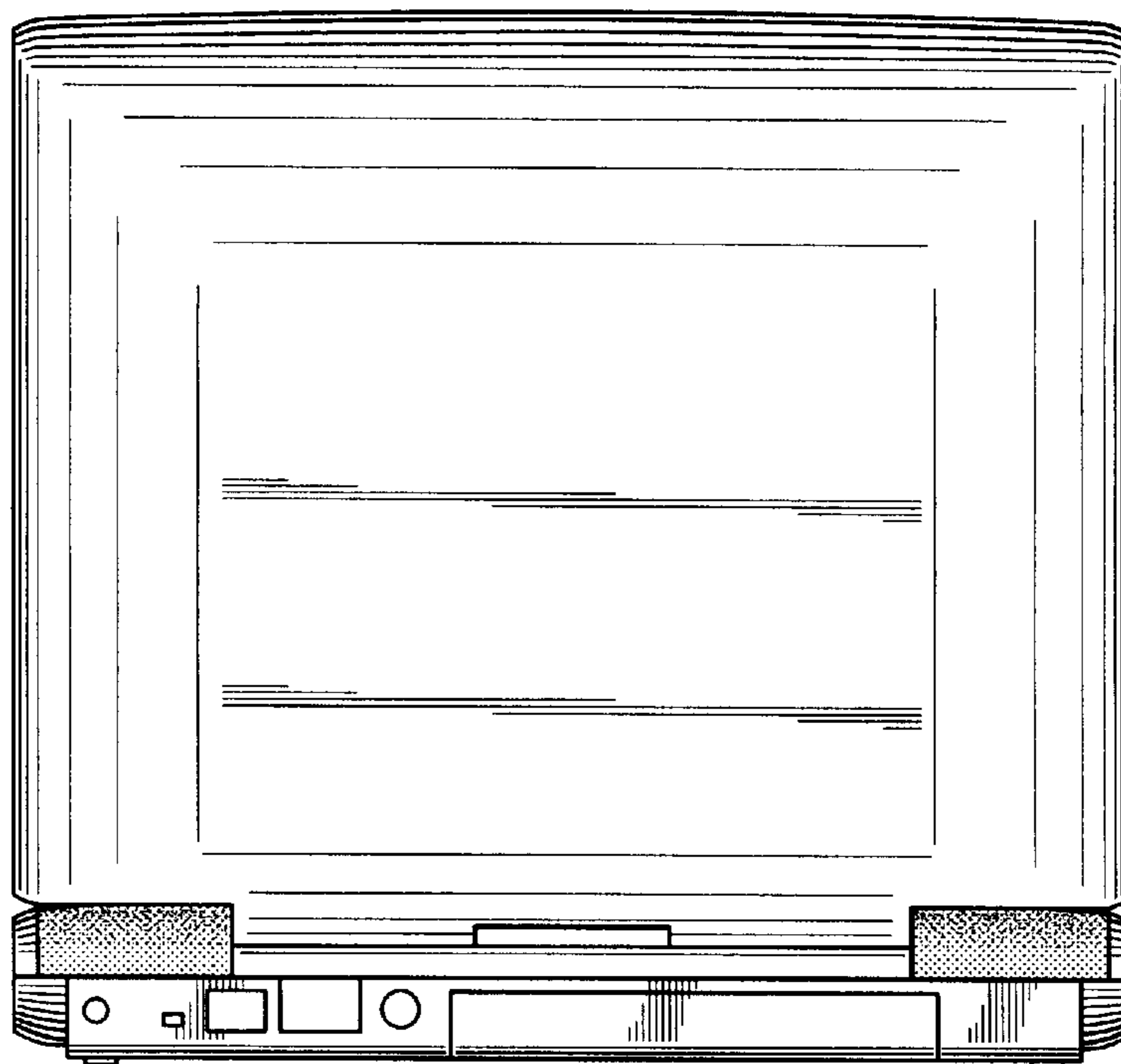


FIG. 4

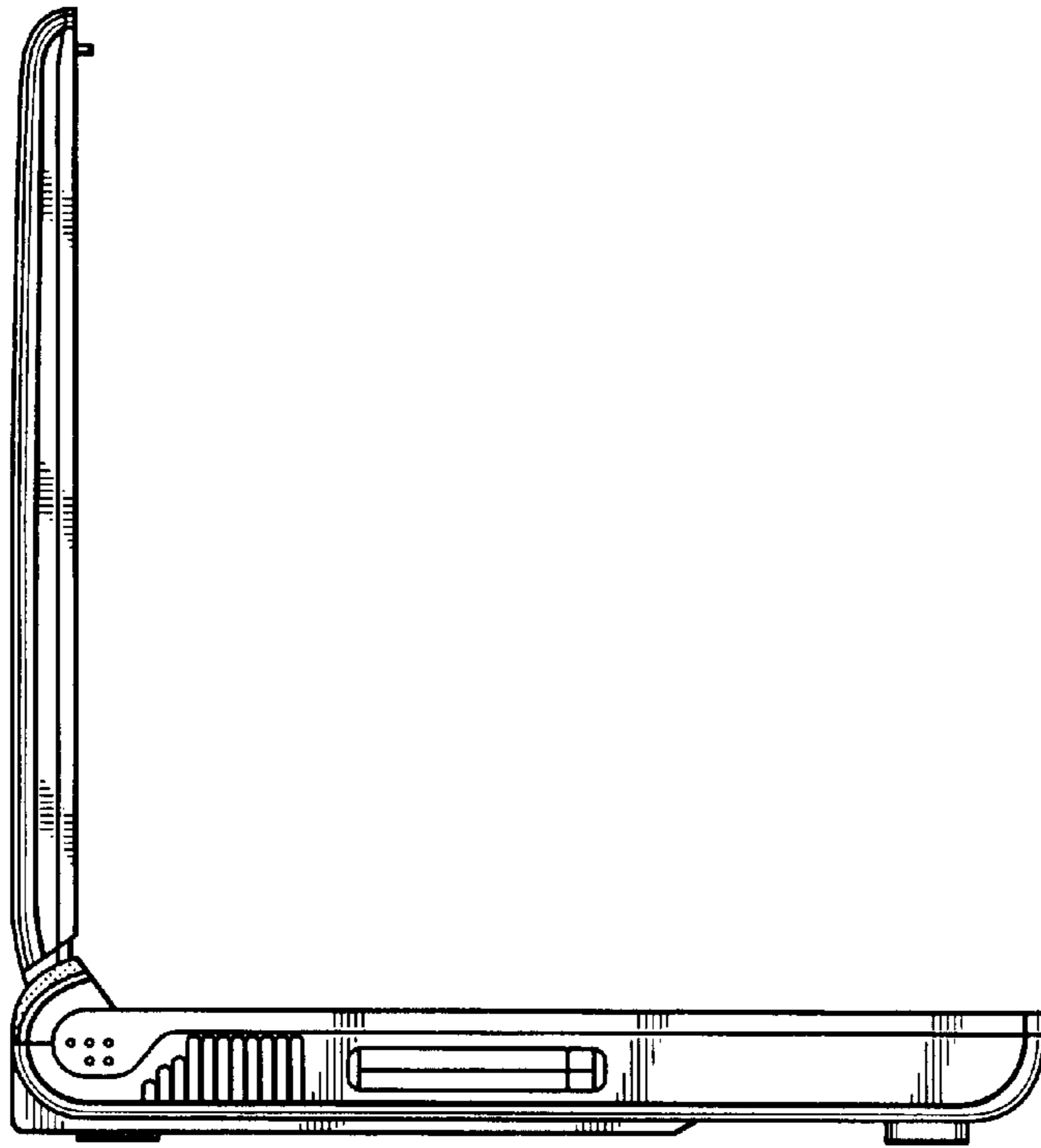


FIG. 5

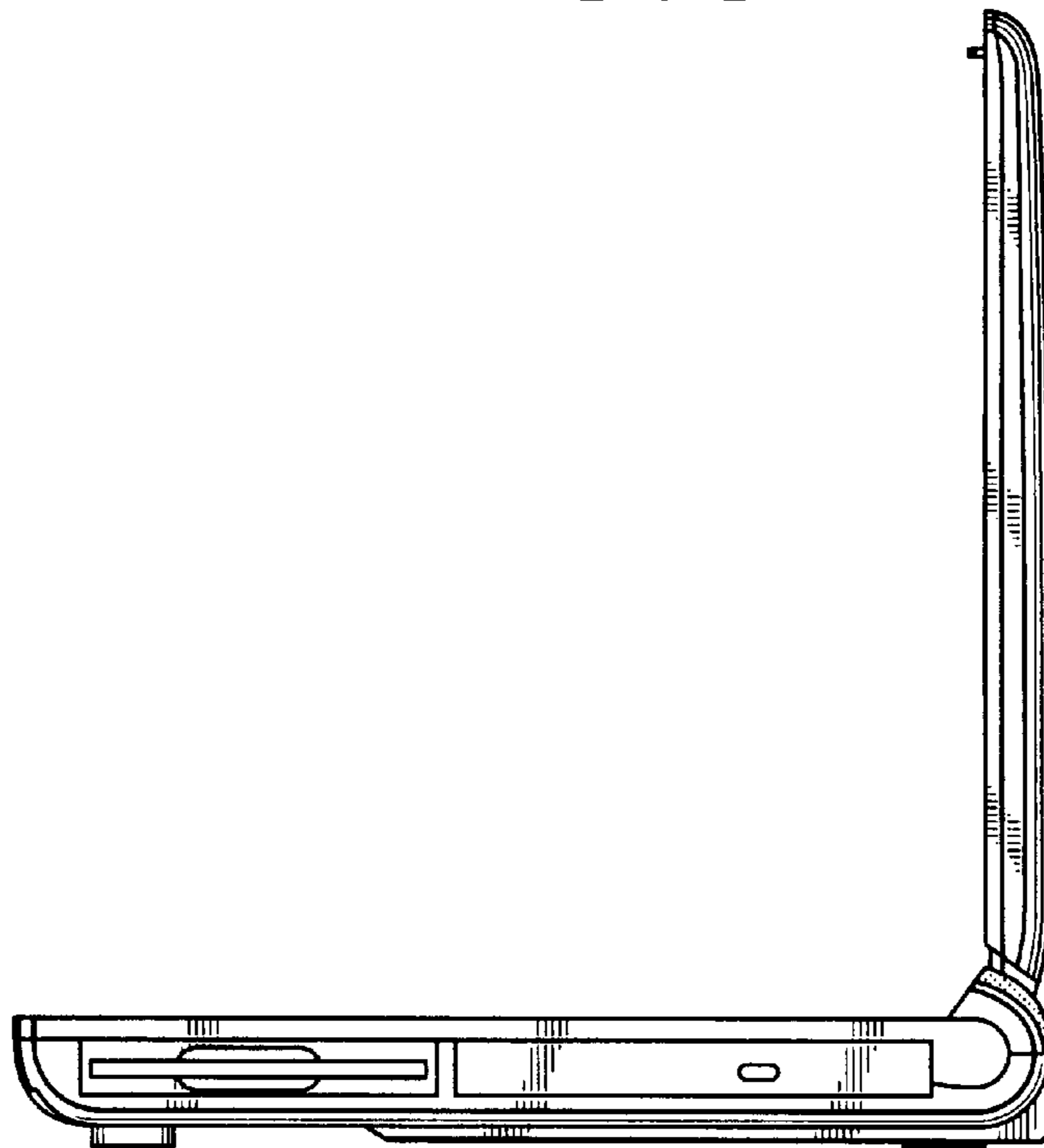


FIG. 6

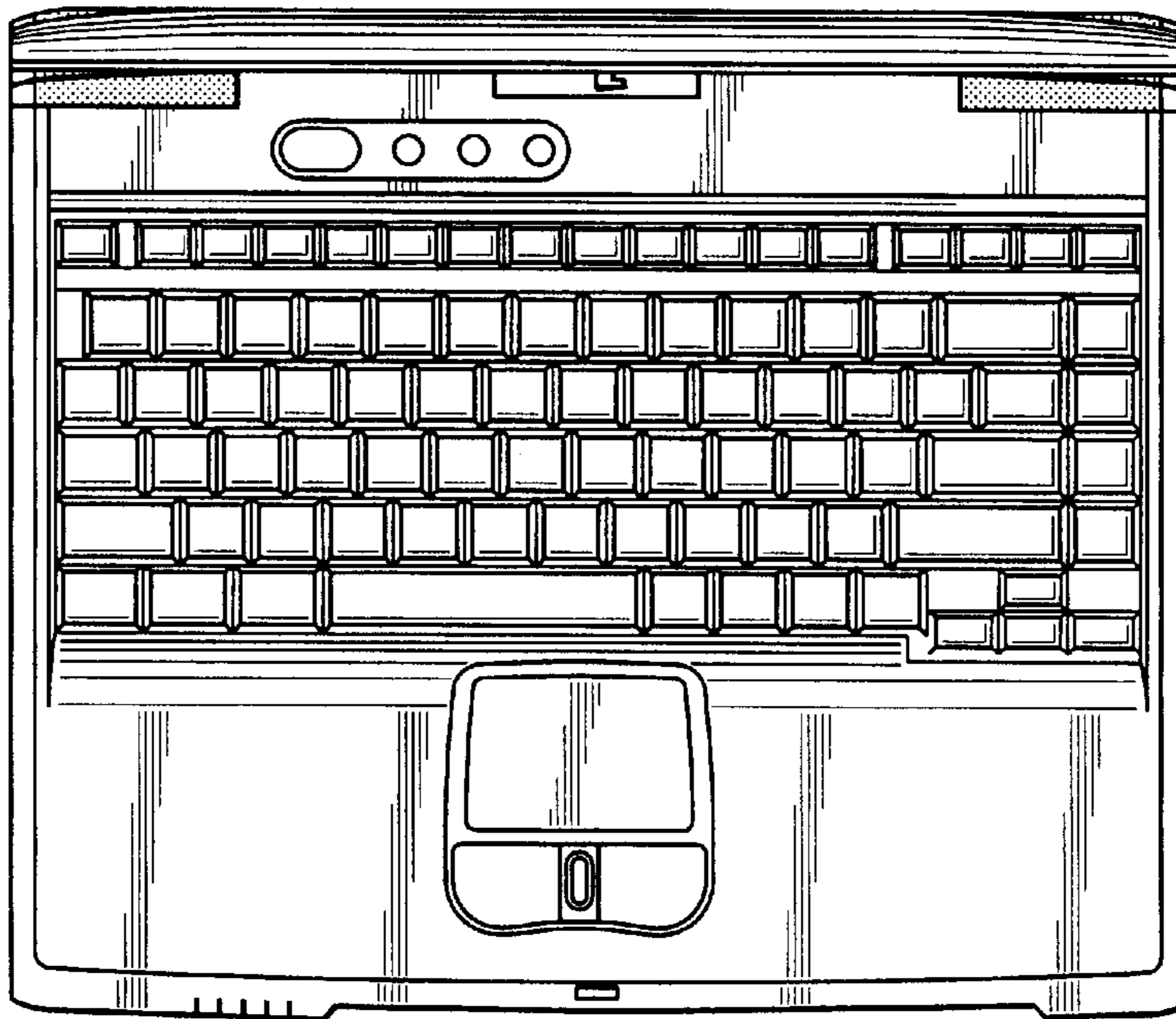


FIG. 7

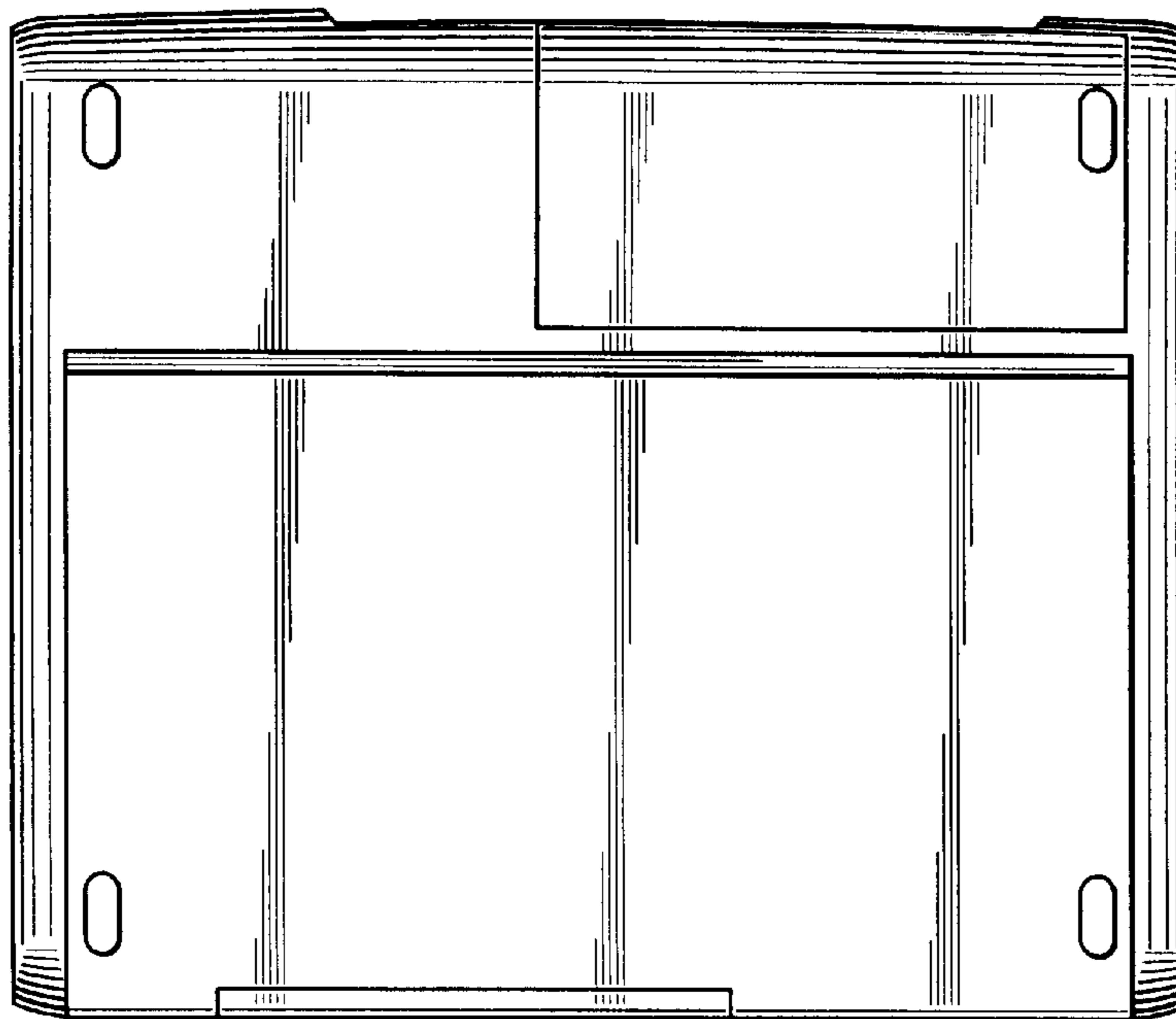


FIG. 8

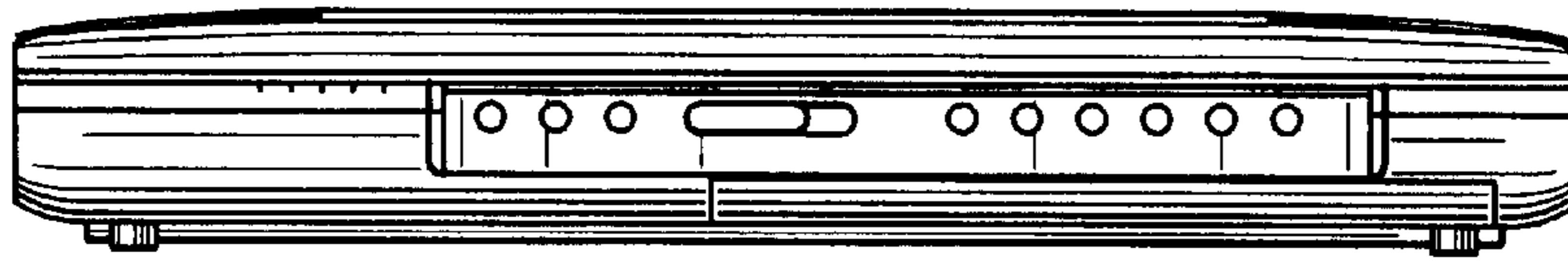


FIG. 9

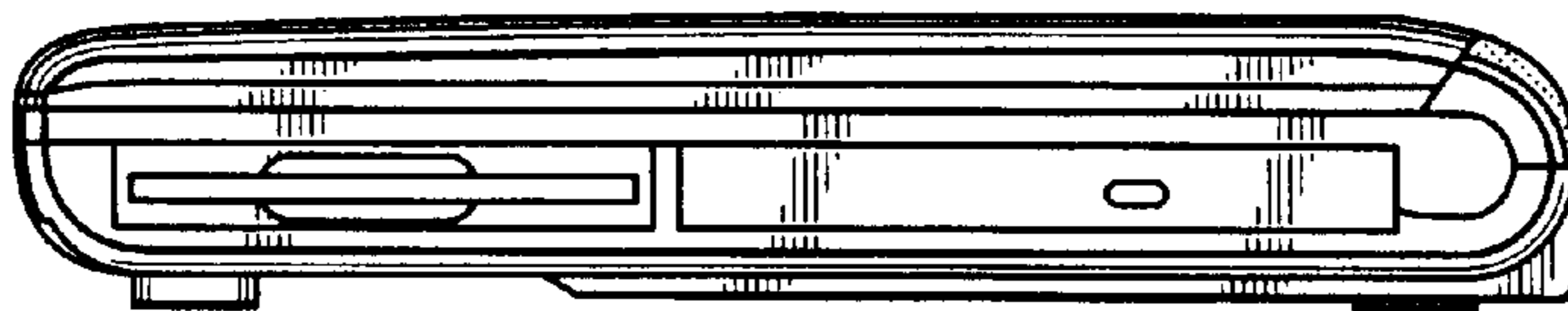


FIG. 10

